

VOLUME 043 MACHINE 3705- -0080232 MODEL M81 SYSTEM 0002904 MODE BOX SHIP 81/12/11

## LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
AA000		BINDER TAB VOL 043	0008496498	344270	.W. 0001862344
DB101		CENTRAL CONTROL	0001852861	344270	.W. 0001862344
DE002		CENTRAL CONTROL	0001852862	344270	.W. 0001862344
DE003		CENTRAL CONTROL	0001852863	344270	.W. 0001862344
DE008		CENTRAL CONTROL	0001852864	344270	.W. 0001862344
DE971		CENTRAL CONTROL	0001852865	344828	.W. 0001862344
DE974		CENTRAL CONTROL	0001852866	344270	.W. 0001862344
DE975		CENTRAL CONTROL	0001852867	344270	.W. 0001862344
DE976		CENTRAL CONTROL	0001852868	344270	.W. 0001862344
DE977		CENTRAL CONTROL	0001852869	344270	.W. 0001862344
DF002		CENTRAL CONTROL	0001852870	344270	.W. 0001862344
DF003		CENTRAL CONTROL	0001852871	344270	.W. 0001862344
DF008		CENTRAL CONTROL	0001852872	344270	.W. 0001862344
DF009		CENTRAL CONTROL	0001852873	344270	.W. 0001862344
DF971		CENTRAL CONTROL	0001852874	344828	.W. 0001862344
DF974		CENTRAL CONTROL	0001852875	344270	.W. 0001862344
DF975		CENTRAL CONTROL	0001852876	344270	.W. 0001862344
DF976		CENTRAL CONTROL	0001852877	344270	.W. 0001862344
DF977		CENTRAL CONTROL	0001852878	344270	.W. 0001862344
DG002		CENTRAL CONTROL	0001852879	344270	.W. 0001862344
DG003		CENTRAL CONTROL	0001852880	344270	.W. 0001862344
DG008		CENTRAL CONTROL	0001852881	344270	.W. 0001862344
DG009		CENTRAL CONTROL	0001852882	344270	.W. 0001862344
DG971		CENTRAL CONTROL	0001852883	344828	.W. 0001862344
DG974		CENTRAL CONTROL	0001852884	344270	.W. 0001862344
DG975		CENTRAL CONTROL	0001852885	344270	.W. 0001862344
DG976		CENTRAL CONTROL	0001852886	344270	.W. 0001862344
DG977		CENTRAL CONTROL	0001852887	344270	.W. 0001862344
DH002		CENTRAL CONTROL	0001852888	344270	.W. 0001862344
DH003		CENTRAL CONTROL	0001852889	344270	.W. 0001862344
DH008		CENTRAL CONTROL	0001852890	344270	.W. 0001862344
DH009		CENTRAL CONTROL	0001852891	344270	.W. 0001862344
DH010		CENTRAL CONTROL	0001852892	344270	.W. 0001862344
DH011		CENTRAL CONTROL	0001852893	344828	.W. 0001862344
DH014		CENTRAL CONTROL	0001852894	344270	.W. 0001862344
DH015		CENTRAL CONTROL	0001852895	344270	.W. 0001862344
DH016		CENTRAL CONTROL	0001852896	344270	.W. 0001862344
DH017		CENTRAL CONTROL	0001852897	344270	.W. 0001862344
DJ002		CENTRAL CONTROL	0001859588	344270	.W. 0001862344
DJ003		CENTRAL CONTROL	0001859589	344270	.W. 0001862344
DJ008		CENTRAL CONTROL	0001859590	344270	.W. 0001862344
DJ009		CENTRAL CONTROL	0001859591	344270	.W. 0001862344

VOLUME 043 MACHINE 3705- -0080232 MODEL M81 SYSTEM 0002904 MODE BOX SHIP 81/12/11

## LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
DJ010		CENTRAL CONTROL	0001859592	344270	.W. 0001862344
DJ011		CENTRAL CONTROL	0001859593	344828	.W. 0001862344
DJ014		CENTRAL CONTROL	0001859594	344270	.W. 0001862344
DJ015		CENTRAL CONTROL	0001859595	344270	.W. 0001862344
DJ016		CENTRAL CONTROL	0001859596	344270	.W. 0001862344
DJ017		CENTRAL CONTROL	0001859597	344270	.W. 0001862344
DK002		CENTRAL CONTROL	0001859598	344270	.W. 0001862344
DK003		CENTRAL CONTROL	0001859599	344270	.W. 0001862344
DK008		CENTRAL CONTROL	0001859600	344270	.W. 0001862344
DK009		CENTRAL CONTROL	0001859601	344270	.W. 0001862344
DK971		CENTRAL CONTROL	0001859602	344828	.W. 0001862344
DK974		CENTRAL CONTROL	0001859603	344270	.W. 0001862344
DK975		CENTRAL CONTROL	0001859604	344270	.W. 0001862344
DK976		CENTRAL CONTROL	0001859605	344270	.W. 0001862344
DK977		CENTRAL CONTROL	0001859606	344270	.W. 0001862344
DL001		CENTRAL CONTROL	0001859607	344828	.W. 0001862344
DL002		CENTRAL CONTROL	0001859608	344270	.W. 0001862344
DL003		CENTRAL CONTROL	0001859609	344270	.W. 0001862344
DL004		CENTRAL CONTROL	0001859610	344270	.W. 0001862344
DL005		CENTRAL CONTROL	0001859611	344270	.W. 0001862344
DL006		CENTRAL CONTROL	0001859612	344270	.W. 0001862344
DL007		CENTRAL CONTROL	0001859613	344270	.W. 0001862344
DL008		CENTRAL CONTROL	0001859614	344270	.W. 0001862344
DL009		CENTRAL CONTROL	0001859615	344270	.W. 0001862344
DL010		CENTRAL CONTROL	0001859616	344270	.W. 0001862344
DM001		CENTRAL CONTROL	0001859617	344828	.W. 0001862344
DM002		CENTRAL CONTROL	0001859618	344828	.W. 0001862344
DM003		CENTRAL CONTROL	0001859619	344270	.W. 0001862344
DM004		CENTRAL CONTROL	0001859620	344270	.W. 0001862344
DM005		CENTRAL CONTROL	0001859621	344270	.W. 0001862344
DM006		CENTRAL CONTROL	0001859622	344270	.W. 0001862344
DM007		CENTRAL CONTROL	0001859623	344270	.W. 0001862344
DM008		CENTRAL CONTROL	0001859624	344270	.W. 0001862344
DM009		CENTRAL CONTROL	0001859625	344270	.W. 0001862344
DM010		CENTRAL CONTROL	0001859626	344270	.W. 0001862344
DN001		CENTRAL CONTROL	0001859627	344270	.W. 0001862344
DN002		CENTRAL CONTROL	0001859628	344270	.W. 0001862344
DN003		CENTRAL CONTROL	0001859629	344270	.W. 0001862344
DN004		CENTRAL CONTROL	0001859630	344270	.W. 0001862344
DN005		CENTRAL CONTROL	0001859631	344270	.W. 0001862344
DP991		CENTRAL CONTROL	0001859632	344270	.W. 0001862344
DP992		CENTRAL CONTROL	0001859633	344270	.W. 0001862344

REQUESTED BY \* LINE

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## LOGIC TYPE -0- SYSTEMS DIAGRAMS

PAGE NUM	SH	TITLE	PART NUM	EC NUM	FEATURE B/M OR B/MS
DP993		CENTRAL CONTROL	0001859634	344270	.W. 0001862344
DP994		CENTRAL CONTROL	0001859635	344270	.W. 0001862344
DQ001		CENTRAL CONTROL	0001859636	344270	.W. 0001862344
DQ002		CENTRAL CONTROL	0001859637	344270	.W. 0001862344
DQ003		CENTRAL CONTROL	0001859638	344270	.W. 0001862344
DQ004		CENTRAL CONTROL	0001859639	344270	.W. 0001862344
DR991		CENTRAL CONTROL	0001859640	344270	.W. 0001862344
DR992		CENTRAL CONTROL	0001859641	344270	.W. 0001862344
DR993		CENTRAL CONTROL	0001986952	344270	.W. 0001862344
DR994		CENTRAL CCNTROL	0001986953	344270	.W. 0001862344
DS001		CENTRAL CONTROL	0001986954	344270	.W. 0001862344
DS002		CENTRAL CONTROL	0001986955	344270	.W. 0001862344
DS003		CENTRAL CONTROL	0001986956	344270	.W. 0001862344
DS004		CENTRAL CONTROL	0001986957	344270	.W. 0001862344
DS005		CENTRAL CONTROL	0001986958	344270	.W. 0001862344
DT001		CENTRAL CONTROL	0001986959	344270	.W. 0001862344
DU001		CENTRAL CONTROL	0001986960	344270	.W. 0001862344
DV001		CENTRAL CONTROL	0001986961	344270	.W. 0001862344
DW001		CENTRAL CONTROL	0001986962	344828	.W. 0001862344
DY001		CENTRAL CONTROL	0001986963	344270	.W. 0001862344
DZ001		CENTRAL CONTROL	0001986966	344270	.W. 0001862344
DZ002		CENTRAL CONTROL	0001986967	344828	.W. 0001862344
DZ002A		CENTRAL CONTROL	0004499514	344828	.W. 0001862344

TOTAL PART NUMBERS THIS VOLUME 107

+ MEM RESET—CM001BB6—2  
 + MEM STORE NEW—CM001DD2—7  
 + RESET MEMORY DIAG REG—CM003GM6—12  
 - ALLOW SET MEMORY DIAG REG—CM003SA2—17  
 + BAD ADDRESS—CS002DK6—22  
 + RESET—CU010FM2—27  
 - FLOAT—DB101020—32  
 - FLOAT—DB101021—37  
 - FLOAT—DB101022—42  
 - FLOAT—DB101023—47  
 - FLOAT—DB101024—52  
 - FLOAT—DB101025—57  
 - FLOAT—DB101026—62  
 - FLCAT—DB101033—67  
 - FLOAT—DB101034—72  
 - FLOAT—DB101039—77  
 - FLCAT—DB101100—82  
 GND—DU001AL6—87

2 ZB02 [BF] 01J 102  
 7 ZD03 [AB] 04C 109  
 27 ZB05 [DB] 03E 116  
 17 ZD07 [CD] 04G 123  
 12 ZD09 [BD] 05G 130  
 22 ZB09 [AH] 06G 137  
 37 ZD06 [DD] 03J 144  
 42 ZB10 [AD] 06K 151  
 47 ZD10 [BH] 09J 158  
 52 ZD12 [DH] 09K 165  
 57 ZB12 [DL] 11J 172  
 62 ZB13 [AL] 12C 179  
 87 ZB11 [CH] 09E 186

102 01R FD W02 201  
 109 02R SPEC W03 202  
 116 03R W05 204  
 123 05R W06 205  
 130 06R W07 206  
 137 08R W09 208  
 CONN A-B4A4

144 05N FM W26 243  
 151 07N W28 245  
 158 08N W29 246  
 165 09N W30 247  
 172 11N W32 249  
 179 12N W33 250  
 CONN A-B4A4

67 ZD11 [CL] 09F 257  
 72 ZD13 [BL] 11E 262  
 77 ZB03 [BB] 01F 267  
 32 ZD04 [CB] 01K 272  
 CONN A-B4A4

186 09M FH W10 301  
 257 10M SPEC W11 302  
 262 12M W13 304  
 82 D02 W22 305  
 267 03N W24 307  
 272 04N W25 308  
 CONN A-B4A4

000 DB101  
 201 + MEM RESET—AM001-FD1  
 202 + STORE NEW—AM001-FD2  
 204 + RESET—AM001-FD4  
 205 - ALLOW SET MEMORY DIAG REG—FD5  
 206 + RESET MEMORY DIAG REG—AM001-FD6  
 208 + BAD ADDRESS—AM001-FD8  
 301 + CABLE PLUGGED IN—AM001-FH1  
 302 GROUND LEVEL—AM001-FH2  
 304 GROUND LEVEL—AM001-FH4  
 305 GROUND LEVEL—AM001-FH5  
 307 GROUND LEVEL—AM001-FH7  
 308 GROUND LEVEL—AM001-FH8  
 243 GROUND LEVEL—AM001-FM1  
 245 GROUND LEVEL—AM001-FM3  
 246 GROUND LEVEL—AM001-FM4  
 247 GROUND LEVEL—AM001-FM5  
 249 GROUND LEVEL—AM001-FM7  
 250 GROUND LEVEL—AM001-FM8

LOC. TYPE  
A-B4A4 6797

MEMORY CONTROL CABLE	
E.C.—HISTORY	E.MACH.3705
FRAME	01
DATE	10-14-80
LAST EC	344270
IBM CORP. SCD	DB101
P.N. 1852861	000

DB101  
000

- SELECT LS REG GROUP 1+2 CC006AU4 2  
 - SELECT LS REG GROUP 1+3 CC006AV4 10  
 - SELECT LS REG BIT 0+1+2+3 CC006AU4 18  
 - WRITE LS CC006BJ4 26  
 - SELECT LS REG 0+1+4+5 CC006BK4 34  
 - SELECT LS REG BIT 0+2+4+6 CC006BL4 42  
 + FORCE ERROR IN BIT 5 CK002DC2 50  
 + FORCE ERROR IN BIT 4 CK002DD2 58  
 - GATE INPUT 74 CC004FJ6 66  
 + SET LAR CS001DM2 74  
 - GATE Y BUS TO B REC CS004ED2 82

42 G09 GE SERV\*  
 34 J09 GD SERV\*  
 18 J10 GC SERV\*  
 10 J11 GB SERV\*  
 2 G10 GA SERV\*  
 26 G08 FM SERV\*  
 50 B07 EM SERV\*  
 58 D12 EK SERV\*  
 82 M11 CJ SERV\*  
 74 P09 CB SERV\*  
 66 M09 OC SERV\*

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-BLANK COLUMN-

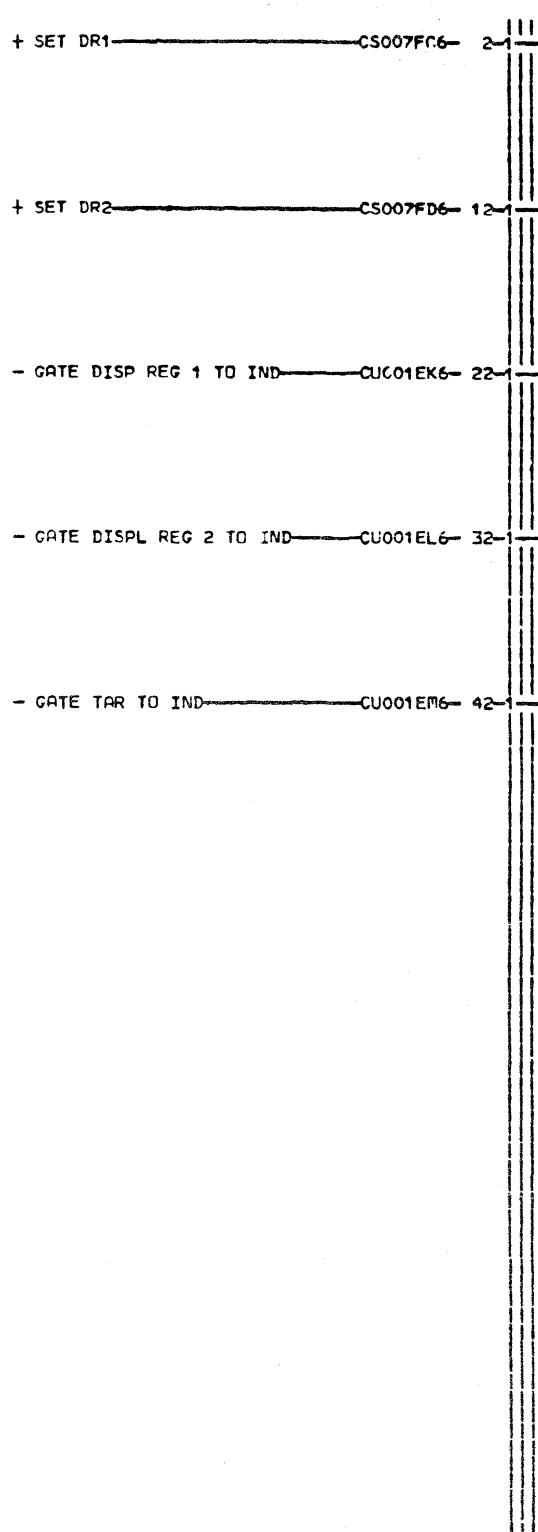
GF SERV\*  
 A-B4S2 S04 404  
 ED SERV\* R10 409  
 A-B4S2 +  
 EB SERV\* R12 416  
 A-B4S2 +

000 DE002  
 416 - FLOAT CU011-EB2  
 409 - FLOAT CU011-ED2  
 404 - FLOAT LCG001 4DE971 4DE976 GF4

LOC. TYPE

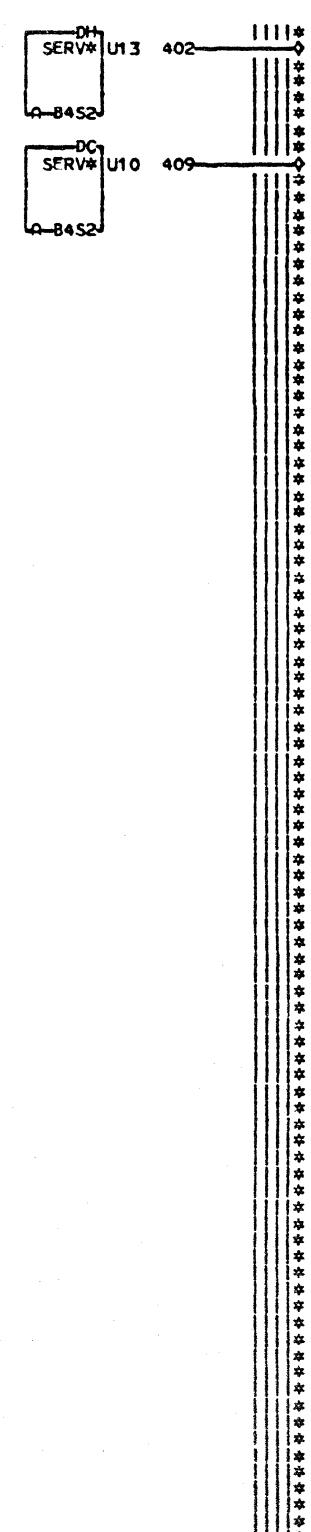
SERV WIRING	
-Ec-HISTORY-E-FACH-3705	
FRAME	01
DATE	LAST EC
10-14-80 344270	
IBM CORP-SCD DE002	
P.N. 1852662 000	

DE002  
000



-BLANK COLUMN-

-BLANK COLUMN-



000 DE003  
----- AP011-DC2

409 - FLOAT

AP011-DC2

402 - FLOAT

08011-0H2

= GATE DISP REG 1 TO IND C1C01EK6-22-1

- GATE DISPL REG 2 TO IND - CU001EL6- 32-

- GATE TAR TO IND - CU001EM6- 42 -

**LOGS TYPE**

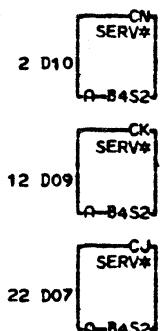
SERV WIRING	
E.C. HISTORY	E MACH 3705
	FRAME 01
DATE LAST EC	IBA CORP. SCD DE003
10-14-80 344270	P.N. 1852863 000

DE003

+ ALU AND CONTROL BYTE X—CA004BA2— 2—

+ ALU OR CONTROL BYTE X—CA004EE6— 12—

+ ALU ADD CONTROL BYTE X—CA004EH6— 22—



000 DE008

LOC. TYPE

DE008  
000

SERV WIRING	
—E.C.—HISTORY—E-MACH-3705	
FRAME	01
DATE	LAST EC
10-14-80	344270
IBR CORP, SCD DE008	
P.N. 1852864 000	

GROUND LEVEL — AA005DB2— 2-1  
 GROUND LEVEL — AA005DB4— 9-1  
 + TIE UP — AU001GF4— 16-2  
 - GATE ADDBUS TO Y BUS — CS004CA6— 23-1  
 - GATE CCU INDATA TO Y BUS — CS004DB2— 30-1  
 - GATE INBUS TO Y BUS — CS004FG2— 37-1  
 - GATE TAR TO Y BUS — CS004FJ2— 44-1  
 + SET TAR — CS007CH2— 51-1  
 + SET SAR — CS007EB2— 58-1  
 - FLOAT — CU011DQ4— 65-1  
 - FLOAT — CU011DR4— 72-1  
 - FLOAT — DE002GF4— 79-1

58 P13 EC SERV# A-B4S2  
 30 P10 CM SERV# A-B4S2  
 37 U05 CK SERV# A-B4S2  
 2 P13 CJ SERV# A-B4S2  
 65 M12 CH SERV# A-B4S2  
 9 S09 CE SERV# A-B4S2  
 16 M08 CD SERV# A-B4S2  
 16 U07 CA SERV# A-B4S2  
 44 S08 BL SERV# A-B4S2  
 23 U06 BC SERV# A-B4S2  
 79 S07 BA SERV# A-B4S2  
 72 P10 AL SERV# A-B4S2  
 51 S05 AG SERV# A-B4S2

-BLANK COLUMN-

-BLANK COLUMN-

EL SERV# U02 402  
 A-B4S2  
 EG SERV# U03 409  
 A-B4S2

000 DE971  
 409 - FLOAT LC0001 LC0002 LC0003 EG2  
 402 - FLOAT LC0001 LC0002 LC0003 EL2

LOC. TYPE

SERV WIRING	
—E.C.—HISTORY—E-MACH-3705	
344270	
FRAME	01
DATE	LAST EC
06-02-81	344828
IBM CORP.SCD	DE971
P.N.	1852865
000	

DE971  
000

+ T2+T3 SET Z-REG BYTE X—CC006FG1- 2-

GR  
SERV\*  
12 B04  
A-B4S2

+ FORCE A REG PARITY ERROR—CK001GB2- 12-

CE  
SERV\*  
2 D13  
A-B4S2

-BLANK COLUMN-

-BLANK COLUMN-

GG  
SERV\* D03 402  
A-B4S2

000 DE974  
427 ALWAYS MINUS  
LDE976 4DF976 LDQ002 EB6

FK  
SERV\*  
A-B4S2 D06 413

420 ALWAYS MINUS  
LDE976 4DF976 LDQ002 EH6

EH  
SERV\*  
A-B4S2 J02 420

413 ALWAYS MINUS 4V DF974-FK6

EB  
SERV\*  
A-B4S2 D04 427

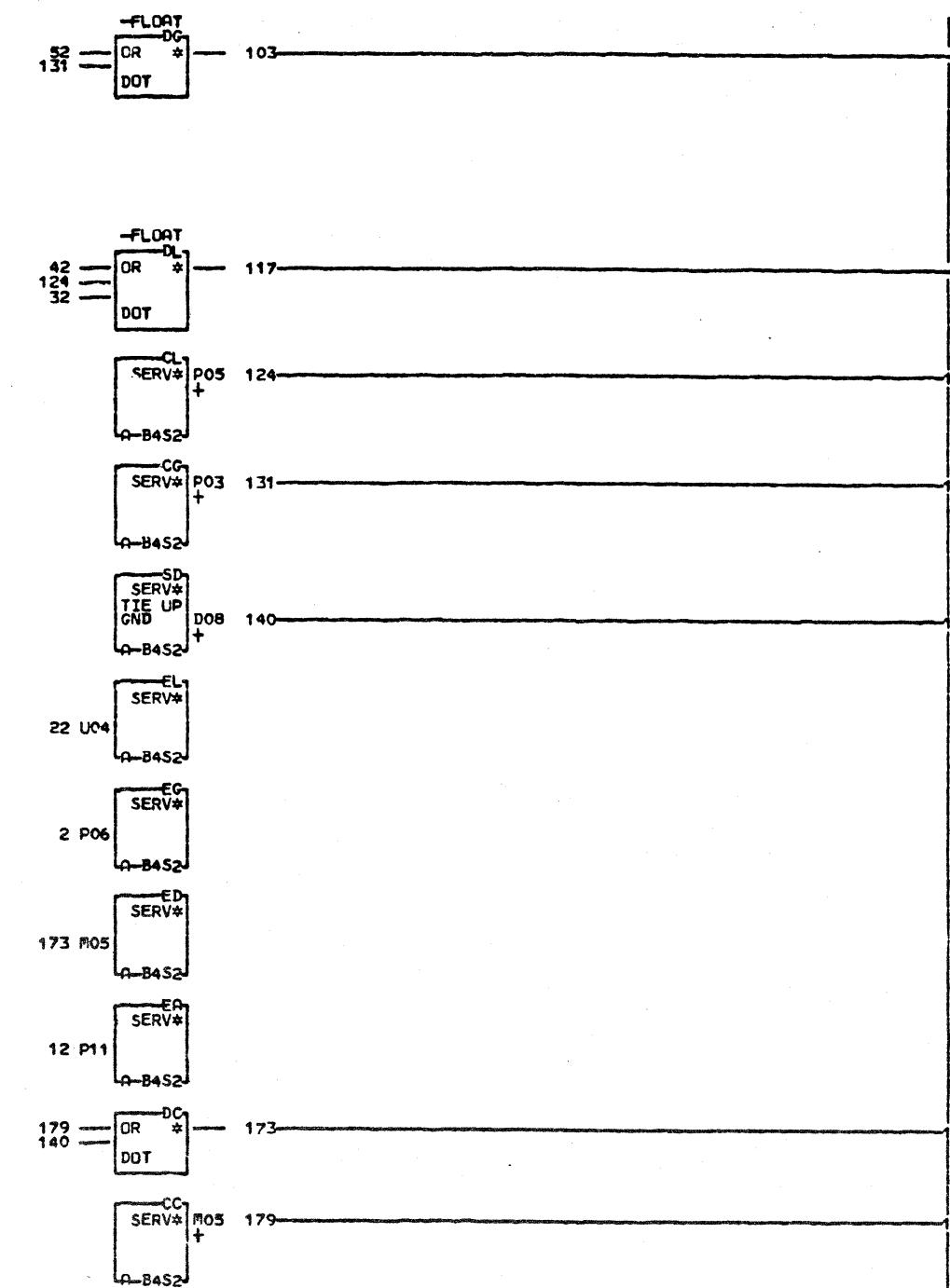
402 - FLOAT DF974-GG2

LOC. TYPE

DE974  
000

SERV WIRING	
E-C HISTORY	E MACH 3705
FRAME	01
DATE	LAST EC
10-14-80	IBM CORP SCD
344270	DE974
PoN 1852866	000

- COMPLEMENT A BUS CA004DD2- 2-  
 - T0+T1 TIME SET A-B REGS CC007HK4- 12-  
 - GATE SAR TO A BUS CS004BK6- 22-  
 - FLOAT DR994GP2- 32-  
 - FLOAT DR994GQ2- 42-  
 - FLOAT DR994GR2- 52-



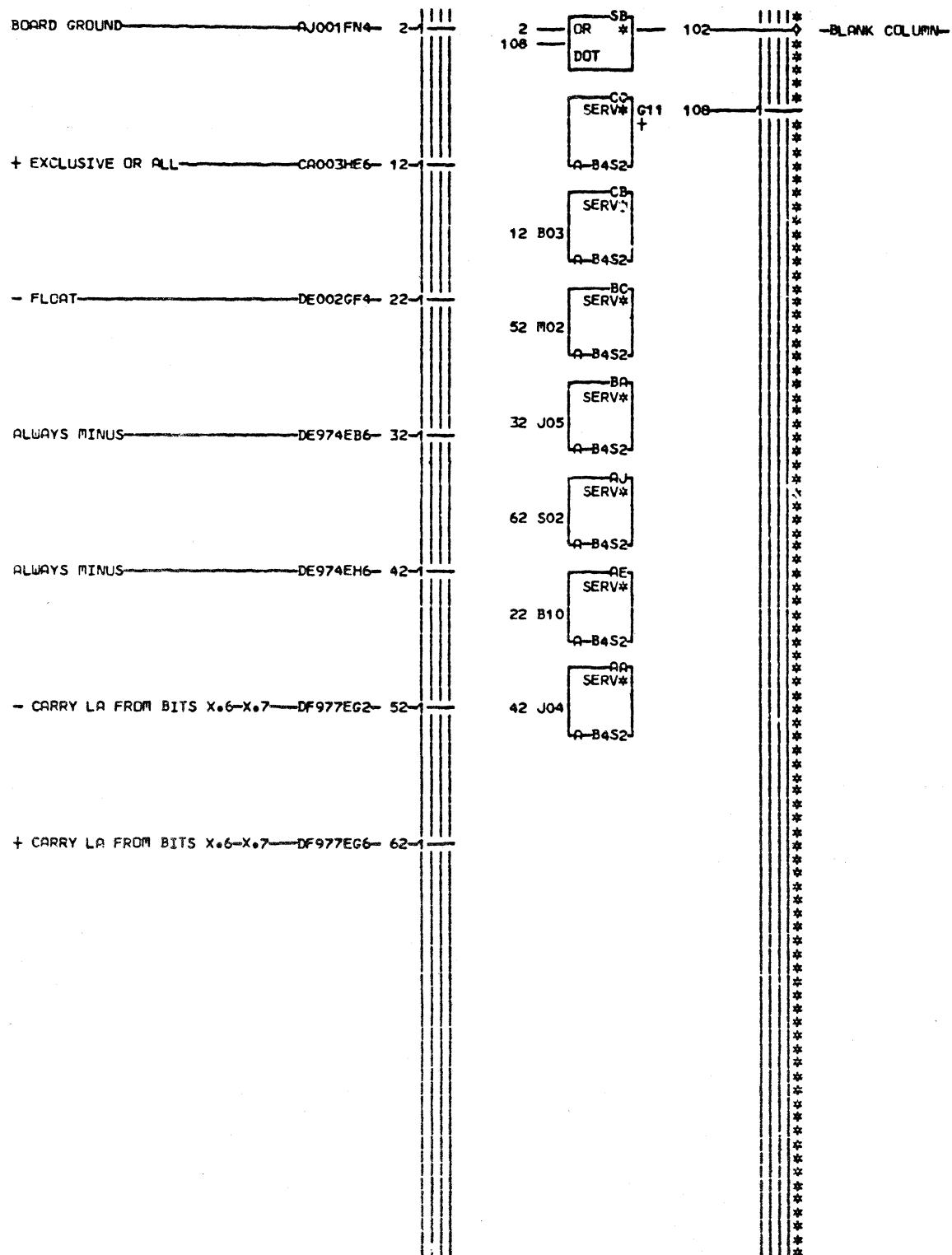
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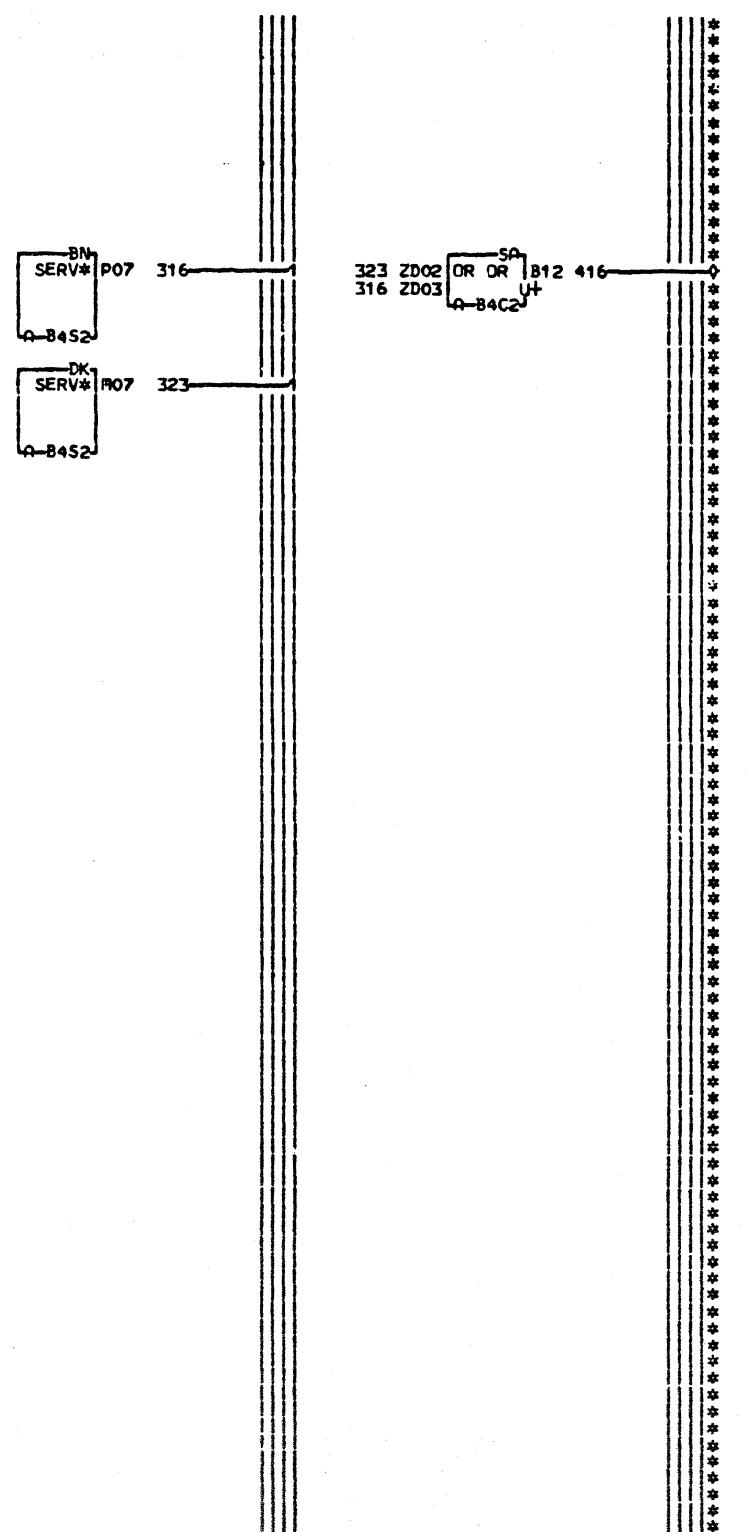
000 DE975  
103 - FLOAT CG001-DG4

117 - FLOAT CG001-DL4

SERV WIRING	
E-C-HISTORY	E-FACH-3705
FRAME	01
DATE LAST EC	DE975
10-14-80 344270	P-N. 1852867 000



LOC. TYPE  
A-B4C2 AB93



000 DE976  
416 FOLLOWS CK003SA4 — CK003-SA6

102 BOARD GROUND — DF976-SB4

SERV WIRING	E-C-HISTORY	MACH 3705
FRAME 01	DATE 10-14-80	LAST EC 344270
IBM CORP-SCD	P.N. 1852868	DE976
000		

DE976  
000

+ TIE UP ————— AU001GF4— 2—

DG  
SERV\*  
A-B4S2  
2 G13

-BLANK COLUMN-

-BLANK COLUMN-

GK  
SERV\*  
A-B4S2 J12 406

000 DE977  
406 ALWAYS PLUS ————— CG001-GK6

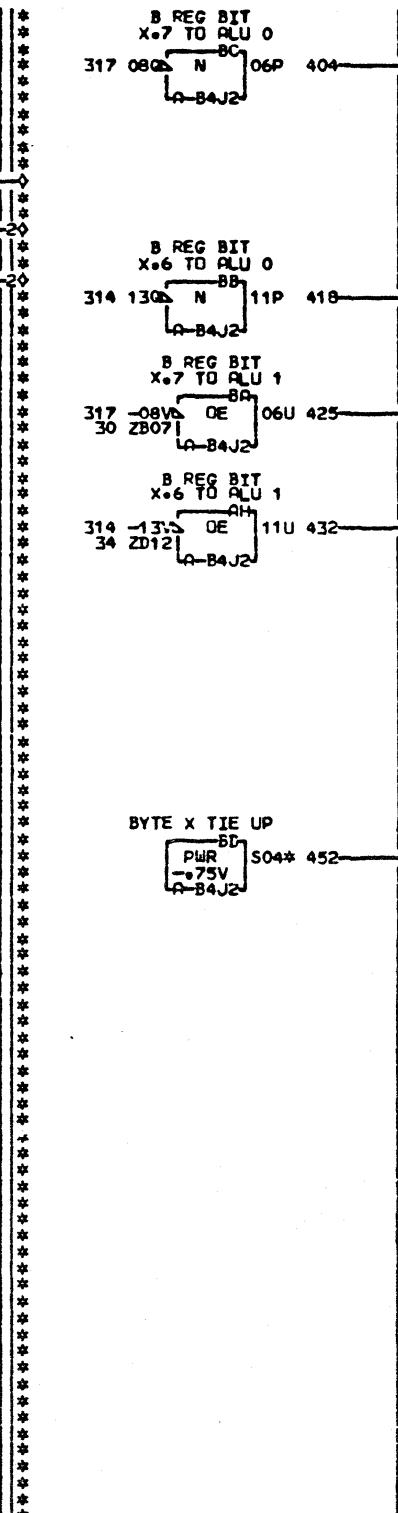
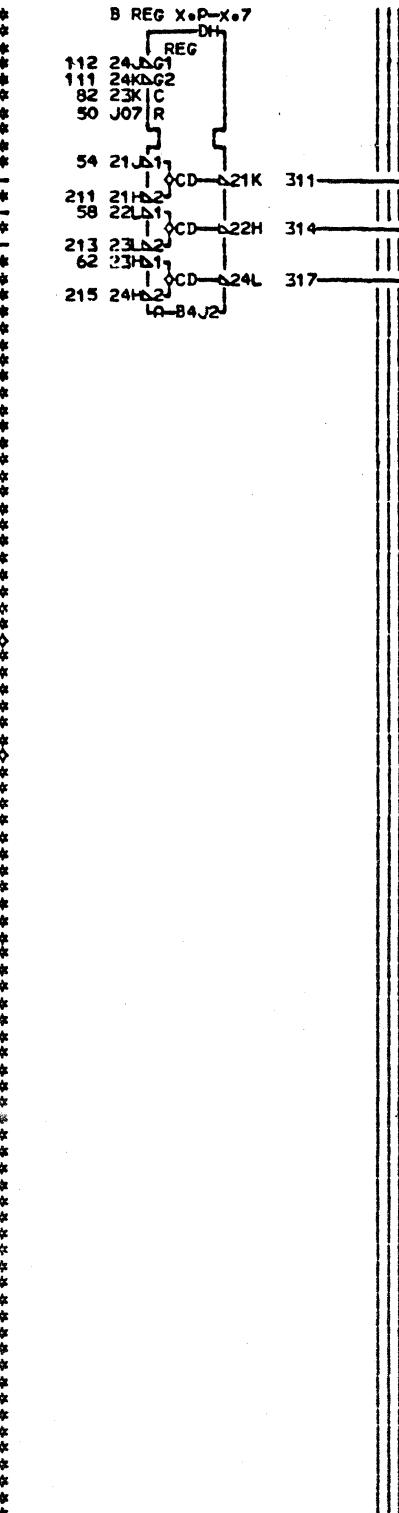
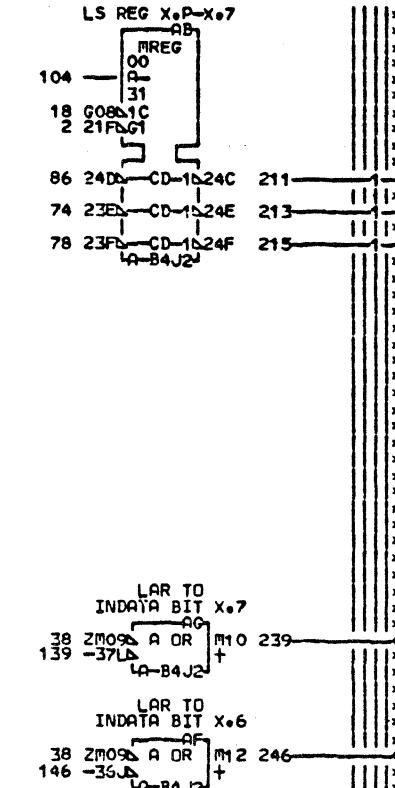
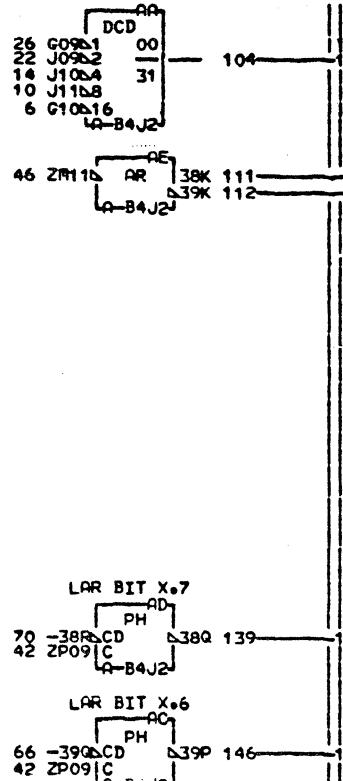
LOC. TYPE

DE977  
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SERV WIRING	
—E.C.—HISTORY—	
E	MACH.3705
FRAME	01
IBM CORP. SCD	DE977
DATE 10-14-80	LAST EC 344270
PoN. 1852869 000	

- FLOAT → AU001GH2- 2  
 - SELECT LS REG GROUP 1+2 → CC006AU4- 6  
 - SELECT LS REG GROUP 1+3 → CC006AV4- 10  
 - SELECT LS REG BIT 0+1+2+3 → CC006AW4- 14  
 - WRITE LS → CC006BJ4- 18  
 - SELECT LS REG 0+1+4+5 → CC006BK4- 22  
 - SELECT LS REG BIT 0+2+4+6 → CC006BL4- 26  
 + FORCE ERROR IN BIT 7 → CK002DA2- 30  
 + FORCE ERROR IN BIT 6 → CK002DB2- 34  
 - GATE INPUT 74 → CQ004FJ6- 38  
 + SET LAR → CS001DM2- 42  
 - GATE Y BUS TO B REG → CS004ED2- 46  
 - FLOAT → DF002001- 50  
 - Y BUS BIT X.P → DF971DB4- 54  
 - Y BUS BIT X.6 → DF971DF4- 58  
 - Y BUS BIT X.7 → DF971DK4- 62  
 - SAR BIT X.6 → DF971EG6- 66  
 - SAR BIT X.7 → DF971EL6- 70  
 - Z REG BIT X.6 → DF974DB2- 74  
 - Z REG BIT X.7 → DF974DB7- 78  
 + SET A REG AND B REG → DF975EA2- 82  
 - Z BUS BIT X.P → DF976BA2- 86

EDGE CONN.  
452 A-B4C6C02  
01A-B3C1C11



- 000 DF002  
 311 - B REG BIT X.P → DF974 LDF976 → DH2  
 314 - B REG BIT X.6 → DF974 LDF977 → DH5  
 317 - B REG BIT X.7 → DF977-DH8  
 246 + LAR TO INDATA BIT X.6 → CU011-EB2  
 239 + LAR TO INDATA BIT X.7 → CU011-ED2  
 432 + B REG BIT X.6 TO ALU 1 → DF008-EK2  
 425 + B REG BIT X.7 TO ALU 1 → DF009-EK2  
 418 + B REG BIT X.6 TO ALU 0 → F008-FK2  
 404 + B REG BIT X.7 TO ALU 0 → F009 LDF974 → FM2  
 452 + BYTE X TIE UP → DF009 LDF976 → CF4  
 0CD003 LCV001 DF009 LDF971

LOC. TYPE  
A-B4J2 6801

B REG LAR AND LOCAL STORE	
BITS X.P X.6 X.7	
-E.C.-HISTORY	-E.RACH-3705
FRAME 01	
DATE 10-14-80	LAST EC 344270
IBM CORP-SCD DF002	
P.N. 1852870	000

+ SET DR1 — CS007FC6- 2-2  
 + SET DR2 — CS007FD6- 12-2  
 - GATE DISP REG 1 TO IND — CU001EK6- 22-2  
 - GATE DISPL REG 2 TO IND — CU001EL6- 32-2  
 - GATE TAR TO IND — CU001EM6- 42-2  
 - TAR BIT X<sub>6</sub> — DF971AC6- 52-2  
 - TAR BIT X<sub>7</sub> — DF971AL6- 62-2  
 - Z REG BIT X<sub>6</sub> — DF974DB2- 72-2  
 - Z REG BIT X<sub>7</sub> — DF974DB7- 82-2

DR1 BIT X<sub>7</sub>  
 PH  
 82 -53RCD 53Q 104  
 2 ZU11 C A-B4J2  
 DR2 BIT X<sub>7</sub>  
 PH  
 82 -53RCD 53K 111  
 12 ZS11 C A-B4J2  
 DR1 BIT X<sub>6</sub>  
 PH  
 72 -54QCD 54P 118  
 2 ZU11 C A-B4J2  
 DR2 BIT X<sub>6</sub>  
 PH  
 72 -54KCD 54J 125  
 12 ZS11 C A-B4J2

TAR DR1 DR2  
 BIT X<sub>7</sub> TO IND  
 42 ZS13A OR U13 204  
 62 -54D 1+  
 22 -S10AA 104 -52F  
 32 -S12AA 111 -53E  
 111 -53E A-B4J2

TAR DR1 DR2  
 BIT X<sub>6</sub> TO IND  
 22 ZS10AA OR U10 210  
 118 -52C 1+  
 32 -S12AA 125 -51E  
 42 -S13AA 52 -53C  
 52 -53C A-B4J2

000 DF003  
 218 + TAR DR1 DR2 BIT X<sub>6</sub> TO IND — DC2  
 4AP011

204 + TAR DR1 DR2 BIT X<sub>7</sub> TO IND — DH2  
 4AP011

LDC TYPE  
 A-B4J2 6801

CCU DISPLAY REGISTERS 1 AND 2	
BITS X <sub>6</sub> AND X <sub>7</sub>	
E-C HISTORY — E MACH 3705	
FRAME	01
DATE	LAST EC
10-14-80	344270
IBM CORP-SCD	DF003
PoN	1852871
000	

+ ALU AND CONTROL BYTE X — CA004BA2— 2-2

+ ALU OR CONTROL BYTE X — CA004EE6— 12-2

+ ALU ADD CONTROL BYTE X — CA004EH6— 22-2

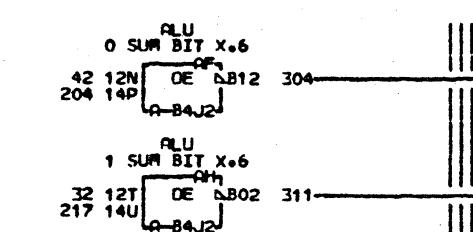
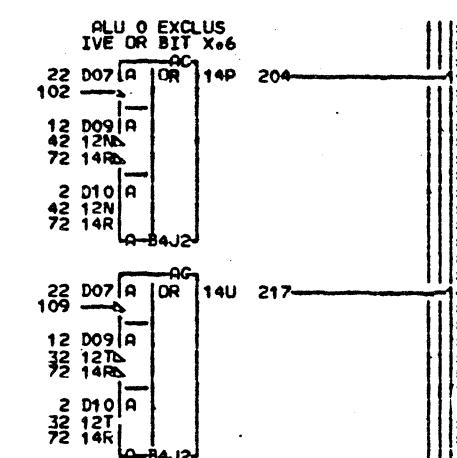
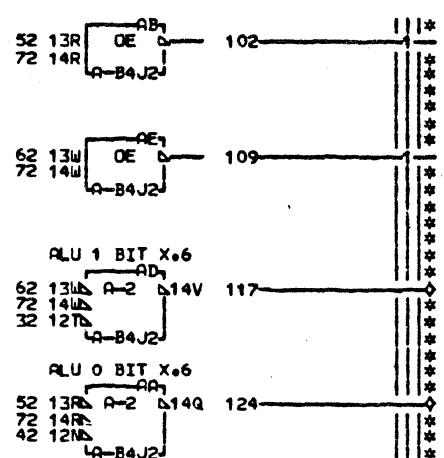
+ B REG BIT X<sub>6</sub> TO ALU 1 — DF002EK2— 32-121

+ B REG BIT X<sub>6</sub> TO ALU 0 — DF002FK2— 42-121

+ ALU 0 CARRY BIT X<sub>7</sub> — DF009DA6— 52-2

+ ALU 1 CARRY BIT X<sub>7</sub> — DF009EG6— 62-2

+ A REG BIT X<sub>5</sub> — DF975GH6— 72-44



- 000 DF008  
124 + ALU 0 CARRY BIT X<sub>6</sub> — DF976-DA6
- 204 + ALU 0 EXCLUSIVE OR BIT X<sub>6</sub> — ED2  
LDF974
- 117 + ALU 1 CARRY BIT X<sub>6</sub> — DF976-EG6
- 304 - ALU 0 SUM BIT X<sub>6</sub> — FF2  
LDF974 LDF976
- 311 - ALU 1 SUM BIT X<sub>6</sub> — GR2  
LDF974 LDF976

LOC. TYPE  
A-B4J2 6801

ALU 0 AND ALU 1		BIT X <sub>6</sub>	
—E.C.—HISTORY—		—E.MACH.3705	
		FRAME	01
DATE	LAST EC	IBM CORP. SCD	DF008
10-14-60	344270	P.N. 1852872	000

DF008  
000

+ ALU AND CONTROL BYTE X—CA004BA2— 2—

+ ALU 0 CONTROL BYTE X—CA004EE6— 12—

+ ALU ADD CONTROL BYTE X—CA004EH6— 22—

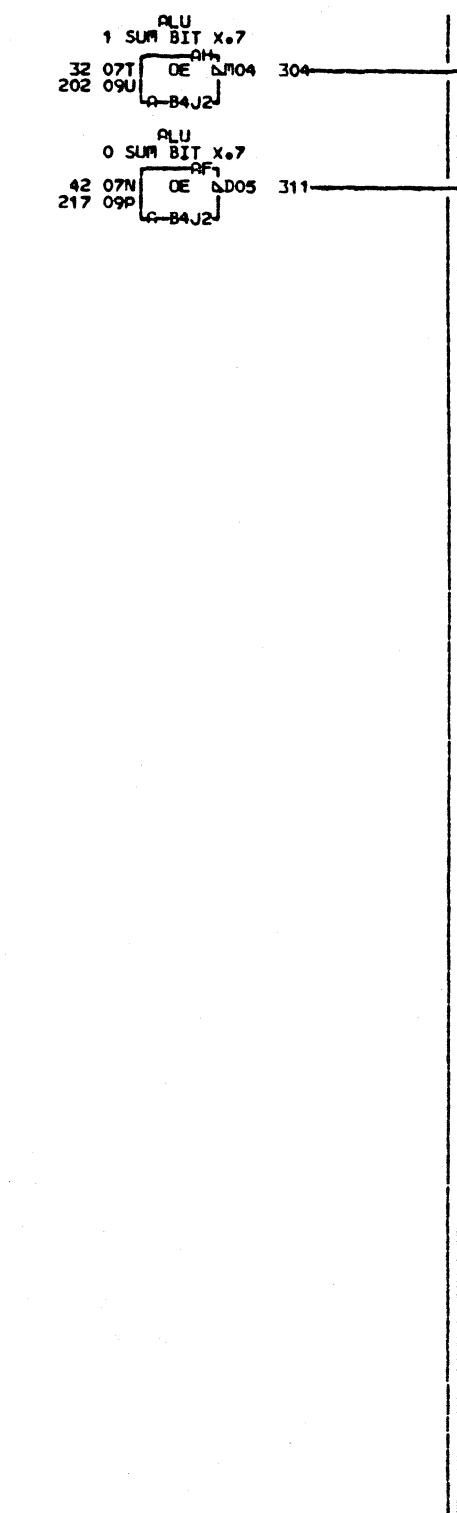
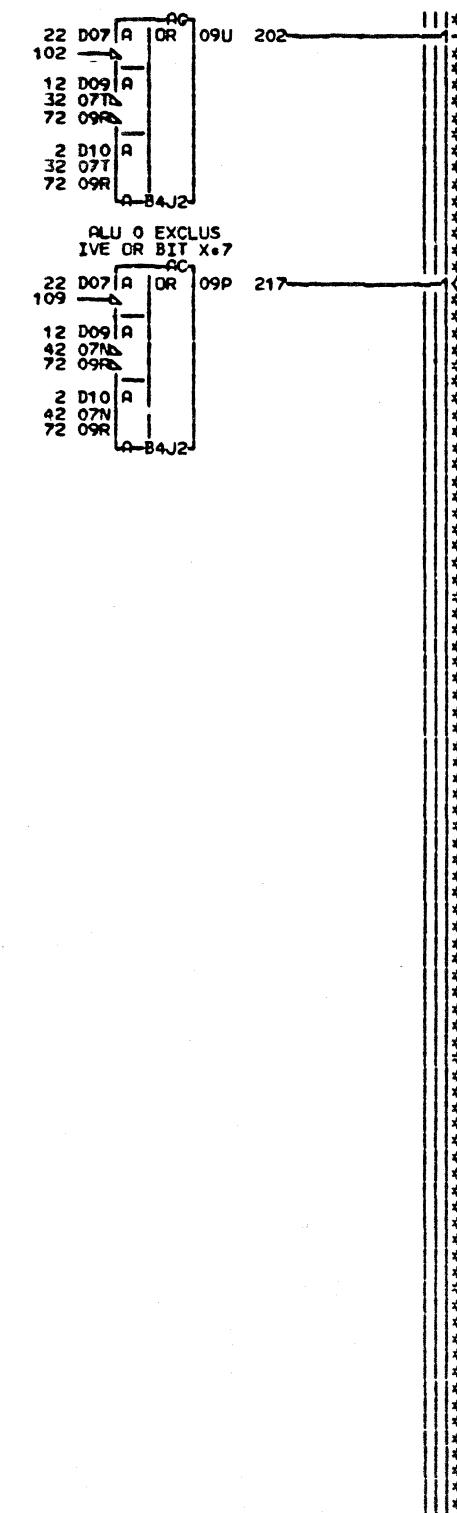
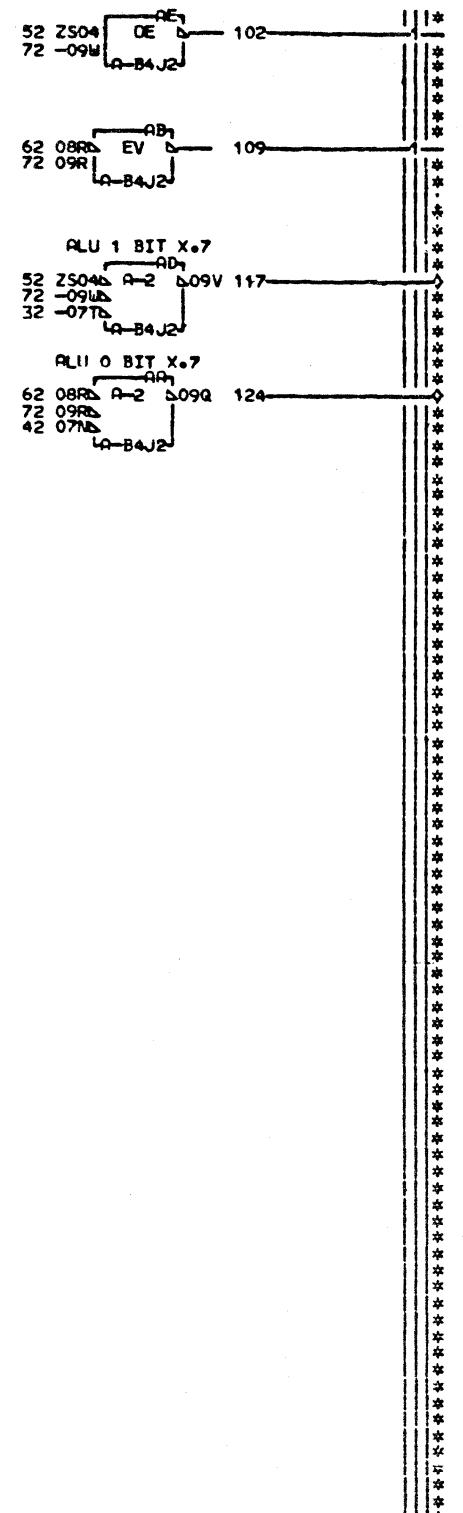
+ B REG BIT X<sub>0</sub>.7 TO ALU 1—DF002EM2— 32—

+ B REG BIT X<sub>0</sub>.7 TO ALU 0—DF002FM2— 42—

+ BYTE X TIE UP—DF002GF4— 52—

- FLDAT—DF009001— 62—

+ R REC BIT X<sub>0</sub>.7—DF975GM6— 72—



000 DF009  
124 + ALU 0 CARRY BIT X<sub>0</sub>.7—DF008-DA6

217 + ALU 0 EXCLUSIVE OR BIT X<sub>0</sub>.7—ED2  
4DF974

117 + ALU 1 CARRY BIT X<sub>0</sub>.7—DF008-EG6

311 - ALU 0 SUM BIT X<sub>0</sub>.7—FF2  
4DF974 4DF976

304 - ALU 1 SUM BIT X<sub>0</sub>.7—GM2  
4DF974 4DF976

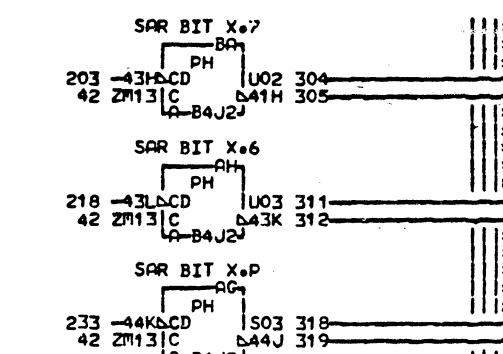
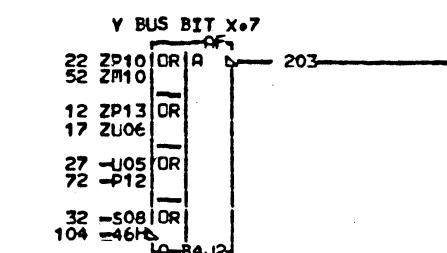
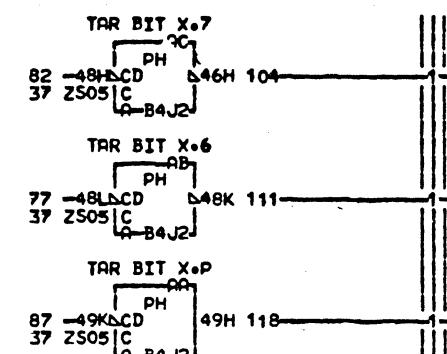
LCC TYPE  
A-B4J2 6801

ALU 0 AND ALU 1	
BIT X <sub>0</sub> .7	
E-C-HISTORY E-MACH-3705	
FRAME	01
DATE LAST EC 10-14-80 344270	
IBM CORP-SCD DF009	
P.N. 1852873 000	

DF009  
000

+ ADBUS BIT X·P — AA002DN2\* 2  
 + ADBUS BIT X·6 — AA002DN4\* 7  
 + ADBUS BIT X·7 — AA002DN6\* 12  
 - GATE ADBUS TO Y BUS — CS004CA6- 17-3  
 - GATE CCU INDATA TO Y BUS — CS004DB2- 22-3  
 - GATE INBUS TO Y BUS — CS004FG2- 27-3  
 - GATE TAR TO Y BUS — CS004FJ2- 32-3  
 + SET TAR — CS007CH2- 37-3  
 + SET SAR — CS007EB2- 42-3  
 + CCU INDATA BIT X·6 — CU011DA4- 47-  
 + CCU INDATA BIT X·7 — CU011DC4- 52-  
 + CCU INDATA BIT X·P — CU013CF6\* 57-  
 + BYTE X TIE UP — DF002GF4\* 62-  
 ALWAYS MINUS 4V — DF971001\* 67-  
 ALWAYS MINUS 4V — DF971002\* 72-  
 - Z REG BIT X·6 — DF974DB2- 77-  
 - Z REG BIT X·7 — DF974DB7- 82-  
 - Z BUS BIT X·P — DF976BA2- 87-

EDGE CONN. A-B4J2S07  
 2 RESISTOR 67 RESISTOR  
 A-B4J2U07 A-B4J2U09  
 7 RESISTOR 72 RESISTOR  
 A-B4J2S09 A-B4J2P12  
 12 RESISTOR A-B4J2P13  
 57 RESISTOR A-B4J2M08  
 62 RESISTOR



000 DF971

111 - TAR BIT X·6 — DF003-AG6

104 - TAR BIT X·7 — DF003-AL6

233 - Y BUS BIT X·P — DF002-DB4

218 - Y BUS BIT X·6 — DF002-DF4

203 - Y BUS BIT X·7 — DF002-DK4

318 + SAR BIT X·P — CM003-EC2

319 - SAR BIT X·P — DF975-EC6

311 + SAR BIT X·6 — EG2  
 CM002 CV001 DS001 DT001  
 DU001 DV001

312 - SAR BIT X·6 — E66  
 DF002 DF975

304 + SAR BIT X·7 — EL2  
 CM002 CV001 DS001 DT001  
 DU001 DV001

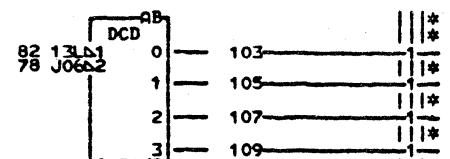
305 - SAR BIT X·7 — EL6  
 DF002 DF975

LOC. TYPE  
A-B4J2 6801

SAR TWR AND Y BUS ASSEMBLER  
 BITS X·P-X·7  
 EC-HISTCRY-E MACH.3705  
 344270

FRAME 01	IBM CORP-SCD DF971
DATE LAST EC 06-02-81 344828	P.N. 1852874 000

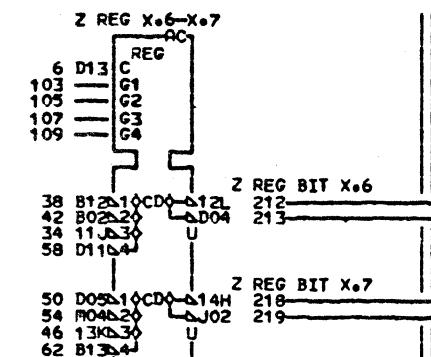
+ ALU ADD CONTROL BYTE X—CA004EH6— 2  
 + T2+T3 SET Z-REG BYTE X—CC006FG1\*— 6  
 + FORCE A REG PAR ERR GATED—CG001SC2— 10  
 ALWAYS MINUS 4V—DE974FK6\*— 14  
 - FLOAT—DE974GG2— 18  
 - B REG BIT X<sub>0</sub>P—DF002DH2— 22  
 - B REG BIT X<sub>0</sub>6—DF002DH5— 26  
 + B REG BIT X<sub>0</sub>7 TO ALU 0—DF002FM2— 30  
 + ALU 0 EXCLUSIVE OR BIT X<sub>0</sub>6—DF008ED2— 34  
 - ALU 0 SUM BIT X<sub>0</sub>6—DF008FF2— 38  
 - ALU 1 SUM BIT X<sub>0</sub>6—DF008GM2— 42  
 + ALU 0 EXCLUSIVE OR BIT X<sub>0</sub>7—DF009ED2— 46  
 - ALU 0 SUM BIT X<sub>0</sub>7—DF009FF2— 50  
 - ALU 1 SUM BIT X<sub>0</sub>7—DF009GM2— 54  
 - FLOAT—DF974014— 58  
 - FLOAT—DF974015— 62  
 + A REG BIT X<sub>0</sub>P—DF975GD6— 66  
 + A REG BIT X<sub>0</sub>6—DF975GH6— 70  
 + A REG BIT X<sub>0</sub>7—DF975GR6— 74  
 - Z BUS BITS X<sub>0</sub>6-X<sub>0</sub>7 SELECT 2—DF976CB2— 78  
 - Z BUS BITS X<sub>0</sub>6-X<sub>0</sub>7 SELECT 1—DF976CC2— 82



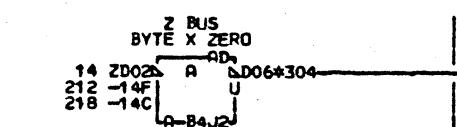
+ EQUALS ERROR  
AE  
18 ZG02EV DR DD03 114  
22 19A  
26 16AB  
30 17N  
A-B4J2

ALU ADD  
CONTROL BYTE X  
2 ZD07 N DD03K 123  
A-B4J2

PARITY ERROR I  
IN A BUS BYTE X  
10 ZB04 EV 16V 130  
66 19T  
70 16T  
74 17T  
A-B4J2



Z REG BIT X<sub>0</sub>6—212  
Z REG BIT X<sub>0</sub>7—213  
Z REG BIT X<sub>0</sub>6—218  
Z REG BIT X<sub>0</sub>7—219



000 DF974  
123 - ALU ADD CONTROL BYTE X— DF976-AC2

212 - Z REG BIT X<sub>0</sub>6—DF002 DF003 DF971—082

218 - Z REG BIT X<sub>0</sub>7—DF002 DF003 DF971—DB7

213 - Z BUS BIT X<sub>0</sub>6—DF976 DR991—EB6

219 - Z BUS BIT X<sub>0</sub>7—DF976 DR992—EH6

304 - Z BUS BYTE X ZERO—CZ001-FK6

114 + B REG BYTE X EVEN PARITY—GG2  
LCK003

130 + PARITY ERROR IN A BUS BYTE X—GM2  
DF976

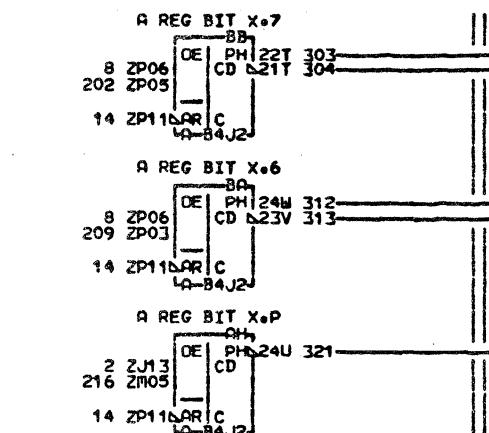
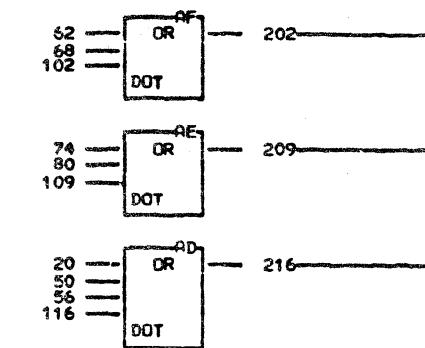
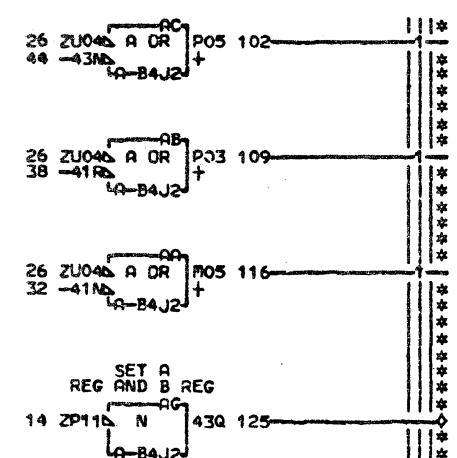
EDGE CONN.  
6 RESISTOR  
A-B4J2D13  
14 RESISTOR  
A-B4J2D02  
304 RESISTOR  
A-B3G2B09  
01A-B4L6D02  
01A-B3L1D11

LOC. TYPE  
A-B3G2 Y702  
A-B4J2 6801

DF974  
000

ALU 0 ALU 1 AND Z REG		
BITS X <sub>0</sub> P-X <sub>0</sub> 6		
E-C-HISTORY	E-MACH-3705	
FRAME	01	
DATE	LAST EC	DF974
10-14-80	344270	000
	IBM CORP-SCD	
	PoNo. 1852875	

+ TIE UP AU001CF4- 2  
 - COMPLEMENT A BUS CA004DD2- 8  
 - TO+T1 TIME SET A-B REGS CC007HK4- 14-3  
 + FORCE A BUS BIT X·P CF002FE6- 20  
 - GATE SAR TO A BUS CS004BK6- 26-3  
 - SAR BIT X·P DF971EC6- 32-1  
 - SAR BIT X·6 DF971EG6- 38-1  
 - SAR BIT X·7 DF971EL6- 44-1  
 + SHIFT RIGHT BIT X·P TO A BUS DR994EC2- 50-1  
 + SDR BIT X·P TO A BUS DR994ED2- 56-1  
 + SHIFT RIGHT BIT X·7 TO A BUS DR994EF2- 62-1  
 + SDR BIT X·7 TO A BUS DR994EG2- 68-1  
 + SDR BIT X·6 TO A BUS DR994EK2- 74-1  
 + SHIFT RIGHT BL. X·6 TO A BUS DR994CN2- 80-1

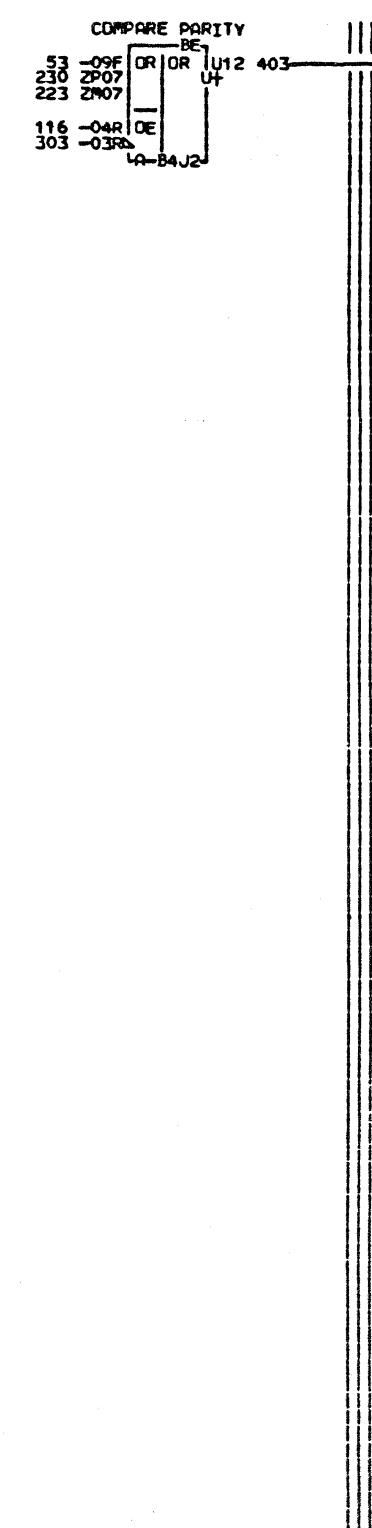
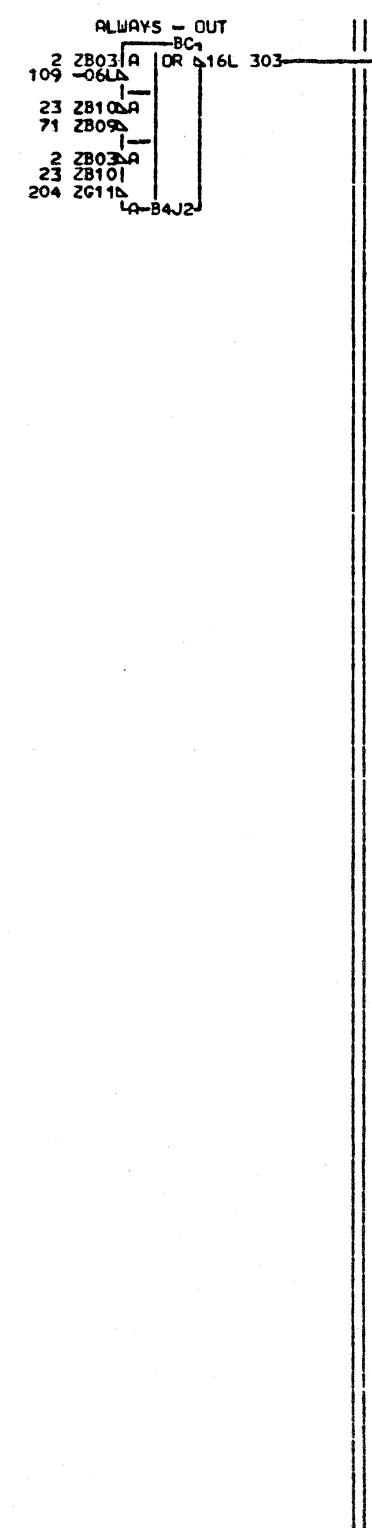
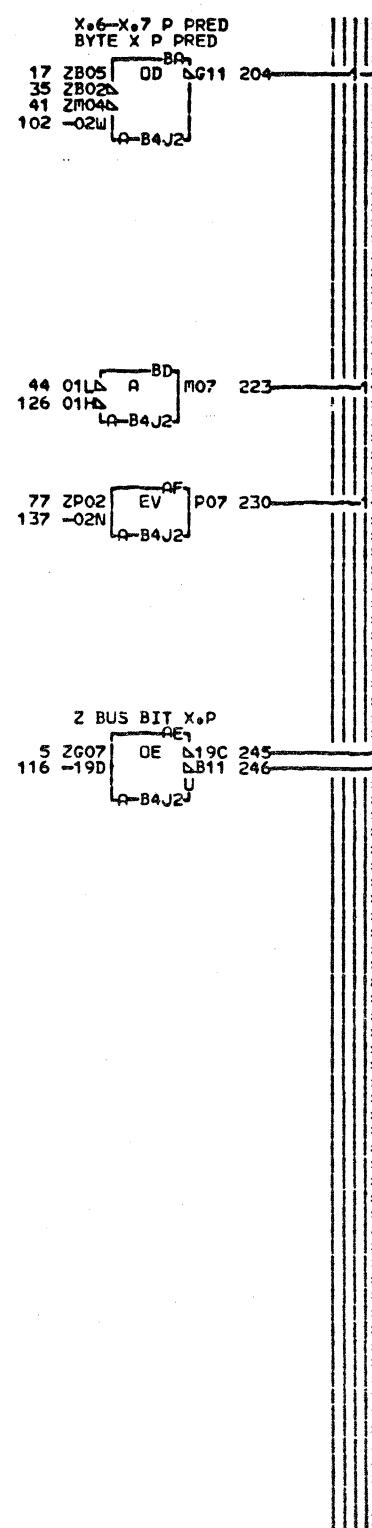
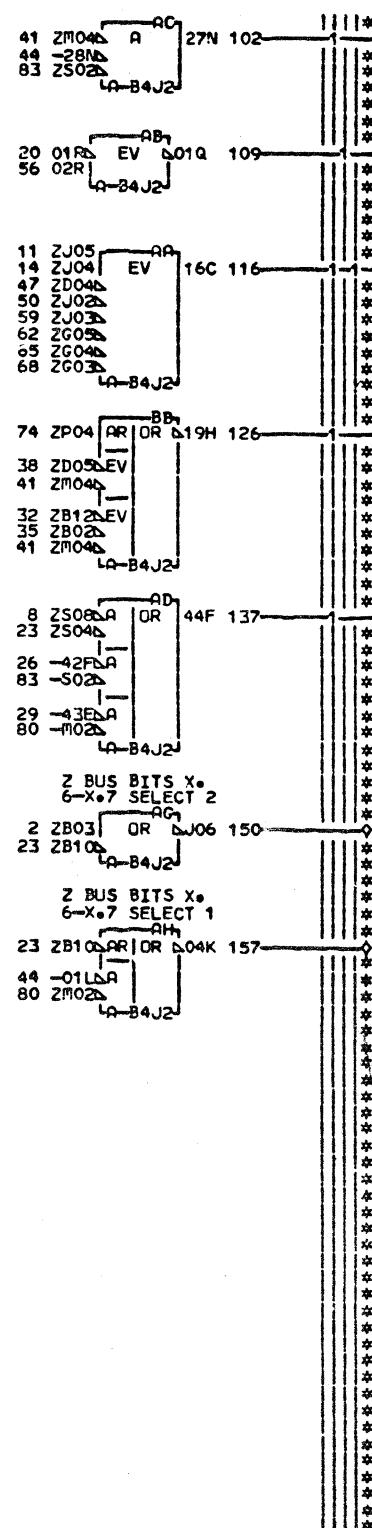
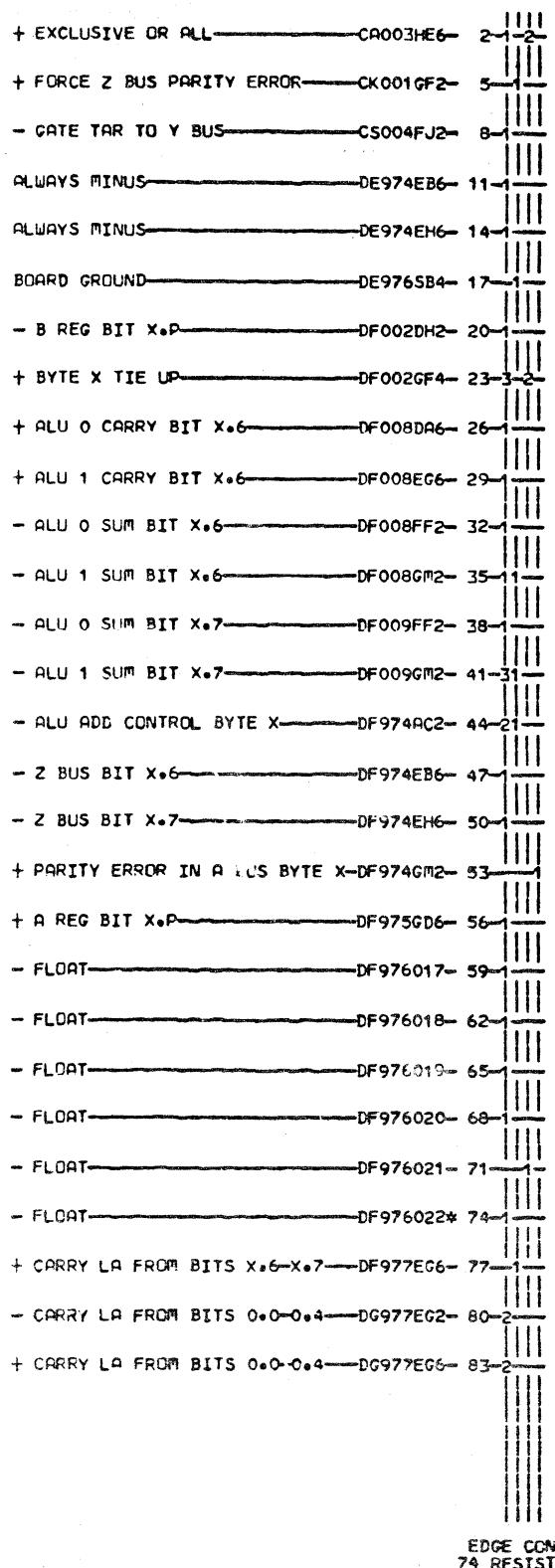


000 DF975  
 125 + SET A REG AND B REC DF002-EA2  
 321 + A REG BIT X·P DF974 4DF976 GD6  
 312 - A REG BIT X·6 DF977-GH2  
 313 + A REG BIT X·6 DF008 4DF974 GH6  
 303 - A REG BIT X·7 DF977-GM2  
 304 + A REG BIT X·7 DF009 4DF974 GM6

LOC. TYPE  
A-84J2 6801

A BUS ASSEMBLER	
BITS X·P-X·7	
-E,C=HISTORY—E,FACH,3705	
FRAME 01	
IBM CORP,SCD DF975	
DATE LRST EC 10-14-80 344270	
PoN 1852876 000	

DF975  
000



000 DF976

150 - 2 BUS BITS X.6-X.7 SELECT 2—CB2  
LDF974

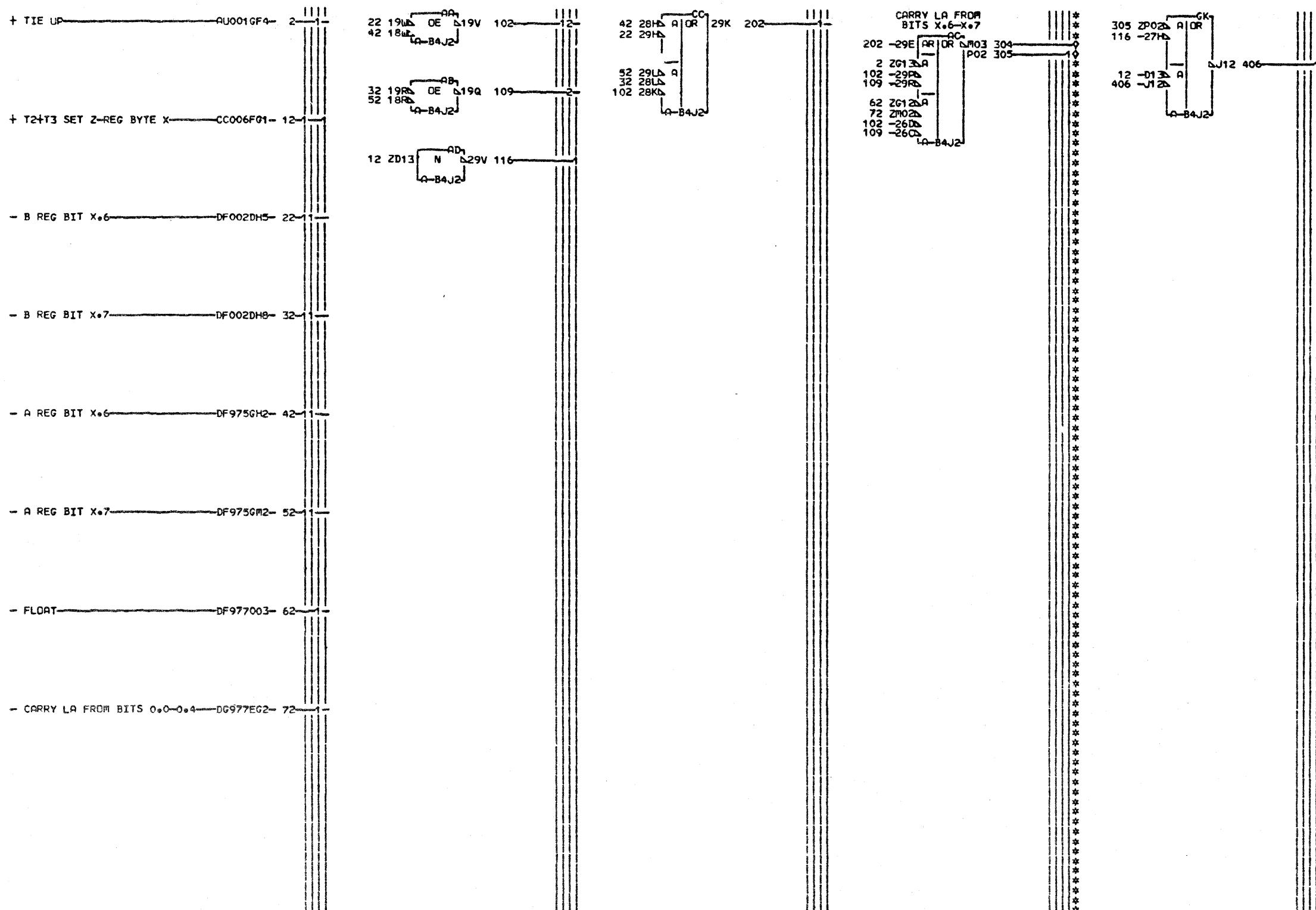
157 - 2 BUS BITS X.6-X.7 SELECT 1—CC2  
LDF974

204 - ALU 1 BIT X,P UNUSED CG2

246 - Z BUS BIT X.P DR992-EA6

403 + ALU+AREG+ZBUS BYTE X ERROR EJ6  
CK003

PLU CHECK	
BITS X=P-Y,7	
<del>E=CE-HISTORY</del>	
E=MACH-3705	
DATE	LAST EC
10-14-80	344270
FRAME	01
IBM CDRP-SCD	DF976
P.N. 1852877	000

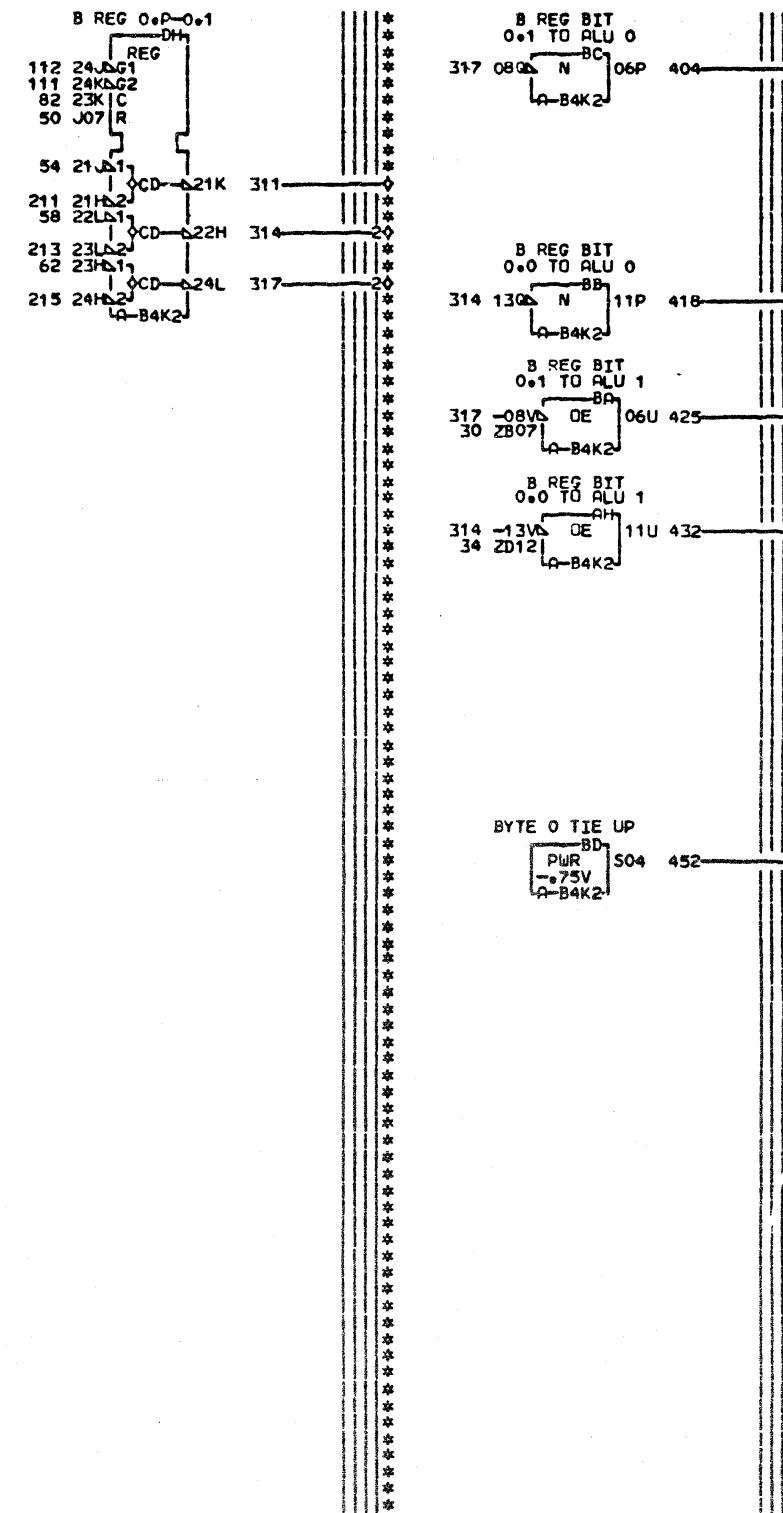
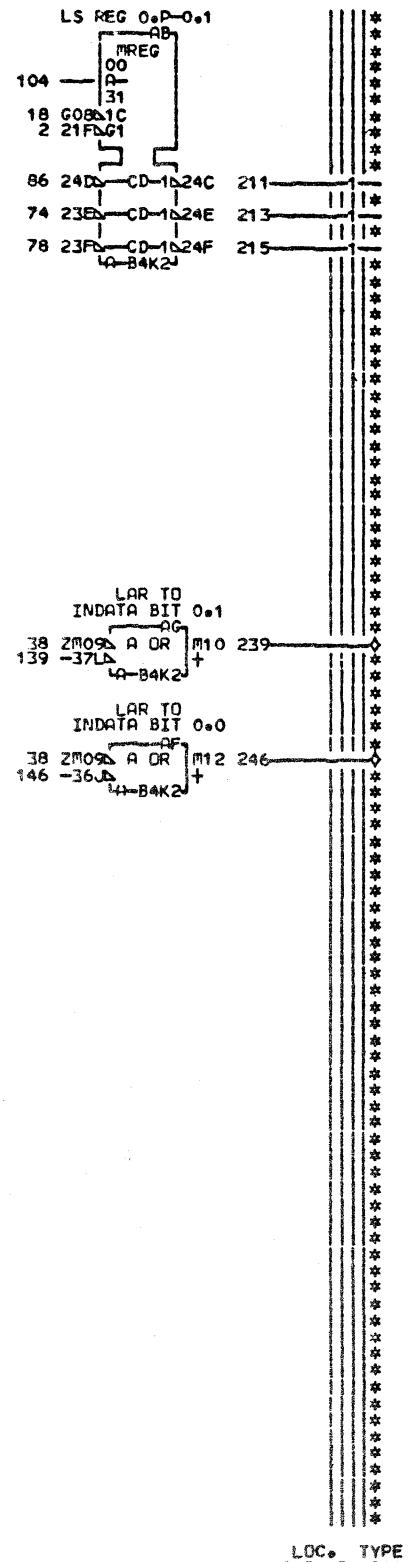
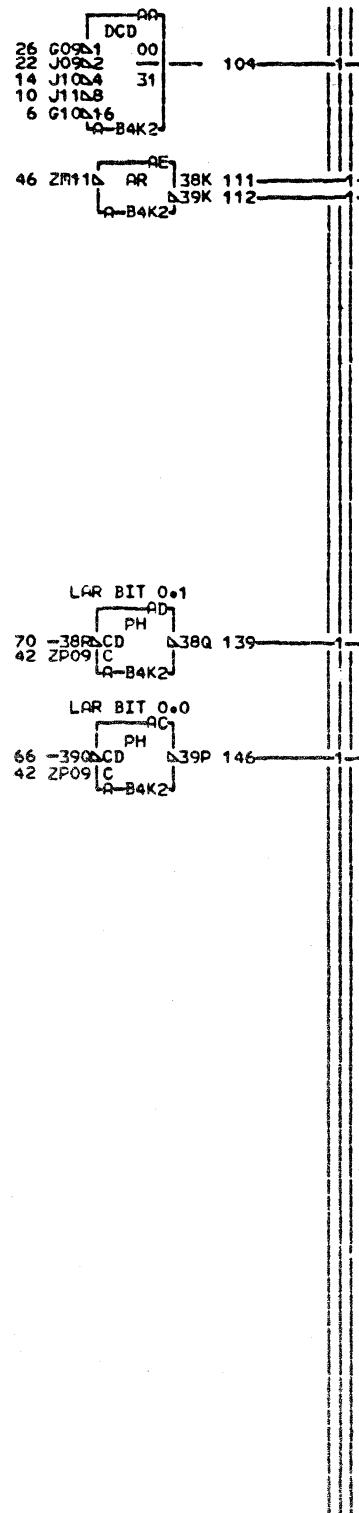


LOC. TYPE  
A-B4J2 6801

5-BIT CARRY LOOKAHEAD	
BITS X.0-X.6	E-MACH 3705
E-C-HISTORY	E-MACH 3705
FRAME	01
DATE 10-14-80	LAST EC 344270
IBM CORP SCD	DF977
P.N. 1852878	000

DF977  
000

- FLOAT AU001GH2- 2  
 - SELECT LS REG GROUP 1+2 CC006AU4- 6  
 - SELECT LS REG GROUP 1+3 CC006AV4- 10  
 - SELECT LS REG BIT 0+1+3 CC006AU4- 14  
 - WRITE LS CC006BJ4- 18  
 - SELECT LS REG 0+1+4+5 CC006BK4- 22  
 - SELECT LS REG BIT 0+2+4+6 CC006BL4- 26  
 + FORCE ERROR IN BIT 1 CK002DK2- 30  
 + FORCE ERROR IN BIT 0 CK002DL2- 34  
 - GATE INPUT 74 CQ004FJ6- 38  
 + SET LAR CS001DM2- 42  
 - GATE Y BUS TO B REG CS004ED2- 46  
 - FLOAT DG002001- 50  
 - Y BUS BIT 0.P DG971DB4- 54  
 - Y BUS BIT 0.0 DG971DF4- 58  
 - Y BUS BIT 0.1 DG971DK4- 62  
 - SAR BIT 0.0 DG971EG6- 66  
 - SAR BIT 0.1 DG971EL6- 70  
 - Z REG BIT 0.0 DG974DB2- 74  
 - Z REG BIT 0.1 DG974DB7- 78  
 + SET A REG AND B REG DG975EP2- 82  
 - Z BUS BIT 0.P DG976BA2- 86



+ SET DR1 — CS007FC6- 2-2  
 + SET DR2 — CS007FD6- 12-2  
 - GATE DISP REG 1 TO IND — CU001EK6- 22-2  
 - GATE DISPLAY REG 2 TO IND — CU001EL6- 32-2  
 - GATE TAR TO IND — CU001EM6- 42-2  
 - TAR BIT 0.0 — DG971AG6- 52-1  
 - TAR BIT 0.1 — DG971AL6- 62-1  
 - Z REG BIT 0.0 — DG974DB2- 72-2  
 - Z REG BIT 0.1 — DG974DB7- 82-2

DR1 BIT 0.1  
 82 -53LCD PH 53Q 104  
 2 ZU11 C BAK2  
 DR2 BIT 0.1  
 82 -53LCD PH 53K 111  
 12 ZS11 C BAK2  
 DR1 BIT 0.0  
 72 -54QCD PH 54P 118  
 2 ZU11 C BAK2  
 DR2 BIT 0.0  
 72 -54KCD PH 54J 125  
 12 ZS11 C BAK2

TAR DR1 DR2  
 BIT 0.1 TO IND  
 42 ZS13DA OR U13 204  
 62 -54DA +  
 22 -S10DA  
 104 -52DA  
 32 -S12DA  
 111 -53ED 4-B4K2

TAR DR1 DR2  
 BIT 0.0 TO IND  
 22 ZS10DA OR U10 218  
 118 -52DA +  
 32 -S12DA  
 125 -S1ED  
 42 -S13DA  
 52 -53ED 4-B4K2

000 DG003  
 218 + TAR DR1 DR2 BIT 0.0 TO IND — DC2  
 4AP012

204 + TAR DR1 DR2 BIT 0.1 TO IND — DH2  
 4AP012

LDC TYPE  
 A-B4K2 6801

CCU DISPLAY REGISTERS 1 AND 2	
BITS 0.0 AND 0.1	
E.C.-HISTORY E.MACH 3705	
FRAME 01	
IBM CORP-SCD	DG003
DATE LAST EC 10-14-80 344270	
P/N 1852880 000	

DG003  
 000

+ ALU AND CONTROL BYTE 0 — CA004EB6— 2-2

+ ALU OR CONTROL BYTE 0 — CA004EF6— 12-2

+ ALU ADD CONTROL BYTE 0 — CA004FJ6— 22-2

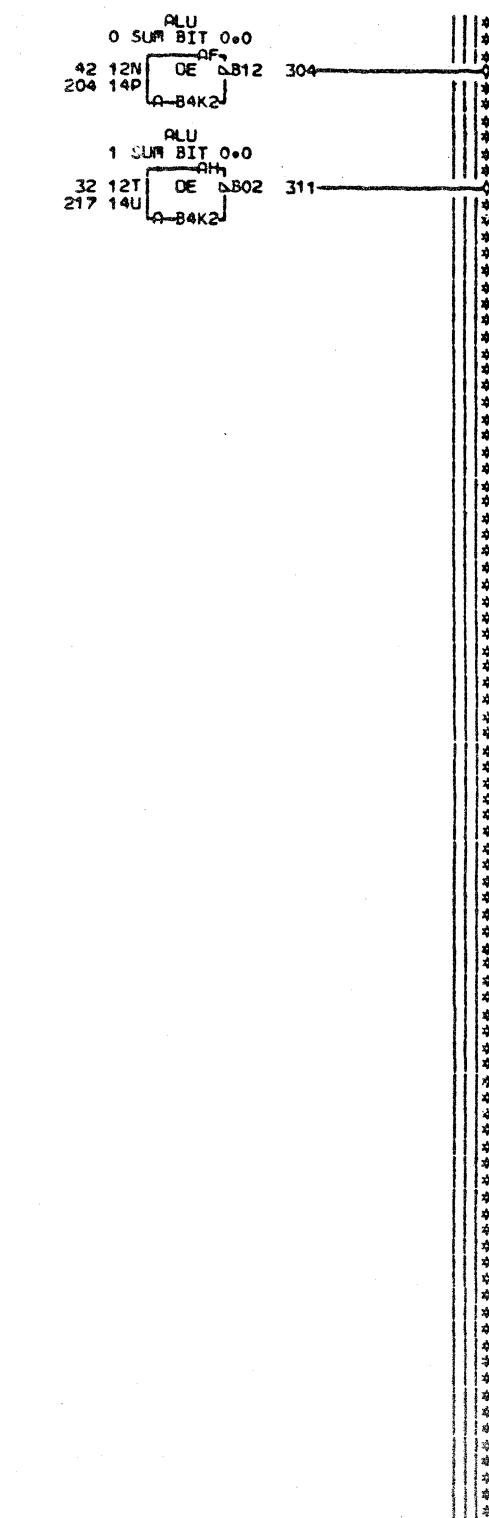
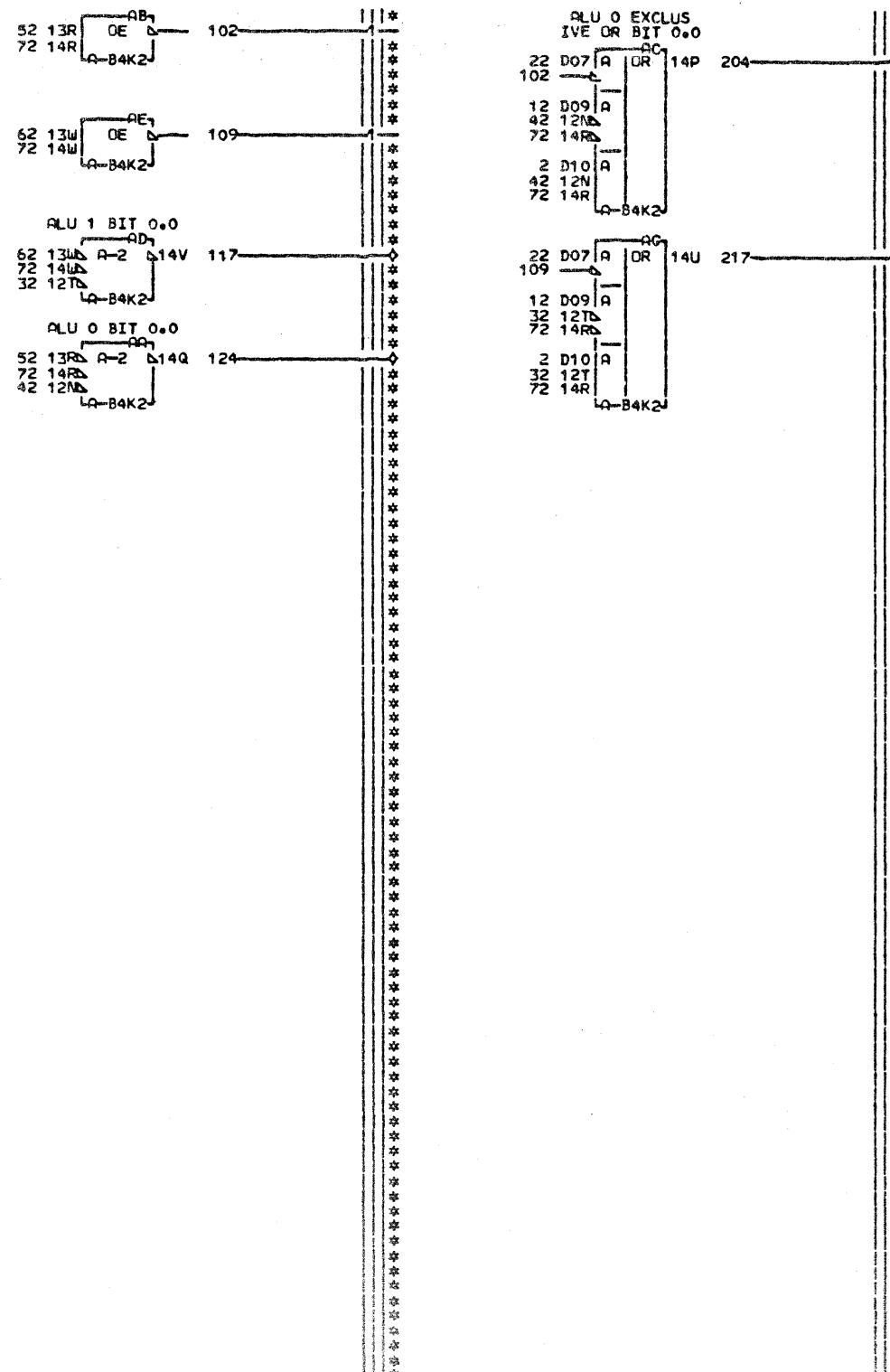
+ B REG BIT 0.0 TO ALU 1 — DG002EK2— 32-2

+ B REG BIT 0.0 TO ALU 0 — DG002FK2— 42-2

+ ALU 0 CARRY BIT 0.1 — DG009DA6— 52-2

+ ALU 1 CARRY BIT 0.1 — DG009EG6— 62-2

+ A REG BIT 0.0 — DG975GH6— 72-4



000 DG008  
124 + ALU 0 CARRY BIT 0.0 — DG976-DAG

204 + ALU 0 EXCLUSIVE OR BIT 0.0 — ED2  
DG974

117 + ALU 1 CARRY BIT 0.0 — DG976-EG6

304 - ALU 0 SUM BIT 0.0 — FF2  
DG974 DG976 LDK974

311 - ALU 1 SUM BIT 0.0 — GM2  
DG974 DG976

LCC<sub>6</sub> TYPE  
A-B4K2 6801

00008  
000

ALU 0 AND ALU 1	BIT 0.0	E-C-HISTORY	MARCH 3705
			FRAME 01
			DATE LAST EC
			10-14-60 344270
			IBM CORP. SCD
			P.N. 1852881
			000

+ ALU AND CONTROL BYTE 0 — CA004EB6— 2-2

+ ALU OR CONTROL BYTE 0 — CA004EF6— 12-2

+ ALU ADD CONTROL BYTE 0 — CA004EJ6— 22-2

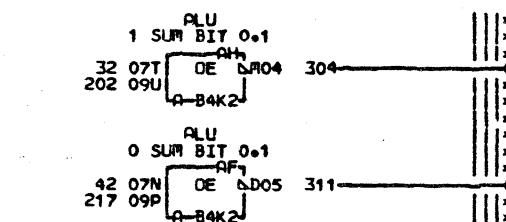
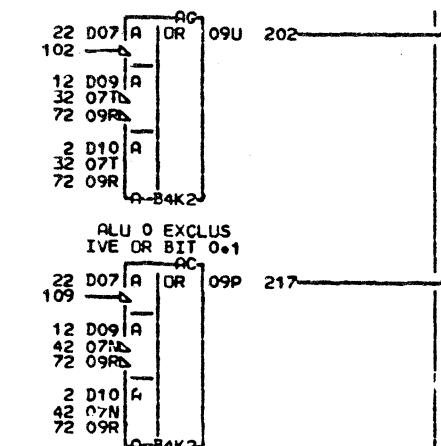
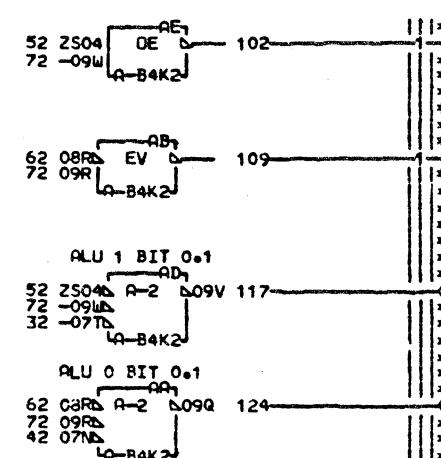
+ B REG BIT 0.1 TO ALU 1 — DG002EM2— 32-21

+ B REG BIT 0.1 TO ALU 0 — DG002FM2— 42-21

+ BYTE 0 TIE UP — DG002GF4— 52-2

- FLOAT — DG009001— 62-2

+ A REG BIT 0.1 — DG975GM6— 72-44



000 DG009  
124 + ALU 0 CARRY BIT 0.1 — DG009-DG6

217 + ALU 0 EXCLUSIVE OR BIT 0.1 — ED2  
4DG974

117 + ALU 1 CARRY BIT 0.1 — DG008-EG6

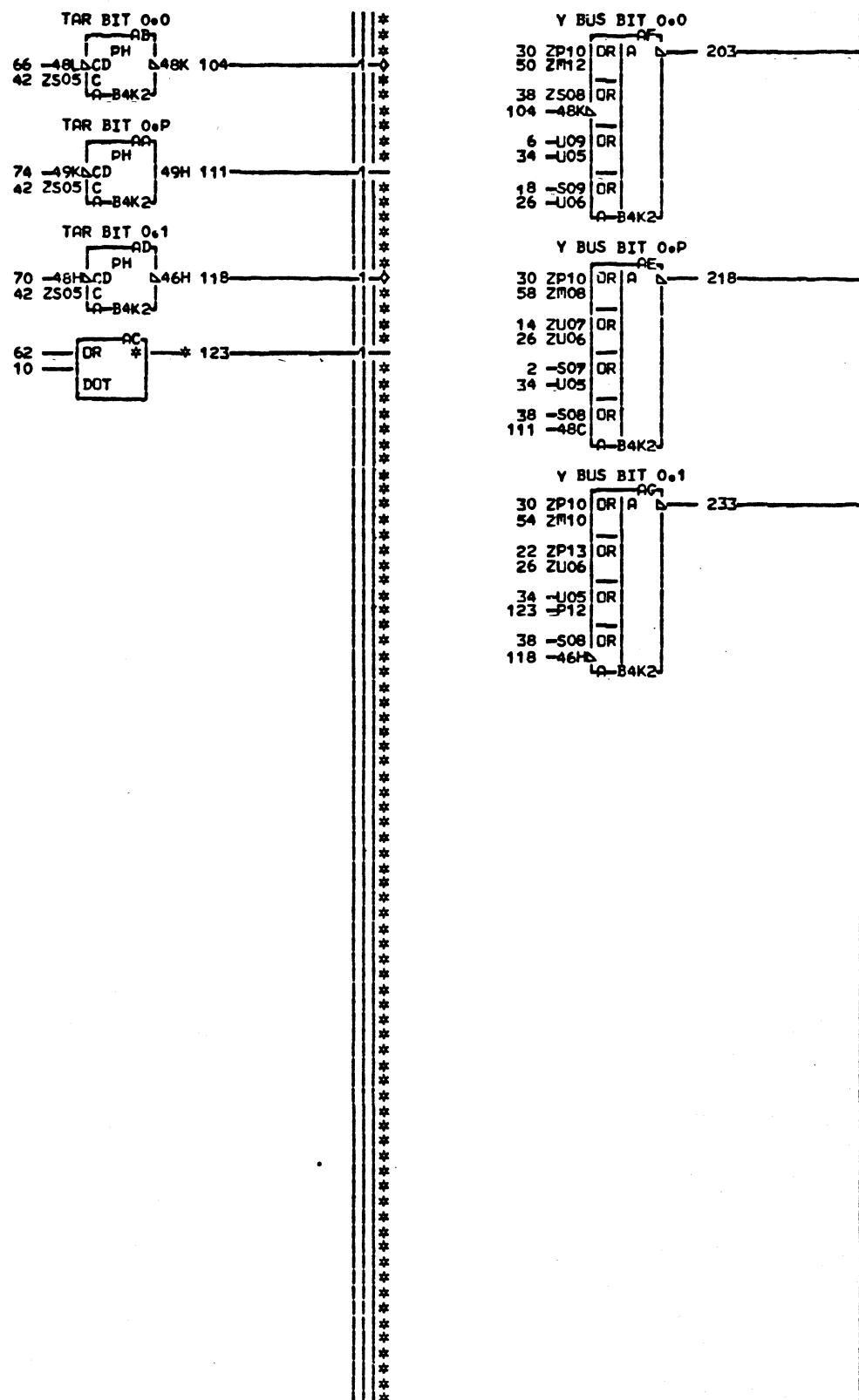
311 - ALU 0 SUM BIT 0.1 — FF2  
4DG974 4DG976 4DK974

304 - ALU 1 SUM BIT 0.1 — GM2  
4DG974 4DG976

LOC<sub>0</sub> TYPE  
A-B4K2 6801

ALU 0 AND ALU 1	
BIT 0.1	
E-Ce-HISTORY — E-MACH-3705	
FRAME	01
IBM CORP-SCD DG009	
DATE LAST EC 10-14-80 344270	
PoNo 1852682 000	

+ INBUS BYTE 0 BIT P AA001DB1\* 2  
 + INBUS BYTE 0 BIT 0 AA001DB3\* 6  
 + INBUS BYTE 0 BIT 1 AA001DB5\* 10  
 + ADBUS BIT 0.P AA003DB1\* 14  
 + ADBUS BIT 0.0 AA003DB3\* 18  
 + ADBUS BIT 0.1 AA003DB5\* 22  
 - GATE ADBUS TO Y BUS CS004CA6 26 3  
 - GATE CCU INDATA TO Y BUS CS004DB2 30 3  
 - GATE INBUS TO Y BUS CS004FG2 34 3  
 - GATE TAR TO Y BUS CS004FJ2 38 3  
 + SET TAR CS007CH2 42 3  
 + SET SAR CS007EB2 46 3  
 + CCU INDATA BIT 0.0 CU011DD4 50 1  
 + CCU INDATA BIT 0.1 CU011DE4 54 1  
 + CCU INDATA BIT 0.P CU013EF6\* 58  
 + 2ND T.P. TO INBUS BIT 0.1 CX003FM2 62  
 - Z REG BIT 0.0 DG974DB2 66  
 - Z REG BIT 0.1 DG974DB7 70  
 - Z BUS BIT 0.P DG976BA2 74

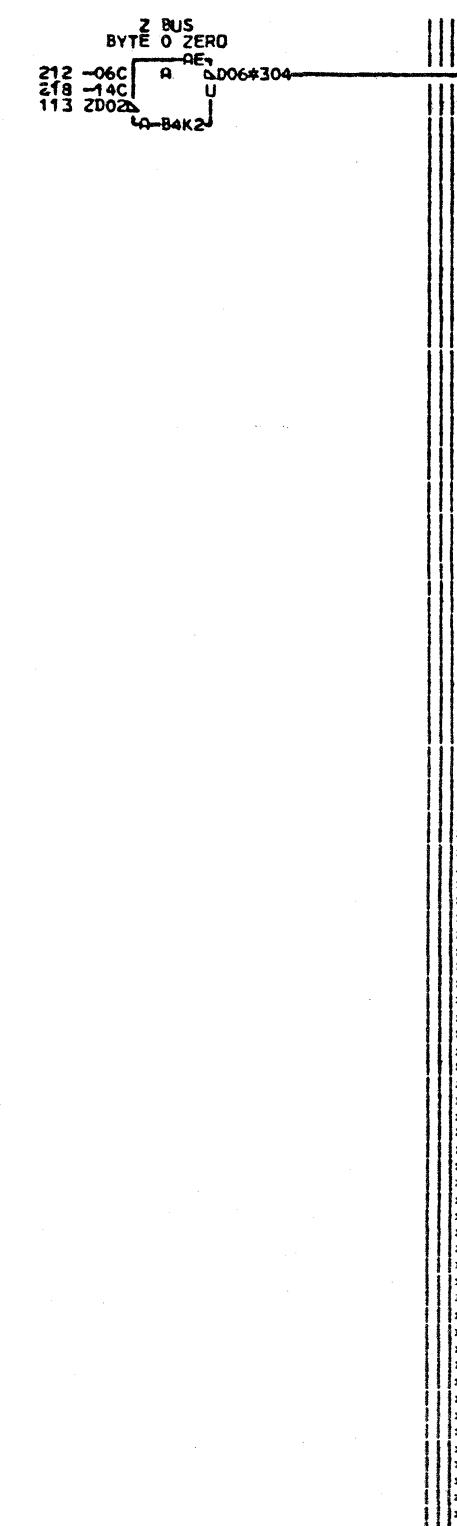
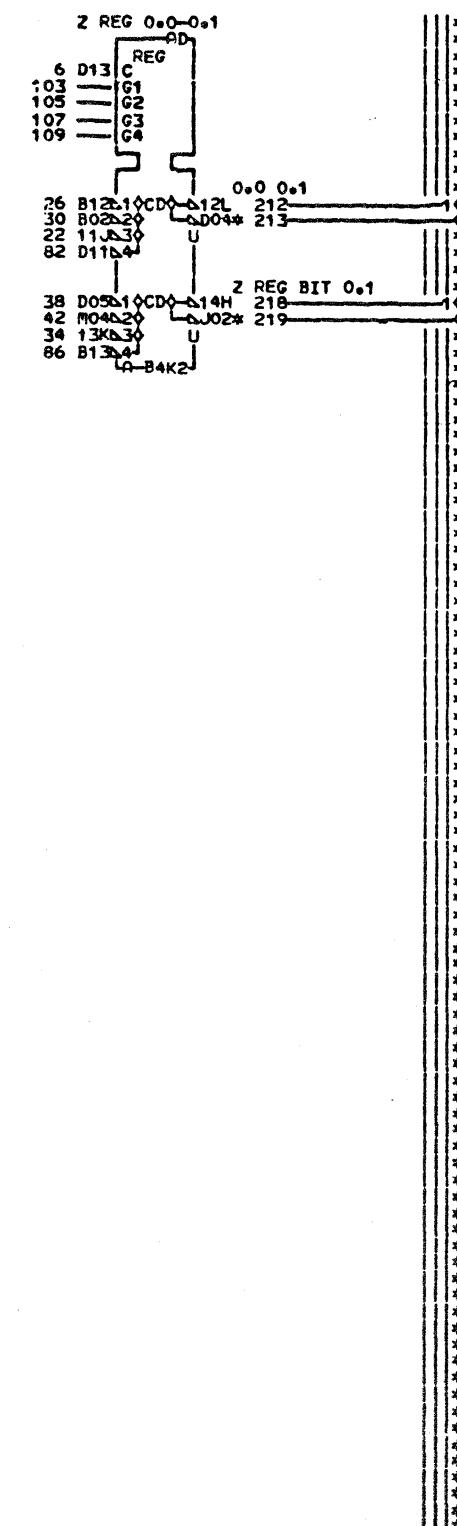
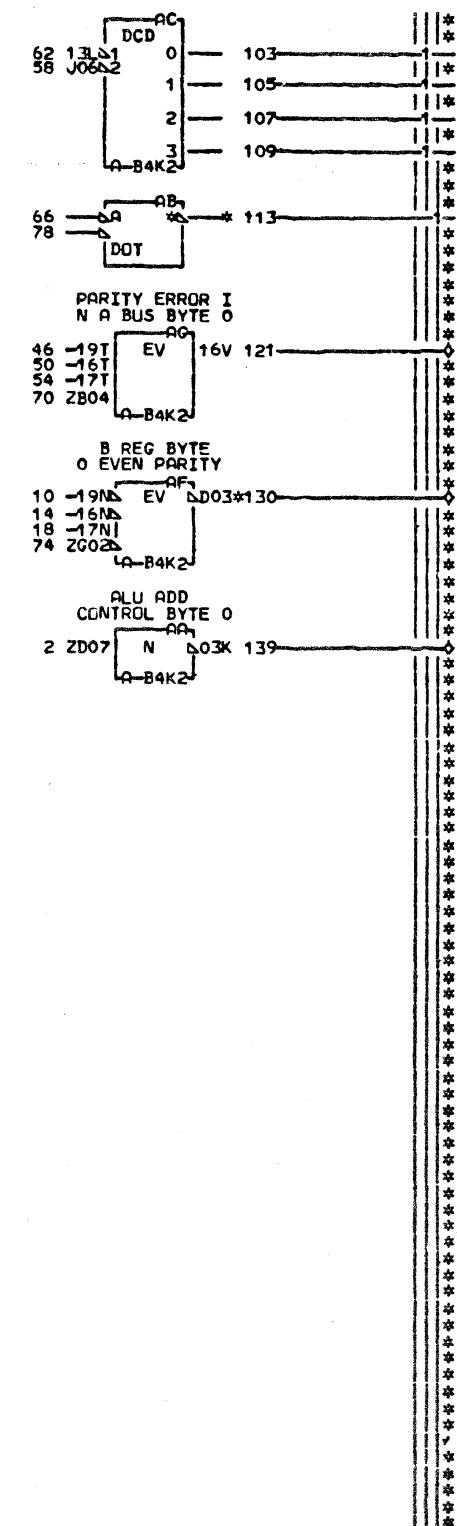
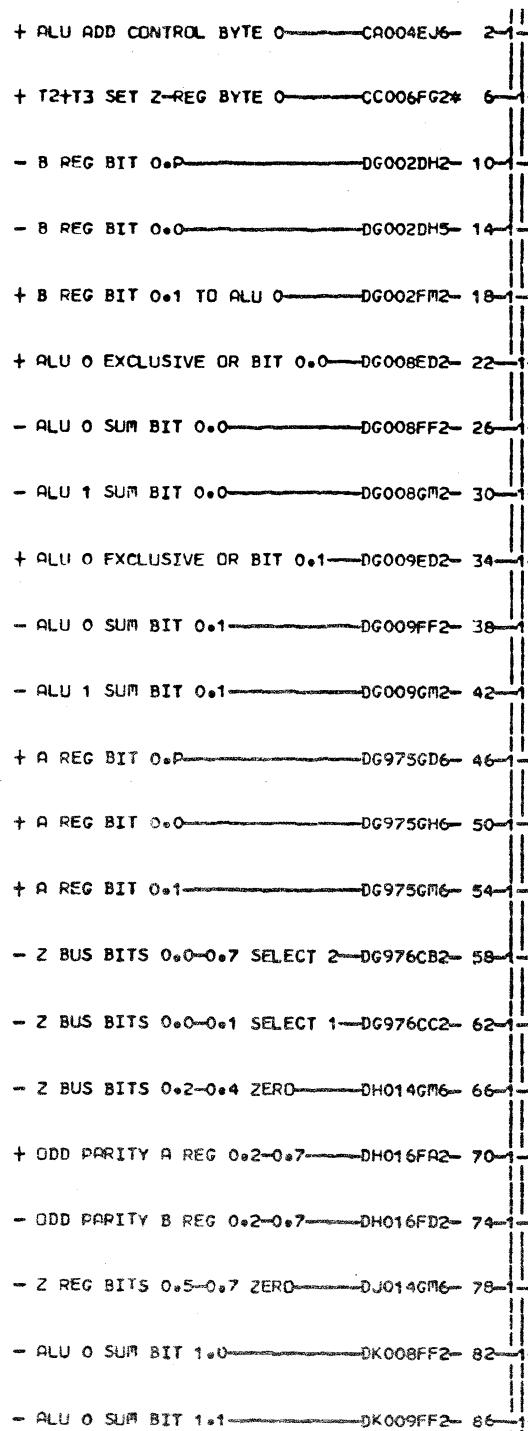


LOC. TYPE  
A-B4K2 6801

EDGE CONN.	A-B4K2P13
2 RESISTOR	58 RESISTOR
A-B4K2S07	A-B4K2R08
6 RESISTOR	123 RESISTOR
A-B4K2U09	A-B4K2P12
14 RESISTOR	01A-B4V4B05
A-B4K2U07	01A-B3Q6D04
18 RESISTOR	01A-B4Q1D13
A-B4K2S09	
22 RESISTOR	

DG971  
000

000 DG971	
104 - TAR BIT 0.0	DG003-AG6
118 - TAR BIT 0.1	DG003-AL6
218 - Y BUS BIT 0.P	DG002-DB4
203 - Y BUS BIT 0.0	DG002-DF4
233 - Y BUS BIT 0.1	DG002-DK4
311 + SAR BIT 0.P	CV001-EC2
312 - SAR BIT 0.P	DG975-EC6
304 + SAR BIT 0.0	EG2 OCM002 LCV001 LDS001 LDT001 LDU001 LDV001
305 - SAR BIT 0.0	EG6 LDG002 LDG975
325 + SAR BIT 0.1	EL2 OCM002 LCV001 LDS001 LDT001 LDU001 LDV001
326 - SAR BIT 0.1	EL6 LDG002 LDG975
SAR TAR AND Y BUS ASSEMBLER	
BITS 0.P-0.1	
-E.C.-HISTORY-	E MACH.3705
344270	
FRAME 01	
DATE LAST EC	IBM CORP.SCD DG971
06-02-81 344828	P.N. 1852883 000



EDGE CONN.	01A-B3E6A02
6 RESISTOR	304 RESISTOR
A-B4K2D13	A-B3G2J06
113 RESISTOR	01A-B4C1E13
A-B4K2D02	01A-B3C6E04
130 A-B4J6E02	
019 A-B3J1E11	
213 A-B4D1E13	
01A-B3D6E04	
219 A-B4E1A11	

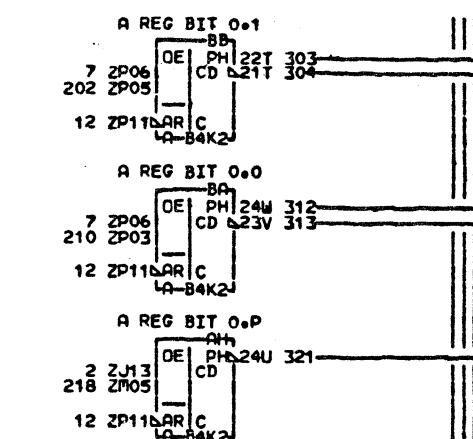
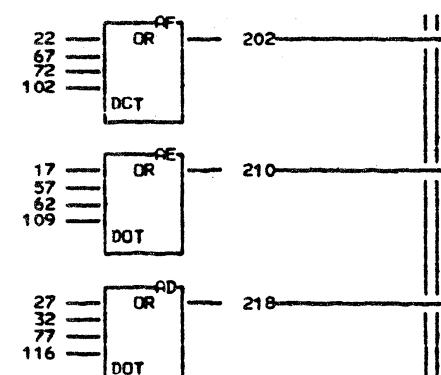
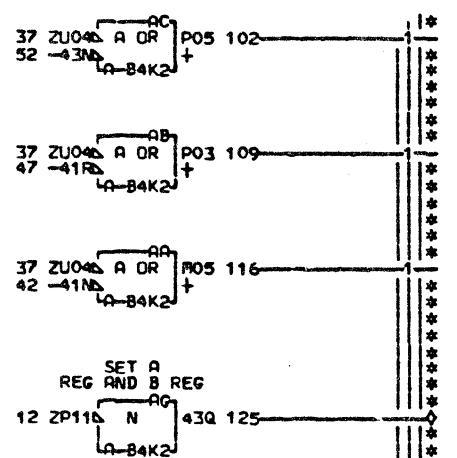
LOC. TYP  
A-B3G2 Y70  
A-B4K2 680

DC974

00

ALU 0 ALU 1 AND 2 REG  
 BITS 0-P-01  
 E-E-C-H-I-S-T-R-Y E-F-A-C-H-3705  
 DATE LAST EC  
 10-14-80 344270 P-N<sub>o</sub> 1852884 000

+ TIE UP AU001GF4- 2  
 - COMPLEMENT A BUS CA004DD2- 7  
 - TO+T1 TIME SET A-B REGS CC007HK4- 12-1-3  
 + FORCE A BUS BIT 0.0 CF002DC2- 17  
 + FORCE A BUS BIT 0.1 CF002DD2- 22  
 + FORCE A BUS BIT 0.P CF002FF6- 27  
 + SDR BIT 0.P TO A BUS CF003GM2- 32  
 - GATE SAR TO A BUS CS004BK6- 37-3  
 - SAR BIT 0.P DG971EC6- 42-1  
 - SAR BIT 0.0 DG971EG6- 47-1  
 - SAR BIT 0.1 DG971EL6- 52-1  
 + SHIFT RIGHT BIT 0.0 TO A BUS-DN003BB2- 57  
 + SDR BIT 0.0 TO A BUS-DN003BC2- 62  
 + SHIFT RIGHT BIT 0.1 TO A BUS-DN003CD2- 67  
 + SDR BIT 0.1 TO A BUS-DN003CE2- 72  
 + BIT 0.P TO A BUS DP994CL2- 77



000 DG975  
125 + SET A REG AND B REG DG002-EA2

321 + A REG BIT 0.P DG6  
4DG974 4DG976

312 - A REG BIT 0.0 DG977-GH2

313 + A REG BIT 0.0 DG6  
4DG008 4DG974

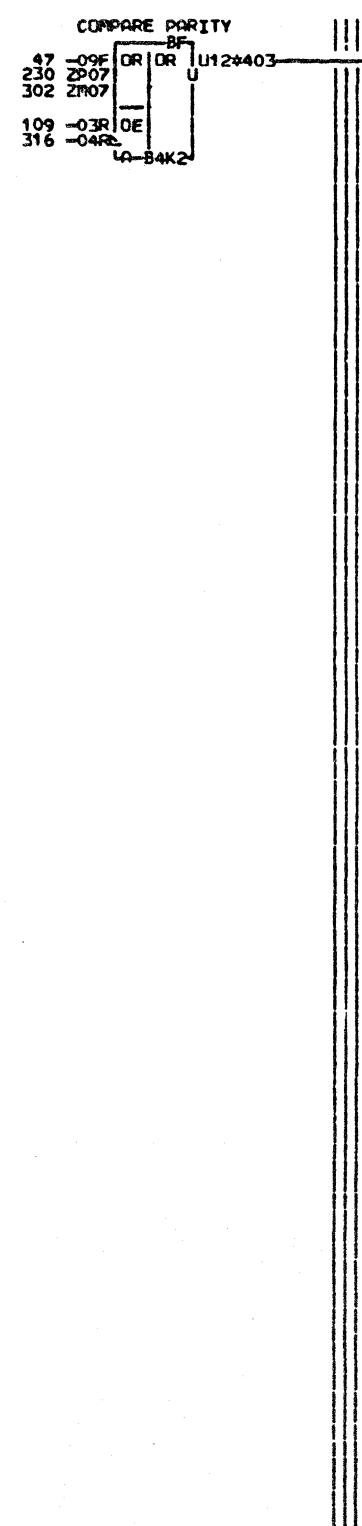
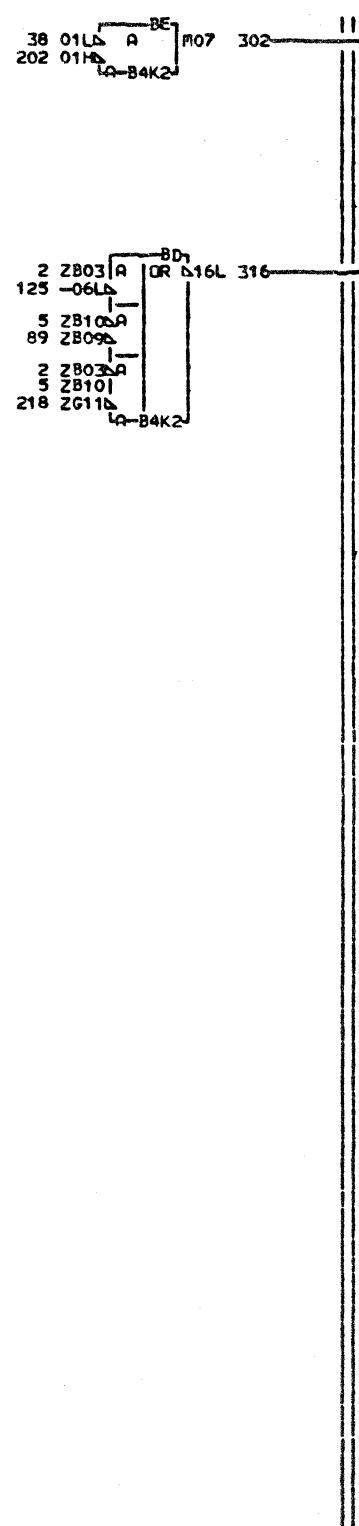
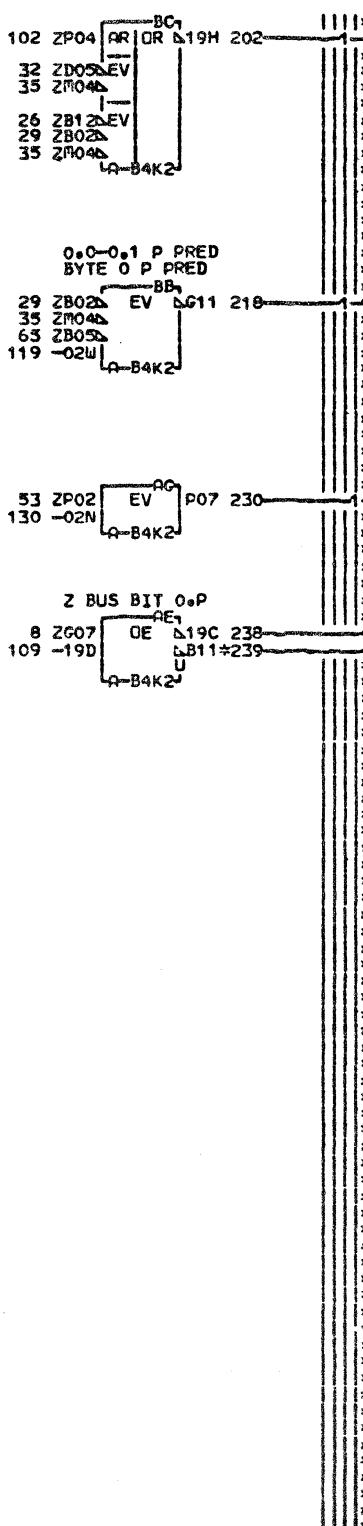
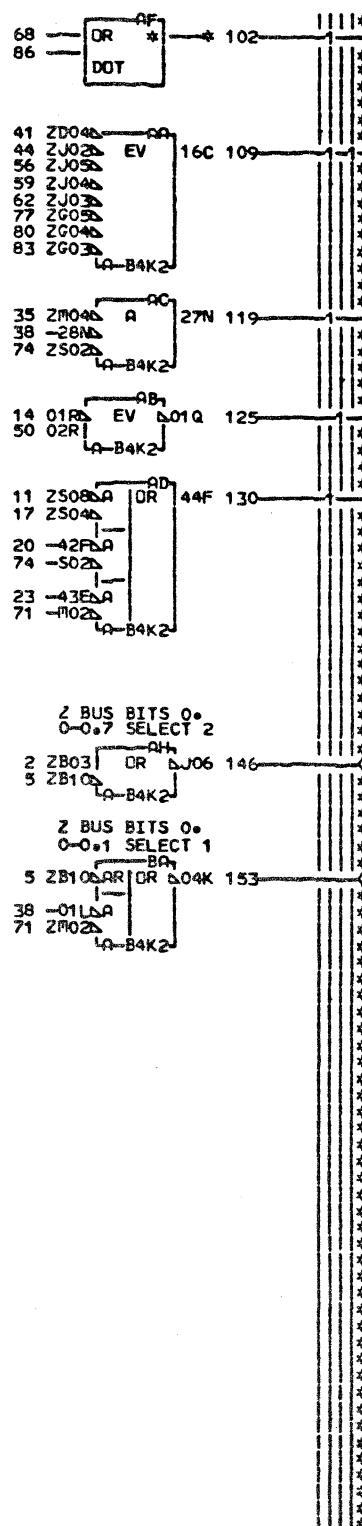
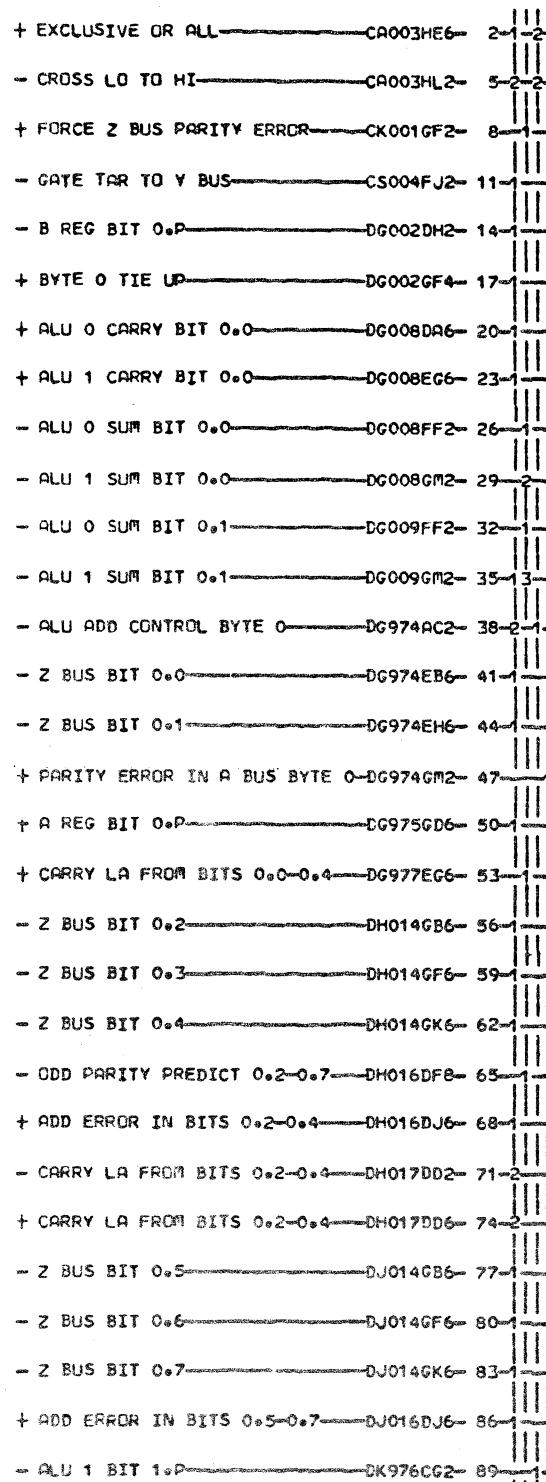
303 - A REG BIT 0.1 DG977-GM2

304 + A REG BIT 0.1 GM6  
4DG009 4DG974

LOC. TYPE  
A-B4K2 6801

A BUS ASSEMBLER	BITS 0.P-0.1	E-C-HISTORY	MACH-3705
FRAME	01	IBM CORP-SCD	DG975
DATE LAST EC	10-14-80 344270	PoNo. 1652685	000

DG975  
000



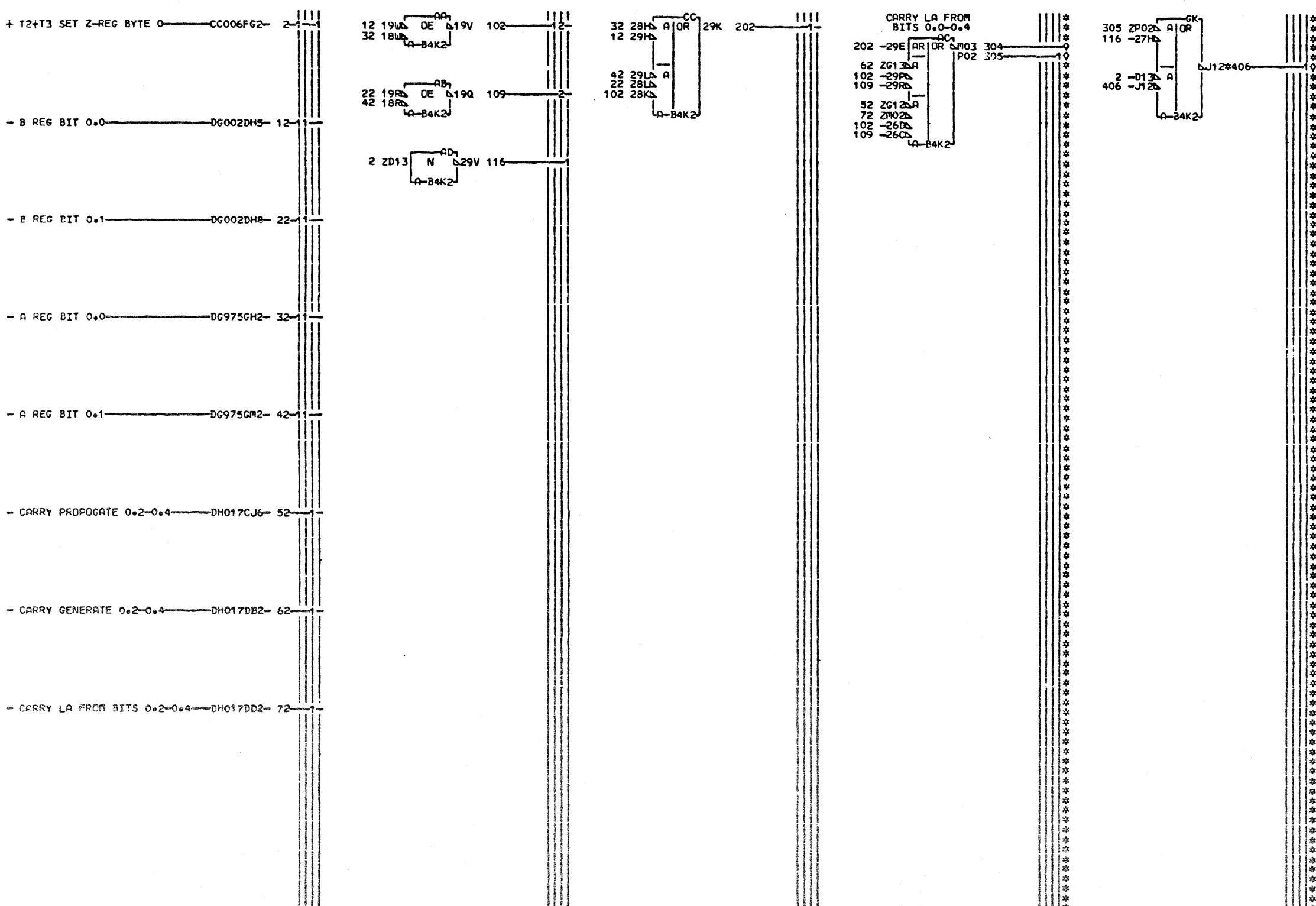
36976

300

EDGE CONN.  
 102 RESISTOR  
 R-B4K2P04  
 239 R-B4D1E11  
 01A-B3D6E02  
 403 RESISTOR  
 R-B3N2609  
 01D-B4K6B04  
 01A-B3X1B13

LOC. TY  
A-B3N2 68  
A-B4K2 68

ALU CHECK  
BITS 0-P-01  
E-C-HISTORY E MACH-3705  
DATE LAST EC  
10-14-80 344270



EDGE CONN.  
406 A-B4K1B11  
01A-B3K6802

LOC. TYPE  
A-B4K2 6801

DG977  
000

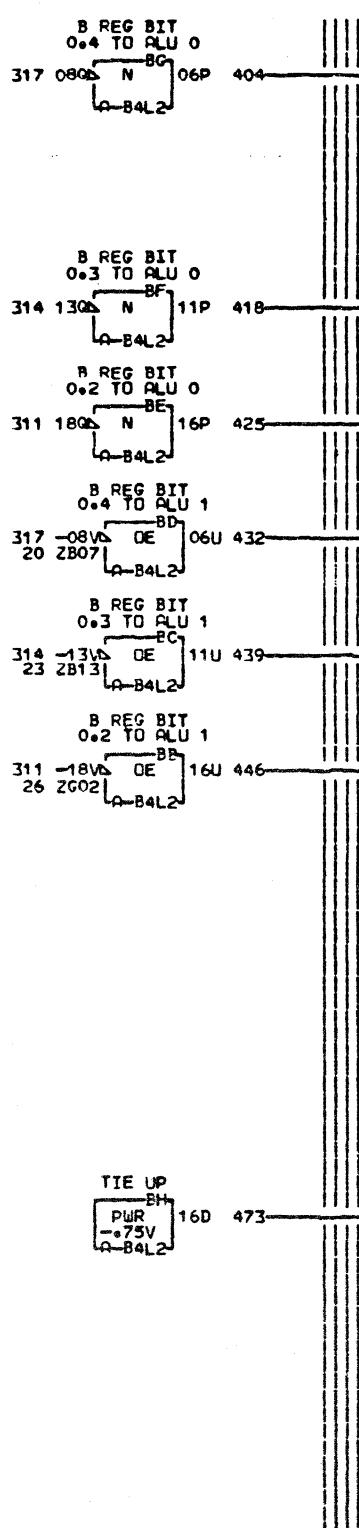
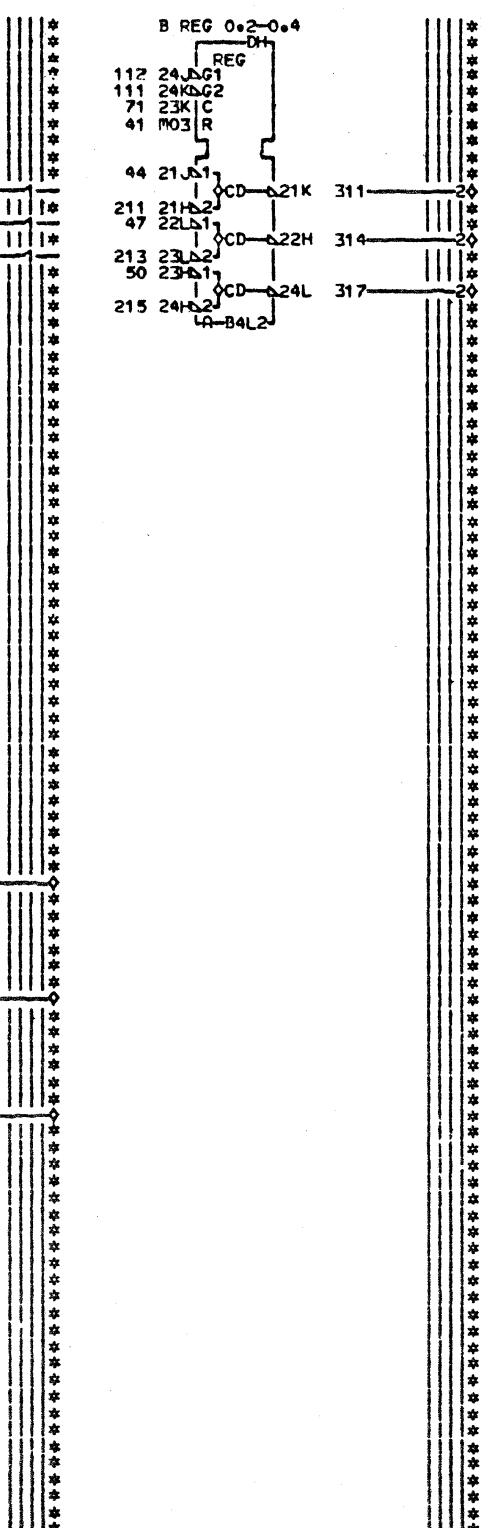
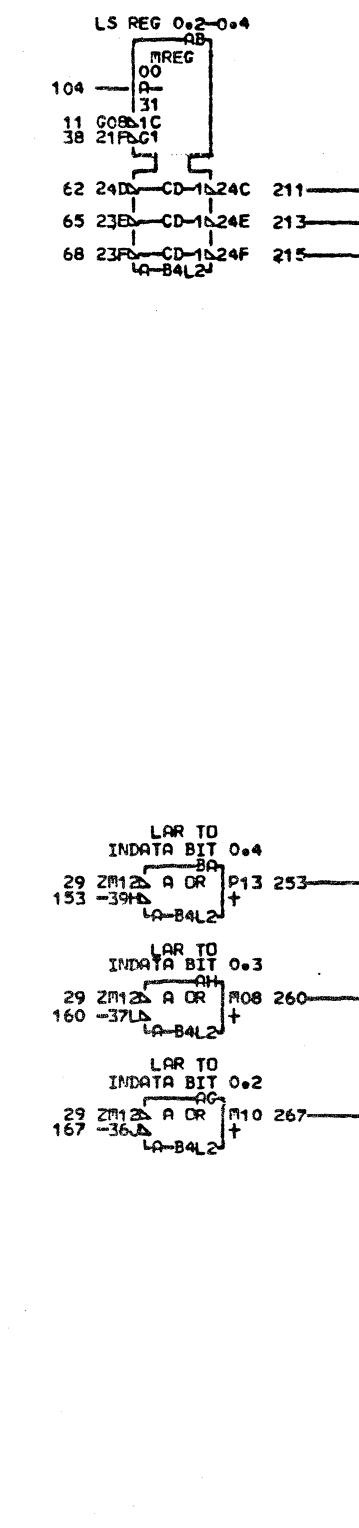
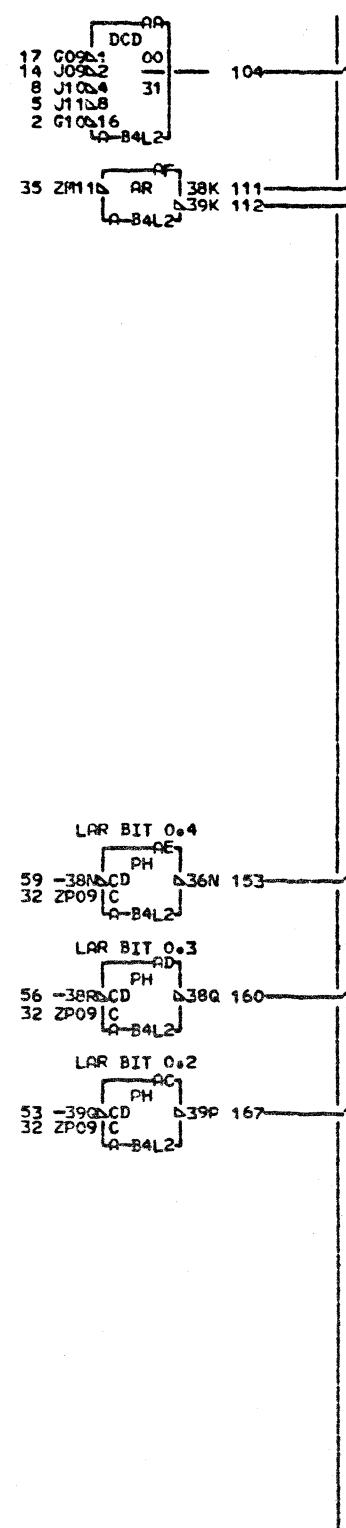
000 DG977  
304 - CARRY LA FROM BITS 0.0-0.4 E62  
LDF976 LDF977

305 + CARRY LA FROM BITS 0.0-0.4 E66  
LDF976 DG976

406 + 0.0 CARRY HOLDOVER  
LCZ002 LCZ003 GK6

5-BIT CARRY LOOKAHEAD	
BITS 0.P-0.1	
EeCs-HISTORY E MACH 3705	
FRAME	01
DATE LAST EC	
10-14-80 344270	
IBM CORP-SCD DG977	
P.N. 1852887 000	

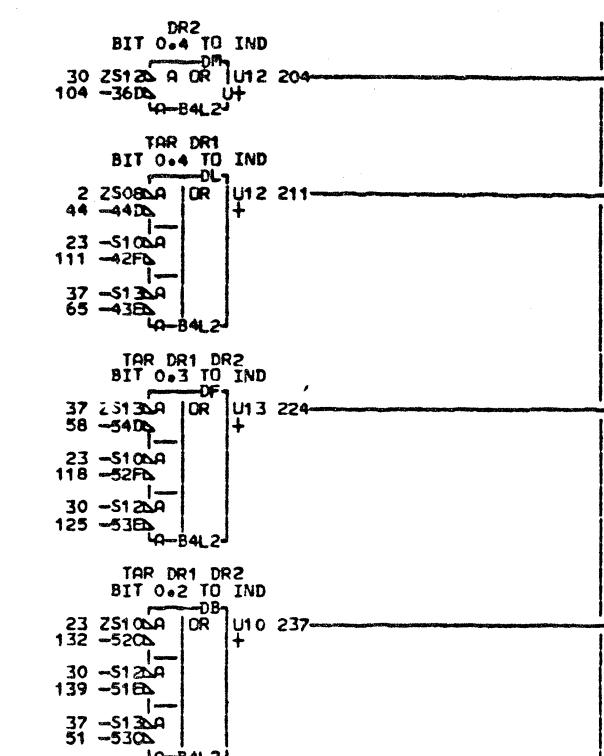
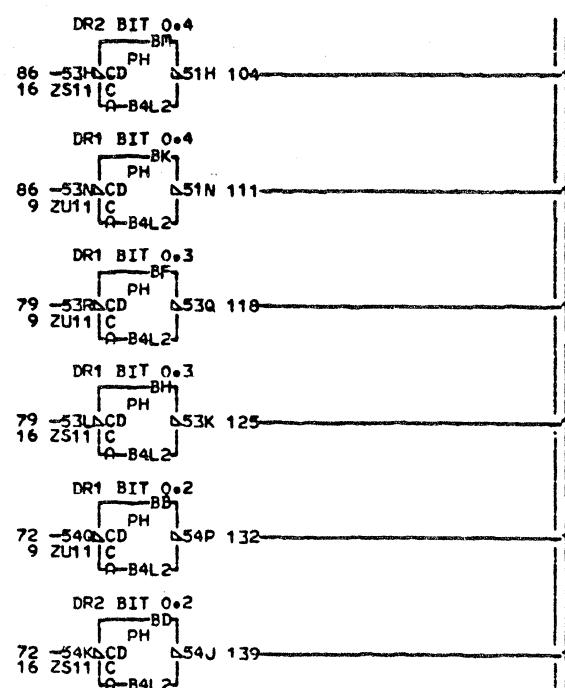
-	SELECT LS REG GROUP 1+2	CC006AU4	2-1
-	SELECT LS REG GROUP 1+3	CC006AV4	5-1
-	SELECT LS REG BIT 0+1+2+3	CC006AW4	8-1
-	WRITE LS	CC006BJ4	11-
-	SELECT LS REG 0+1+4+5	CC006BK4	14-
-	SELECT LS REG BIT 0+2+4+6	CC006BL4	17-
+	FORCE ERROR IN BIT 4	CK002DD2	20-
+	FORCE ERROR IN BIT 3	CK002DH2	23-
+	FORCE ERROR IN BIT 2	CK002DJ2	26-
-	GATE INPUT 74	CQ004FJ6	29-3
+	SET LAR	CS001DM2	32-3
-	GATE Y BUS TO B REG	CS004ED2	35-1
-	SELECT FLOAT	DH002002	38-1
-	FLOAT	DH002003	41-
-	Y BUS BIT 0+2	DH011DC4	44-
-	Y BUS BIT 0+3	DH011DH4	47-
-	Y BUS BIT 0+4	DH011DL4	50-
-	SAR BIT 0+2	DH011EC6	53-1
-	SAR BIT 0+3	DH011EH6	56-1
-	SAR BIT 0+4	DH011EK6	59-1
-	Z REG BIT 0+2	DH014DB2	62-
-	Z REG BIT 0+3	DH014DB7	65-
-	Z REG BIT 0+4	DH014FJ2	68-
+	SET A REG AND B REG	DH015EA2	71-



LDC. TYPE  
Q-34L2 6802

B REG LAR AND LOCAL STORE  
BITS 0..2 0..3 0..4  
E.C. HISTORY E.MACH.3705

- GATE TAR TO Y BUS CS004FJ2- 2  
 + SET DR1 CS007FC6- 9-3  
 + SET DR2 CS007FD6- 16-3  
 - GATE DISP REG 1 TO IND CU001EK6- 23-3  
 - GATE DISPL REG 2 TO IND CU001EL6- 30-3  
 - GATE TAR TO IND CU001EM6- 37-3  
 + TIE UP DH002GF4- 44-  
 - TAR BIT 0.2 DH011AC6- 51-  
 - TAR BIT 0.3 DH011AG6- 58-  
 - TAR BIT 0.4 DH011AM6- 65-  
 - Z REG BIT 0.2 DH014DB2- 72-2  
 - Z REG BIT 0.3 DH014DB7- 79-2  
 - Z PEG BIT 0.4 DH014FJ2- 86-2



000 DH003  
 237 + TAR DR1 DR2 BIT 0.2 TO IND DB2  
 4AP012

224 + TAR DR1 DR2 BIT 0.3 TO IND DF2  
 4AP012

211 + TAR DR1 BIT 0.4 TO IND AP013-DL2

204 + DR2 BIT 0.4 TO IND AP013-DM2

LOC. TYPE  
 A-B4L2 6802

CCU DISPLAY REGISTER 1 AND 2	
BITS 0.2 0.3 AND 0.4	
E.C.-HISTORY E-MACH-3705	
FRAME	01
DATE	LAST EC
10-14-80	344270
IBM CORP-SCD	DH003
P.No 1852889 000	

DH003  
 000

+ ALU AND CONTROL BYTE 0 — CA004EB6— 2-2

+ ALU OR CONTROL BYTE 0 — CA004EF6— 12-2

+ ALU AND CONTROL BYTE 0 — CA004EJ6— 22-2

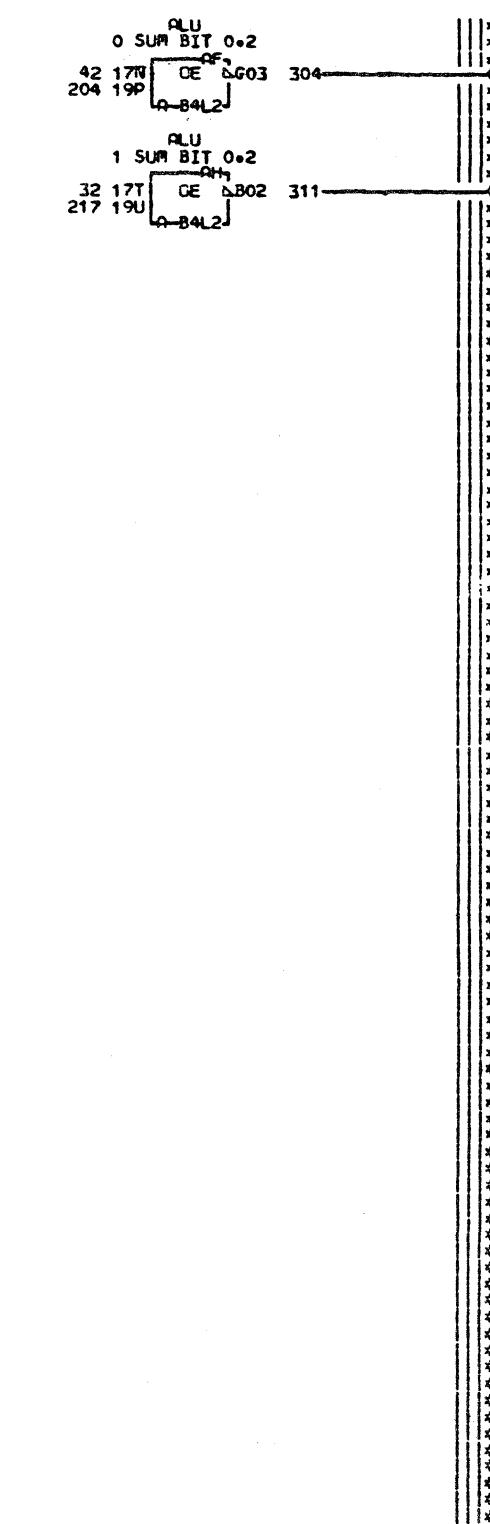
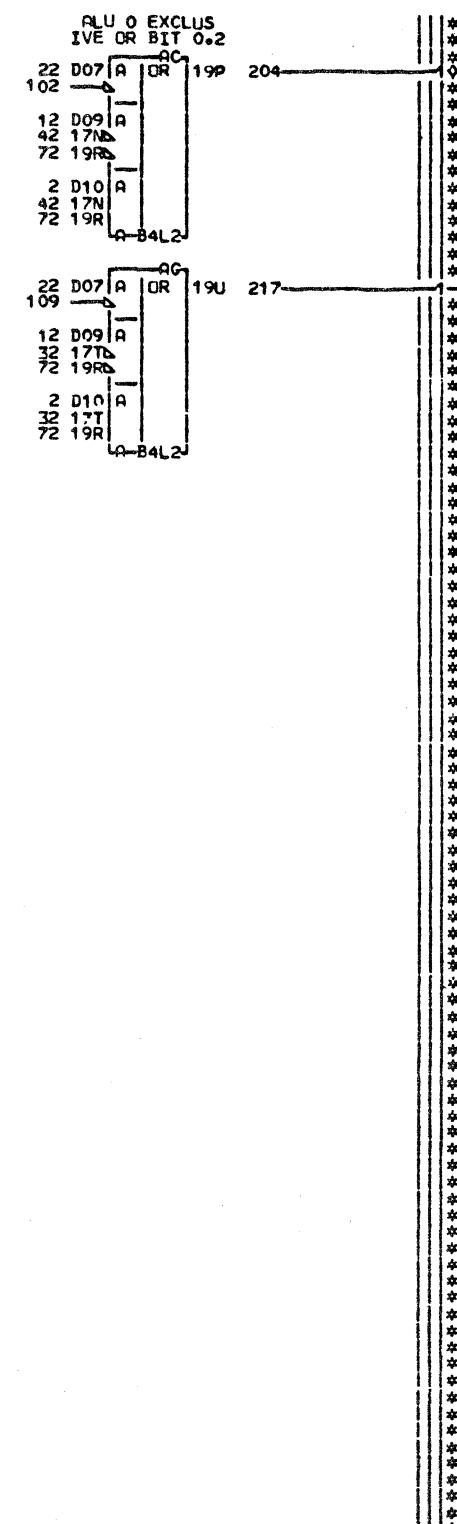
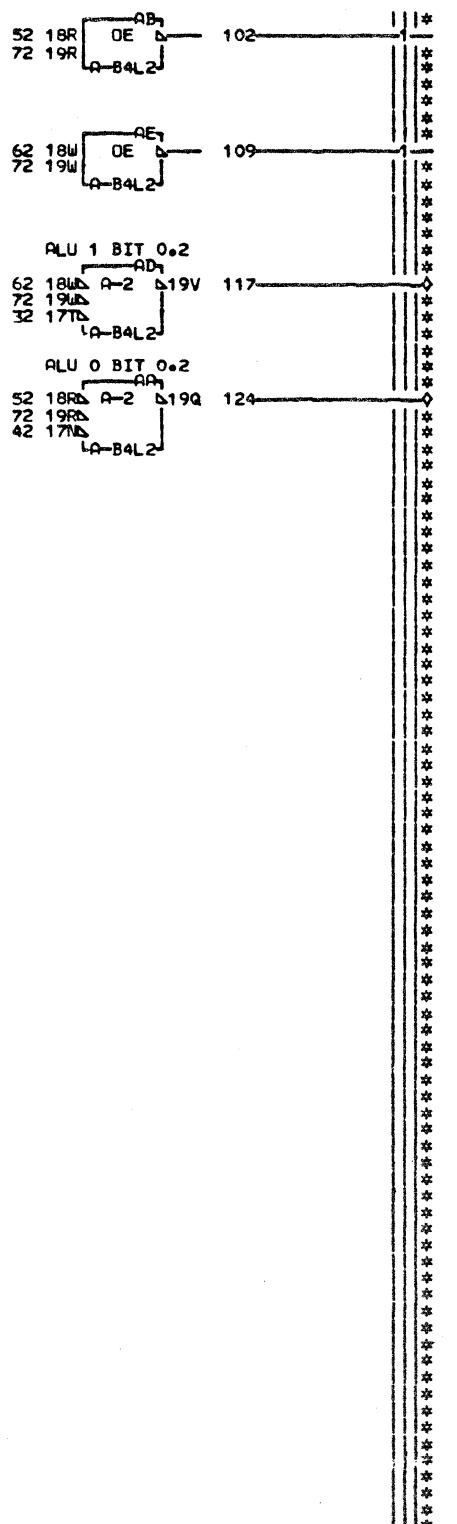
+ B REG BIT 0.2 TO ALU 1 — DH002EJ2— 32-21

+ B REG BIT 0.2 TO ALU 0 — DH002FJ2— 42-21

+ ALU 0 CARRY BIT 0.3 — DH009DA6— 52-2

+ ALU 1 CARRY BIT 0.3 — DH009EG6— 62-2

+ A REG BIT 0.2 — DH015FD6— 72-24



LBC. TYPE  
A-B4L2 6802

ALU 0 AND ALU 1 BIT 0.2	E.C.—HISTORY—	E-MACH-3705
FRAME 01		
DATE LAST EC 10-14-80 344270	IBM CORP-SCD	DH008
PoN. 1852890		000

DH008  
000

+ ALU AND CONTROL BYTE 0 — CA004EB6- 2-2

+ ALU OR CONTROL BYTE 0 — CA004EF6- 12-2

+ ALU ADD CONTROL BYTE 0 — CA004EJ6- 22-2

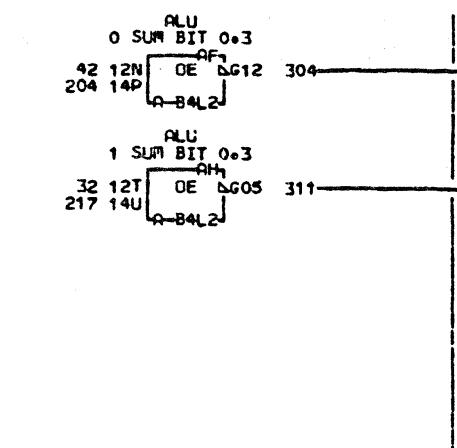
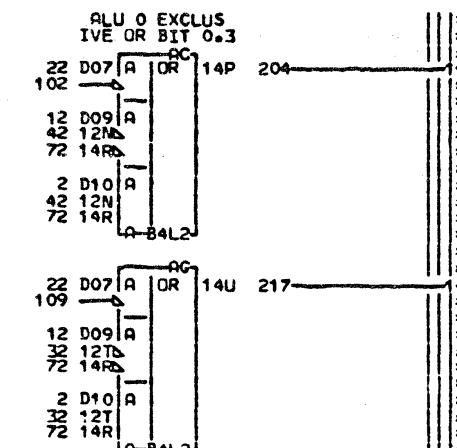
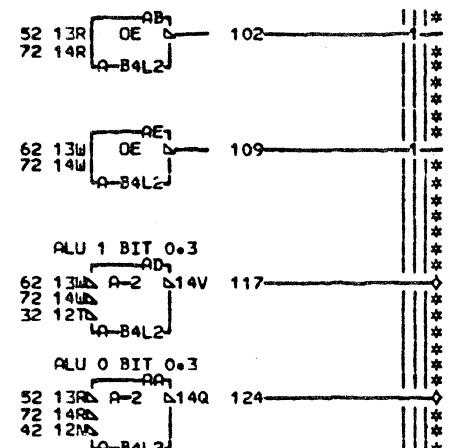
+ B REG BIT 0.3 TO ALU 1 — DH002EL2- 32-121

+ B REG BIT 0.3 TO ALU 0 — DH002FL2- 42-121

+ ALU 0 CARRY BIT 0.4 — DH010DA6- 52-2

+ ALU 1 CARRY BIT 0.4 — DH010EG6- 62-2

+ A REG BIT 0.3 — DH015FH6- 72-44



000 DH009  
124 + ALU 0 CARRY BIT 0.3 — DH008-DA6

204 + ALU 0 EXCLUSIVE OR BIT 0.3 — ED2  
LDH014

117 + ALU 1 CARRY BIT 0.3 — DH008-EG6

304 - ALU 0 SUM BIT 0.3 — FF2  
LDH014 LDH016 LDLO04

311 - ALU 1 SUM BIT 0.3 — GM2  
LDH014 LDH016

LOC. TYPE  
A-B4L2 6802

ALU 0 AND ALU 1	—	MACH. 3705
BIT 0.3	—	FRAME 01
E-C. HISTORY	—	IBM CORP. SCD DH009
DATE 10-14-80	LAST EC 344270	P.N. 1852891 000

+ ALU AND CONTROL BYTE 0 — CR004EB6— 2-2

+ ALU OR CONTROL BYTE 0 — CR004EF6— 12-2

+ ALU ADD CONTROL BYTE 0 — CR004EJ6— 22-2

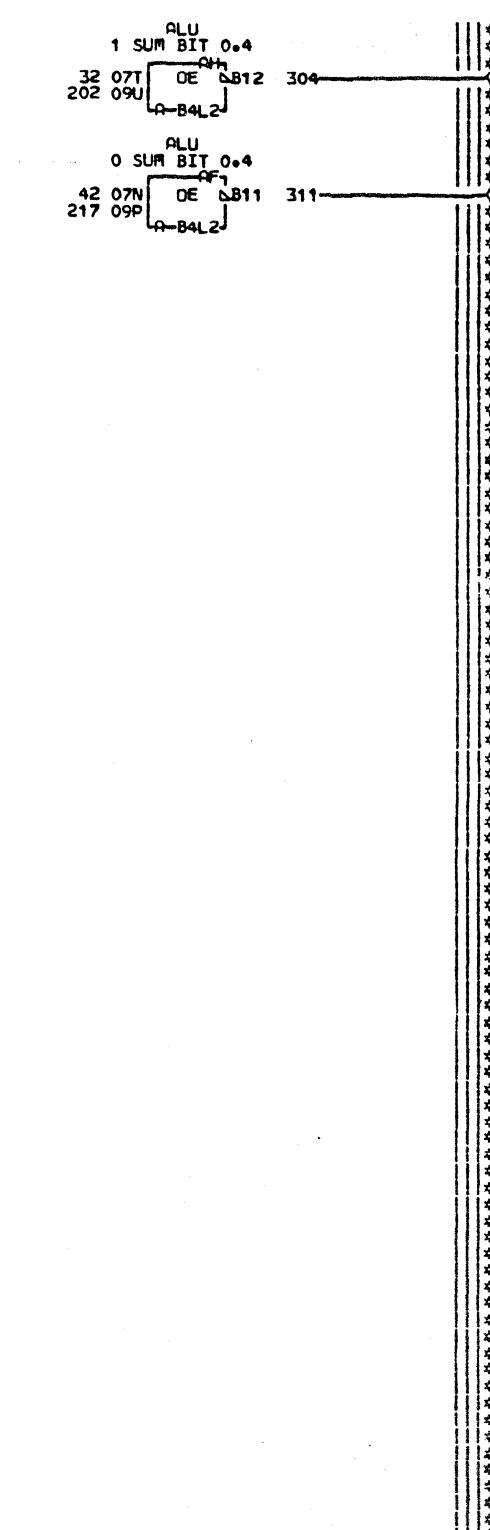
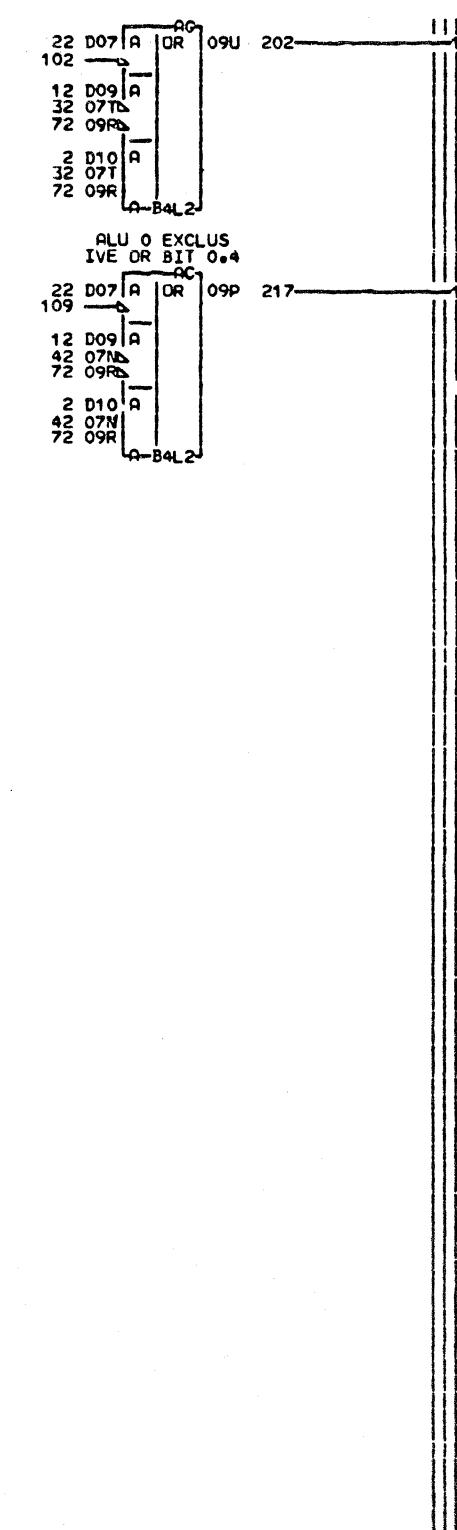
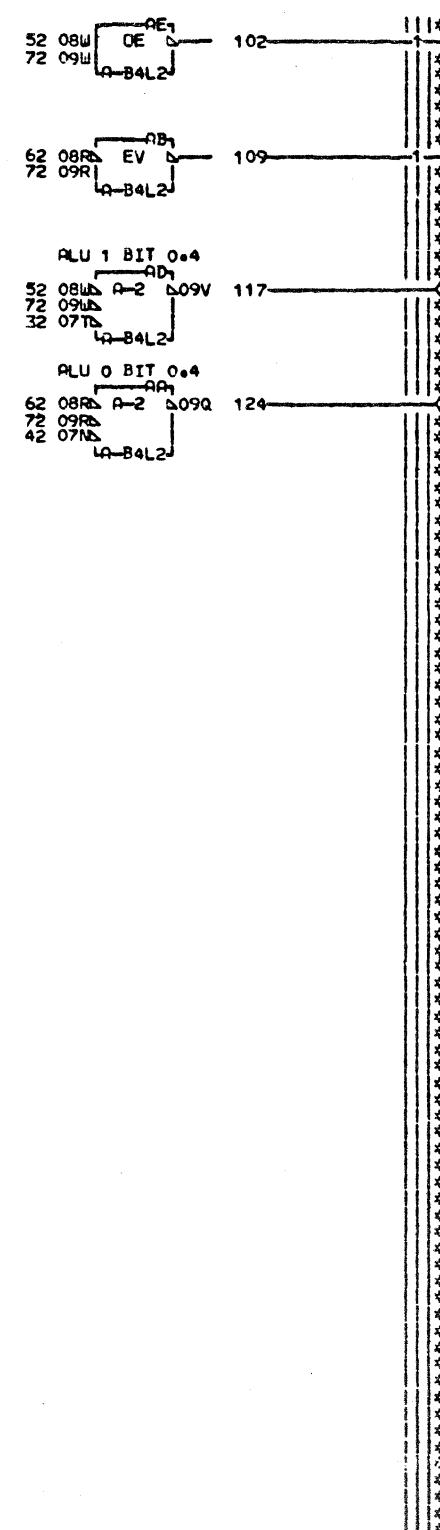
+ B REG BIT 0.4 TO ALU 1 — DH002EN2— 32-121

+ B REG BIT 0.4 TO ALU 0 — DH002FN2— 42-121

+ TIE UP — DH002GF4— 52-2

- FLOAT — DH010001— 62-2

+ A REG BIT 0.4 — DH015FM6— 72-44



000 DH010  
124 + ALU 0 CARRY BIT 0.4 — DH009-DG6

217 + ALU 0 EXCLUSIVE OR BIT 0.4 — ED2  
LDH014

117 + ALU 1 CARRY BIT 0.4 — DH009-EG6

311 - ALU 0 SUM BIT 0.4 — FF2  
LDH014 LDH016 LDL004

304 - ALU 1 SUM BIT 0.4 — GM2  
LDH014 LDH016

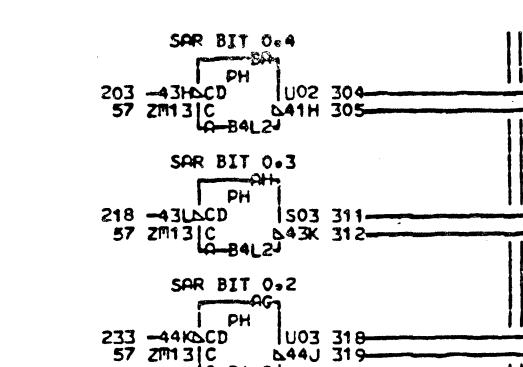
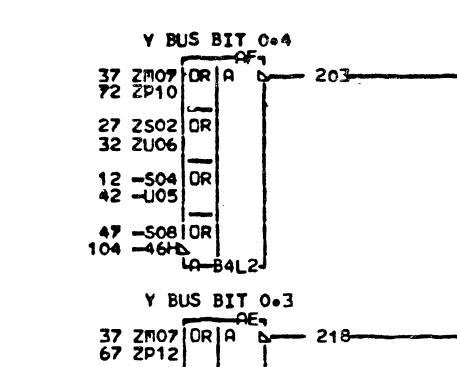
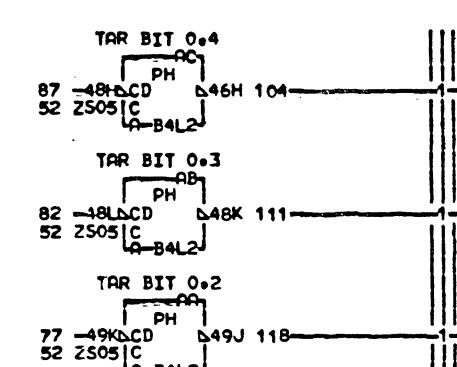
DH010  
000

LGC<sub>0</sub> TYPE  
A-B4L2 6802

ALU 0 AND ALU 1	
BIT 0.4	
E-C-HISTORY — E-RACH-3705	
FRAME	01
DATE	LAST EC
10-14-80	344270
IBM CORP-SCD	DH010
PoNo 1852892	000

+ INBUS BYTE 0 BIT 2 AA001DB7\* 2  
 + INBUS BYTE 0 BIT 3 AA001DC2\* 7  
 + INBUS BYTE 0 BIT 4 AA001DC4\* 12  
 + ADBUS BIT 0.2 AA003DB7\* 17  
 + ADBUS BIT 0.3 AA003DC2\* 22  
 + ADBUS BIT 0.4 AA003DC4\* 27  
 - GATE ADBUS TO Y BUS CS004CP6 32  
 - GATE CCU INDATA TO Y BUS CS004DB2 37  
 - GATE INBUS TO Y BUS CS004FG2 42  
 - GATE TAR TO Y BUS CS004FJ2 47  
 + SET TAR CS007CH2 52  
 + SET SAR CS007EB2 57  
 + CCU INDATA BIT 0.2 CU011DG4 62  
 + CCU INDATA BIT 0.3 CU011DJ4 67  
 + CCU INDATA BIT 0.4 CU011DK4 72  
 - Z REG BIT 0.2 DH014DB2 77  
 - Z REG BIT 0.3 DH014DB7 82  
 - Z REG BIT 0.4 DH014FJ2 87

EDGE CONN.  
 2 RESISTOR A-B4L2S09  
 A-B4L2U09  
 7 RESISTOR A-B4L2U07  
 12 RESISTOR A-B4L2S04  
 17 RESISTOR A-B4L2S07  
 22 RESISTOR



000 DH011  
118 - TAR BIT 0.2 DH003-AG6

111 - TAR BIT 0.3 DH003-AG6

104 - TAR BIT 0.4 DH003-AM6

233 - Y BUS BIT 0.2 DH002-DC4

218 - Y BUS BIT 0.3 DH002-DH4

203 - Y BUS BIT 0.4 DH002-DL4

318 + SAR BIT 0.2 EC2  
OCV001 LDS001 LDT001 LDU001  
LDV001

319 - SAR BIT 0.2 EC6  
LDH002 LDH015

311 + SAR BIT 0.3 EH2  
OCV001 LDS001 LDT001 LDU001  
LDV001

312 -- SAR BIT 0.3 EH6  
LDH002 LDH015

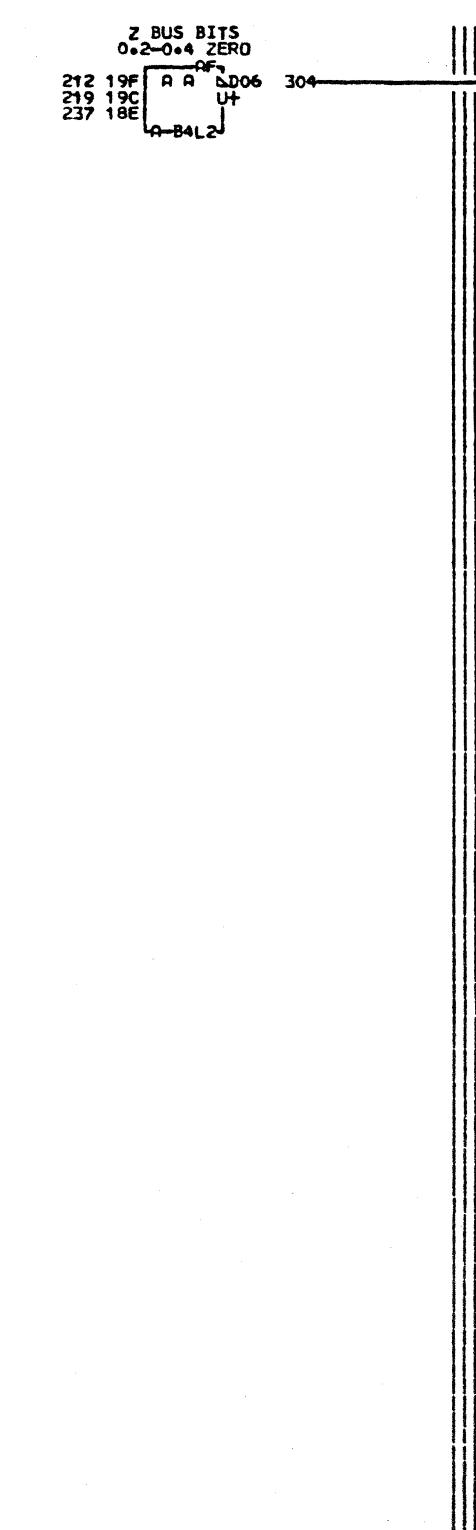
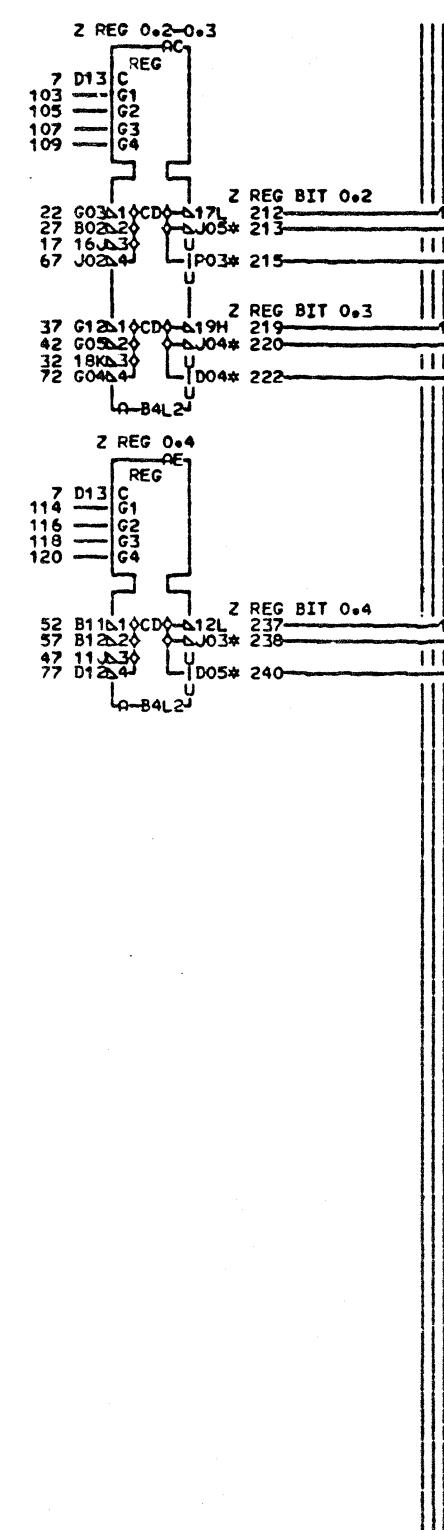
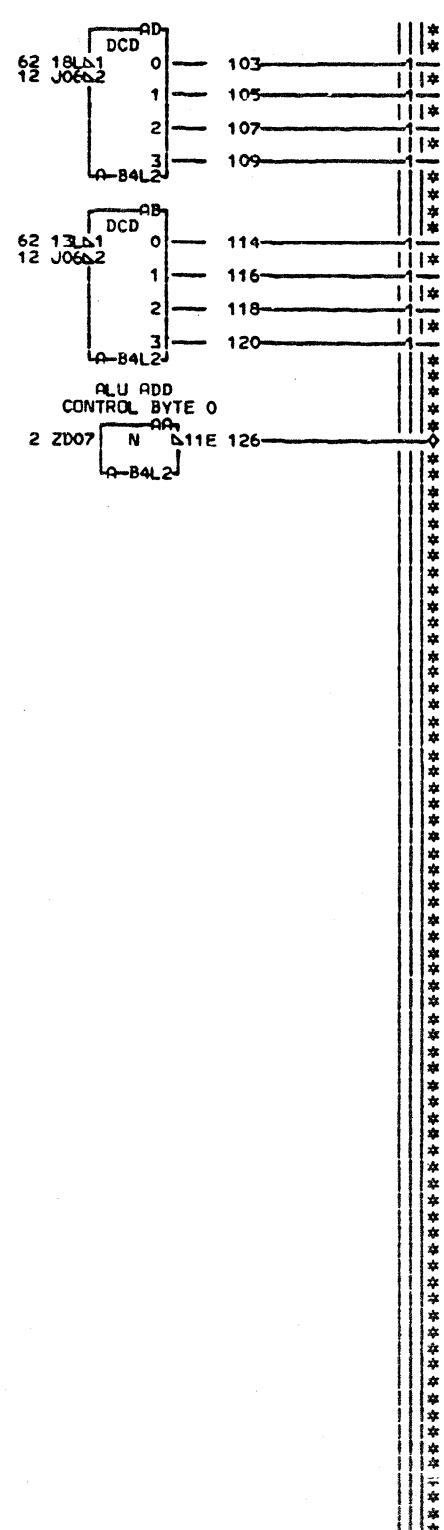
304 + SAR BIT 0.4 EK2  
OCV001 LDS001 LDT001 LDU001  
LDV001

305 - SAR BIT 0.4 EK6  
LDH002 LDH015

LOC. TYPE  
A-B4L2 6802

SAR TAR AND Y BUS ASSEMBLERS	
BITS 0.2-0.4	
E.C.-HISTORY—E.MACH-3705	
344270	
FRAME 01	
DATE 06-02-81	LAST EC 344828
IBM CORP./SCD	DH011
P.N. 1852893	000

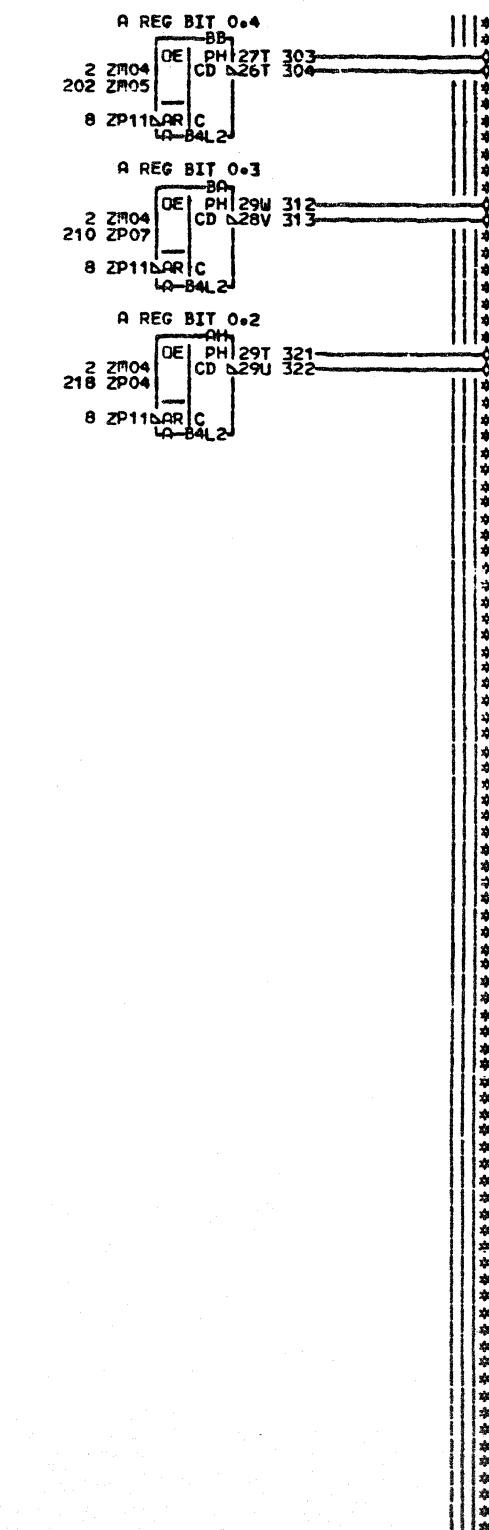
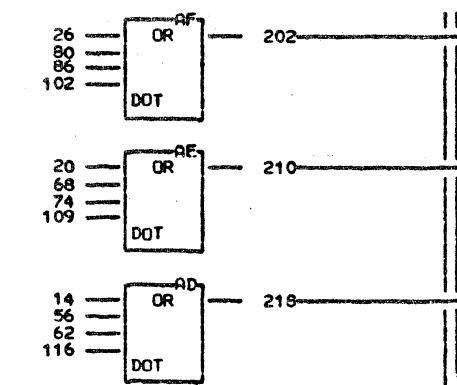
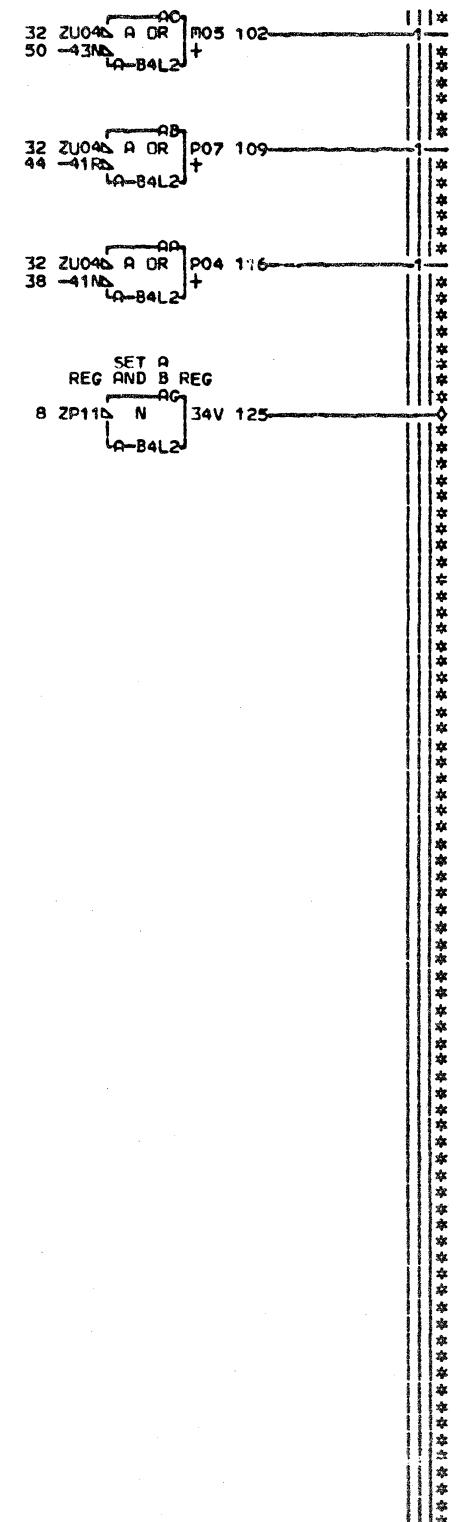
+ ALU ADD CONTROL BYTE 0—CR004EJ6— 2  
 + T2+T3 SET Z-REG BYTE 0—CC006FG2— 7  
 - Z BUS BITS 0.0-0.7 SELECT 2—DG976CB2— 12  
 + ALU 0 EXCLUSIVE OR BIT 0.2—DH008ED2— 17  
 - ALU 0 SUM BIT 0.2—DH008FF2— 22  
 - ALU 1 SUM BIT 0.2—DH008GM2— 27  
 + ALU 0 EXCLUSIVE OR BIT 0.3—DH009ED2— 32  
 - ALU 0 SUM BIT 0.3—DH009FF2— 37  
 - ALU 1 SUM BIT 0.3—DH009GM2— 42  
 + ALU 0 EXCLUSIVE OR BIT 0.4—DH010ED2— 47  
 - ALU 0 SUM BIT 0.4—DH010FF2— 52  
 - ALU 1 SUM BIT 0.4—DH010GM2— 57  
 - Z BUS BITS 0.2-0.4 SELECT A—DH016GL6— 62  
 - ALU 0 SUM BIT 1.2—DL003FF2— 67  
 - ALU 0 SUM BIT 1.3—DL009FF2— 72  
 - ALU 0 SUM BIT 1.4—DL010FF2— 77



000 DH014  
 126 - ALU ADD CONTROL BYTE 0—DH016-AC2  
 212 - Z REG BIT 0.2—DB2  
 4DH002 4DH003 4DH011  
 219 - Z REG BIT 0.3—DB7  
 4DH002 4DH003 4DH011  
 237 - Z REG BIT 0.4—FJ2  
 4DH002 4DH003 4DH011  
 213 - Z BUS BIT 0.2—GB6  
 9CK002 LCR001 LCU010 LCU014  
 LCVO01 4DG976 4DN001  
 215 + OUTBUS BIT 0.2—AA001-GC2  
 220 - Z BUS BIT 0.3—GF6  
 9CK002 LCM003 LCR001 LCV001  
 4DG976 4DN002  
 222 + OUTBUS BIT 0.3—AA001-GG2  
 238 - Z BUS BIT 0.4—GK6  
 9CK002 LCR001 LCR006 LCV001  
 4DG976 4DN002  
 240 + OUTBUS BIT 0.4—AA001-GL2  
 304 - Z BUS BITS 0.2-0.4 ZERO DG974-GM6

ALU 0 ALU 1 AND Z REG	
BITS 0.2-0.4	
E-C-HISTORY E-MARCH-3705	
FRAME	01
DATE	LAST EC
10-14-60	344270
IBM CORP-SCD	DH014
PoN. 1852894	000

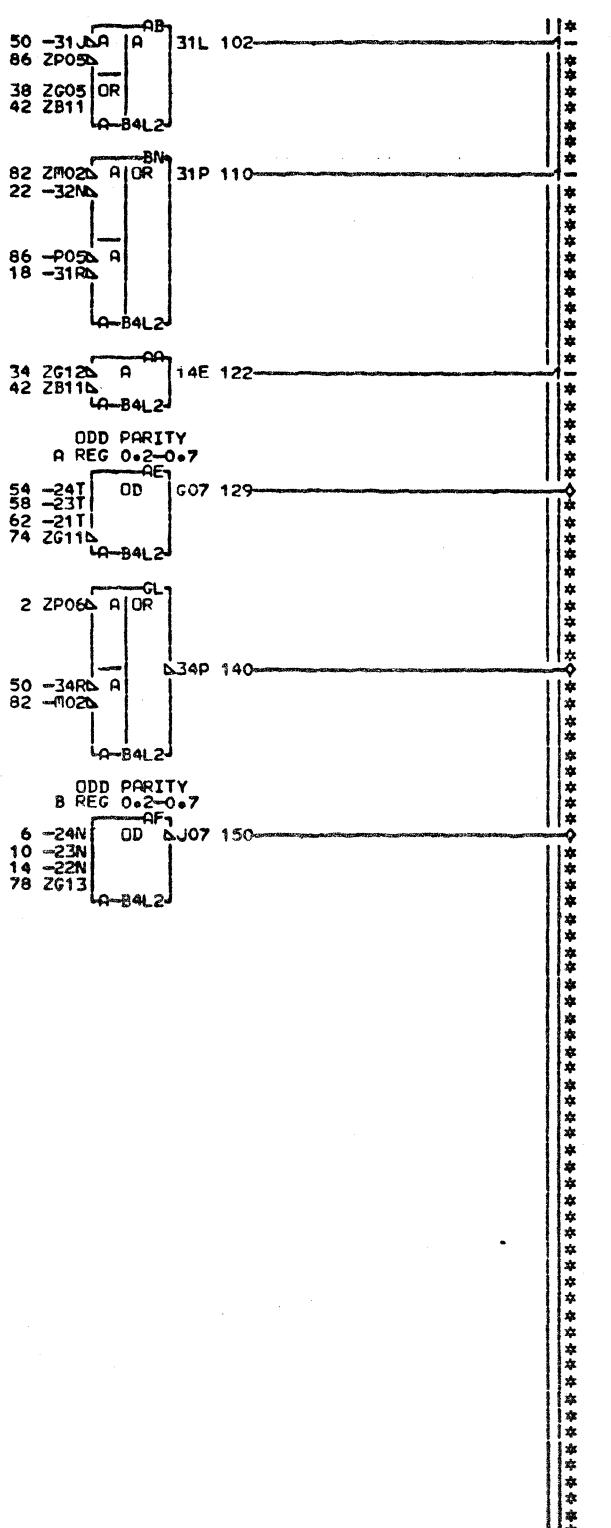
- COMPLEMENT A BUS CA004DD2- 2  
 - T0+T1 TIME SET A-B REGS CC007HK4- 8-3  
 + FORCE A BUS BIT 0.2 CF002DE2- 14  
 + FORCE A BUS BIT 0.3 CF002DF2- 20  
 + FORCE A BUS BIT 0.4 CF002DG2- 26  
 - GATE SAR TO A BUS CS004BK6- 32-3  
 - SAR BIT 0.2 DH011EC6- 38-1  
 - SAR BIT 0.3 DH011EH6- 44-1  
 - SAR BIT 0.4 DH011EK6- 50-1  
 + SHIFT RIGHT BIT 0.2 TO A BUS DN003DF2- 56-1  
 + SDR BIT 0.2 TO A BUS DN003DG2- 62-1  
 + SHIFT RIGHT BIT 0.3 TO A BUS DN003EH2- 68-1  
 + SDR BIT 0.3 TO A BUS DN003EJ2- 74-1  
 + SHIFT RIGHT BIT 0.4 TO A BUS DN003FK2- 80-1  
 + SDR BIT 0.4 TO A BUS DN003FL2- 86-1



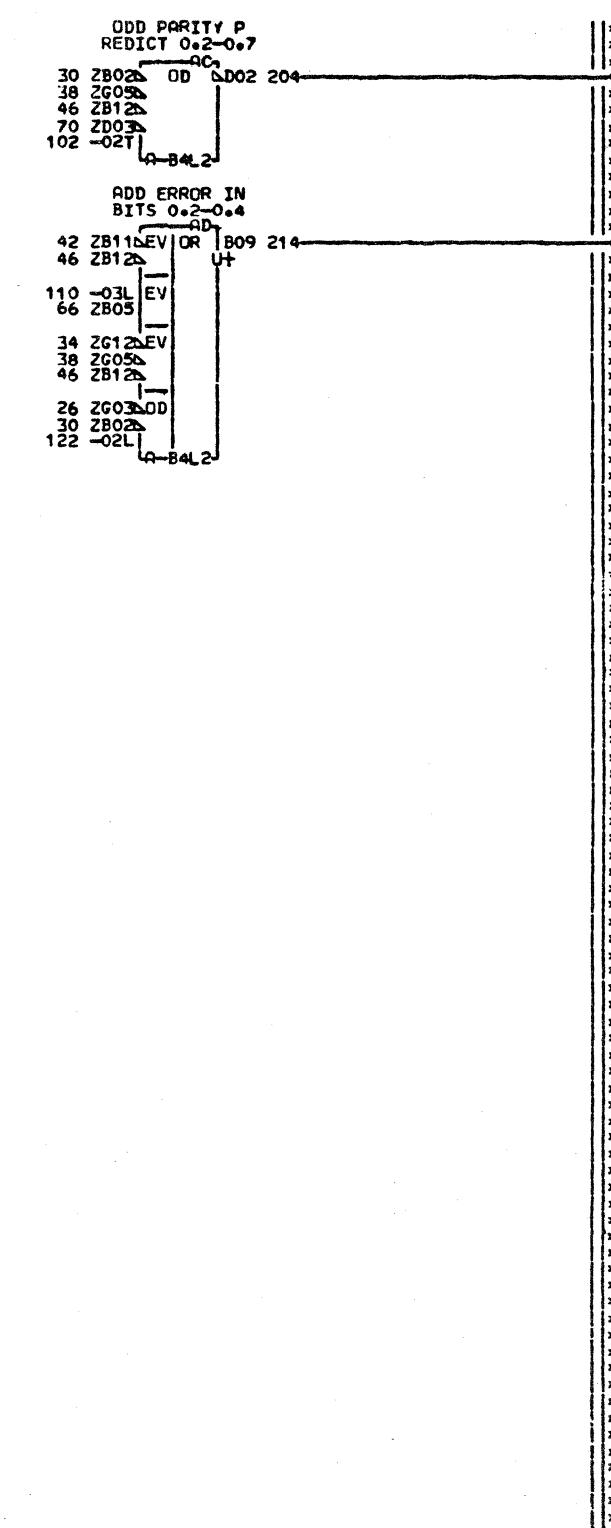
000 DH015  
 125 + SET A REG AND B REG DH002-EA2  
 321 - A REG BIT 0.2 DH017-FD2  
 322 + A REG BIT 0.2 FD6  
 LDH008 LDH016  
 312 - A REG BIT 0.3 DH017-FH2  
 313 + A REG BIT 0.3 FH6  
 LDH009 LDH016  
 303 - A REG BIT 0.4 DH017-FM2  
 304 + A REG BIT 0.4 FM6  
 LDH010 LDH016

A BUS ASSEMBLER		
BITS 0.2-0.4		
E-C-HISTORY		E MACH 3705
		FRAME 01
DATE 10-14-80	LAST EC 344270	IBM CORP SCD DH015
		P.N. 1852895 000

- CROSS LO TO HI CA003HL2- 2  
 + B REG BIT 0..2 TO ALU 0 DH002FJ2- 6  
 + D REG BIT 0..3 TO ALU 0 DH002FL2- 10  
 + B REG BIT 0..4 TO ALU 0 DH002FN2- 14  
 + ALU 0 CARRY BIT 0..2 DH008DR6- 18  
 + ALU 1 CARRY BIT 0..2 DH008EG6- 22  
 - ALU 0 SUM BIT 0..2 DH008FF2- 26  
 - ALU 1 SUM BIT 0..2 DH008GM2- 30  
 - ALU 0 SUM BIT 0..3 DH009FF2- 34  
 - ALU 1 SUM BIT 0..3 DH009GM2- 38-2  
 - ALU 0 SUM BIT 0..4 DH010FF2- 42-21  
 - ALU 1 SUM BIT 0..4 DH010GM2- 46-3  
 - ALU ADD CONTROL BYTE 0 DH014AC2- 50-2  
 + A REG BIT 0..2 DH015FD6- 54  
 + A REG BIT 0..3 DH015FH6- 58  
 + A REG BIT 0..4 DH015FM6- 62  
 + CARRY LA FROM BITS 0..2-0..4 DH017DD6- 66  
 - ODD PARITY PREDICT 0..5-0..7 DJ016DF8- 70  
 - ODD PARITY A REG BIT 0..5-0..7 DJ016FA2- 74  
 + ODD PARITY B REG 0..5-0..7 DJ016FD2- 78  
 - CARRY LA FROM BITS 0..5-0..7 DJ017DD2- 82-2  
 + CARRY LA FROM BITS 0..5-0..7 DJ017DD6- 86-2



LOC. TYPE  
P-BAL2 6802



000 DH016  
204 - ODD PARITY PREDICT 0..2-0..7 DF8  
LDG976

214 + ADD ERROR IN BITS 0..2-0..4 DJ6  
LDG976

129 + ODD PARITY A REG 0..2-0..7 FA2  
LDG974

150 - ODD PARITY B REG 0..2-0..7 FD2  
LDG974

140 - Z BUS BITS 0..2-0..4 SELECT A GL6  
DH014

ALU CHECK	BITS 0..2-0..4	E-C-HISTORY	E-MACH 3705
FRAME	01	IBM CORP-SCD	DH016
DATE	LAST EC	PoNo.	1852896
10-14-80	344270	000	

- B REG BIT 0.2-----DH002DH2- 2-32

- B REG BIT 0.3-----DH002DH5- 12-32

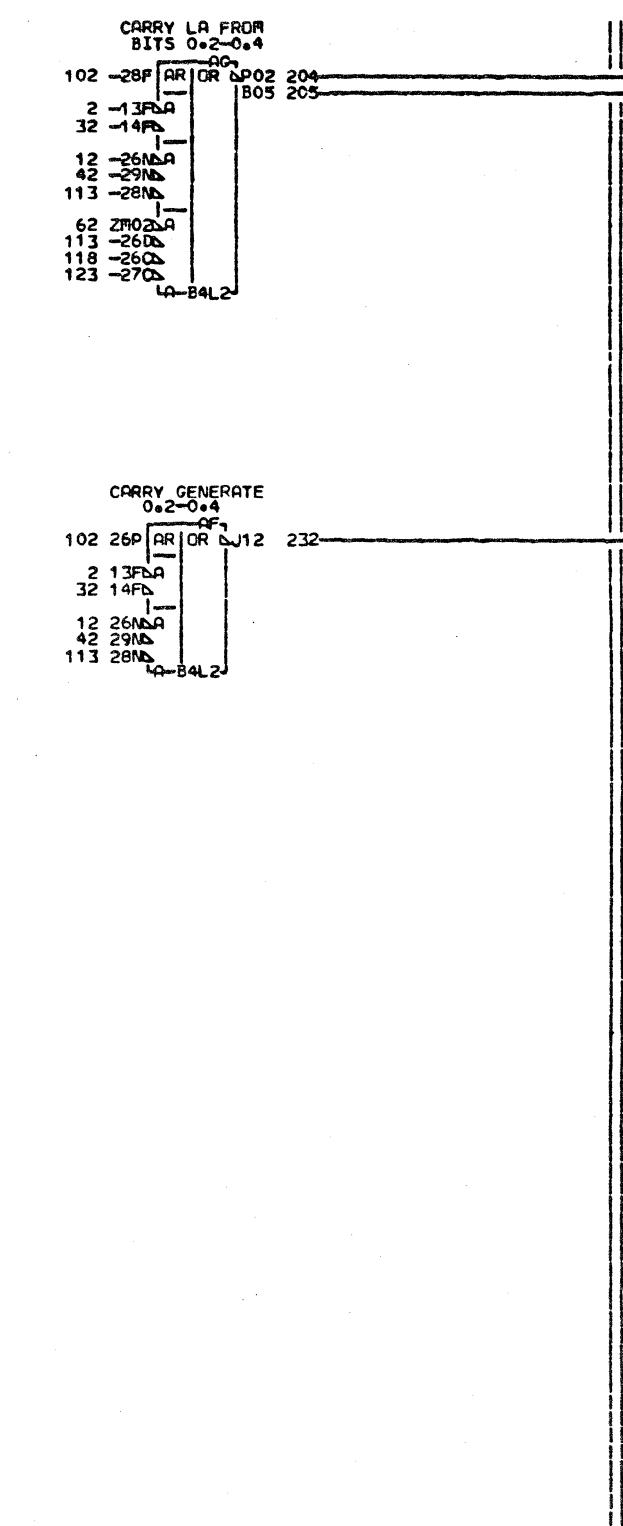
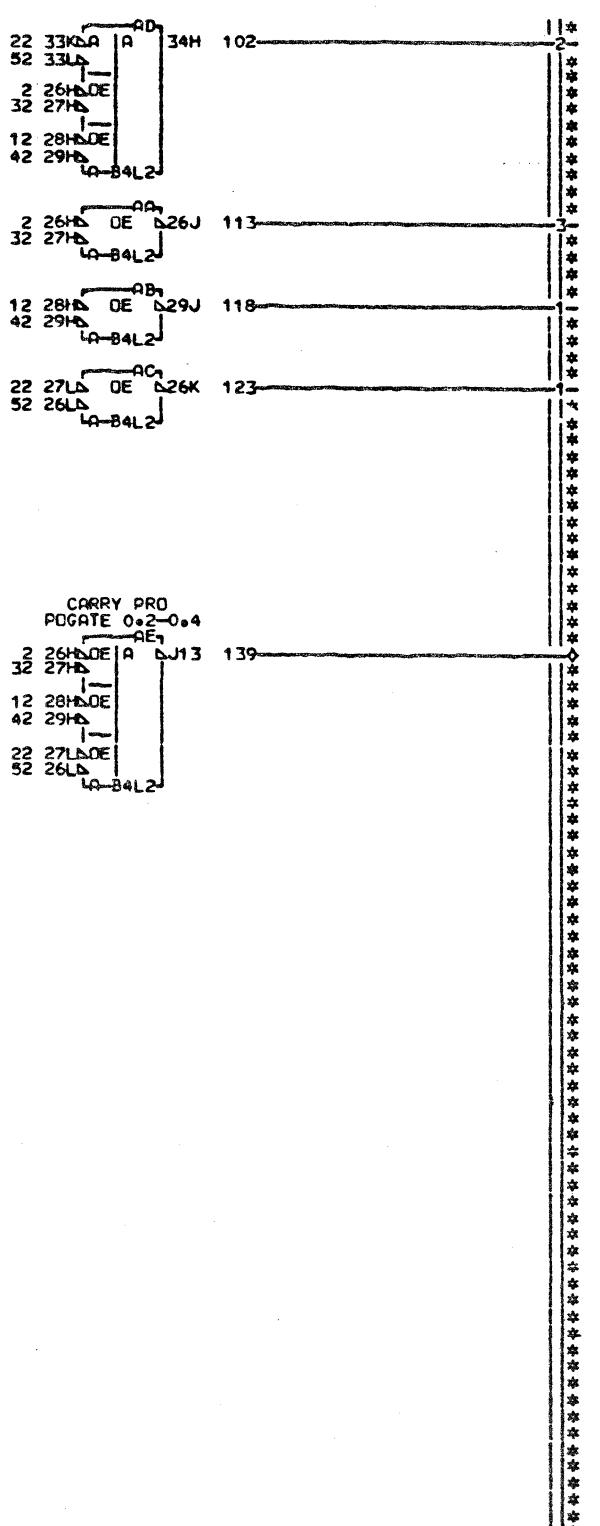
- B REG BIT 0.4-----DH002DH8- 22-3

- A REG BIT 0.2-----DH015FD2- 32-32

- A REG BIT 0.3-----DH015FH2- 42-32

- A REG BIT 0.4-----DH015FM2- 52-3

- CARRY LA FROM BITS 0.5-0.7-----DJ017DD2- 62-1



000 DH017

232 - CARRY GENERATE 0.2-0.4- DG977-DB2

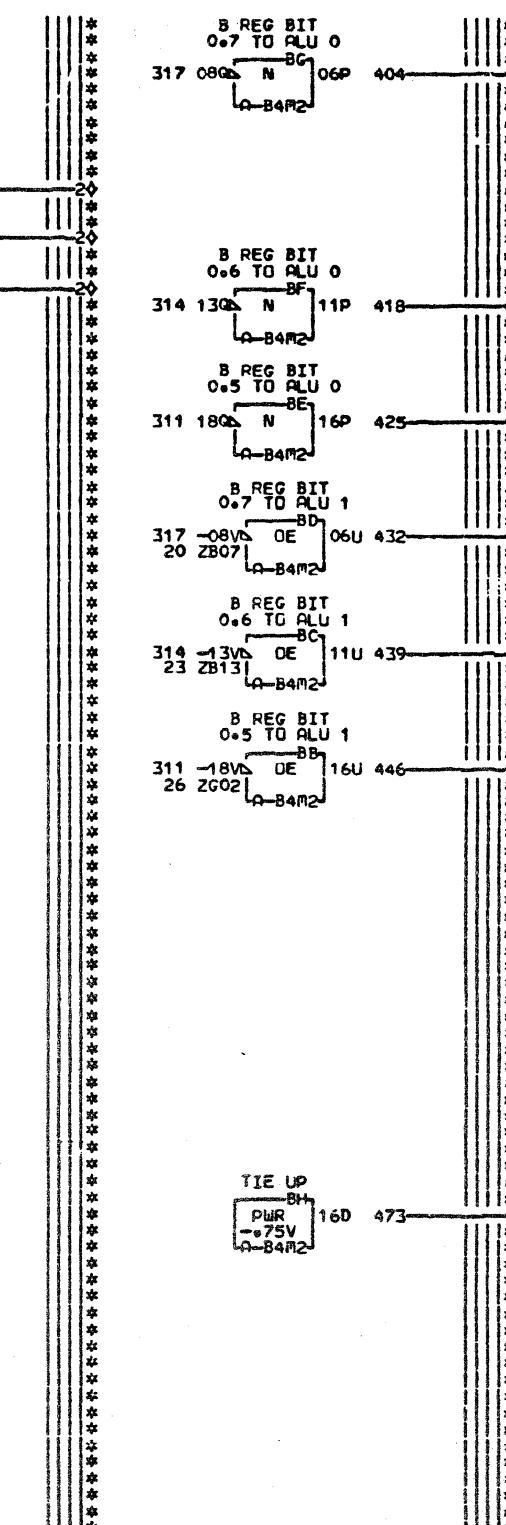
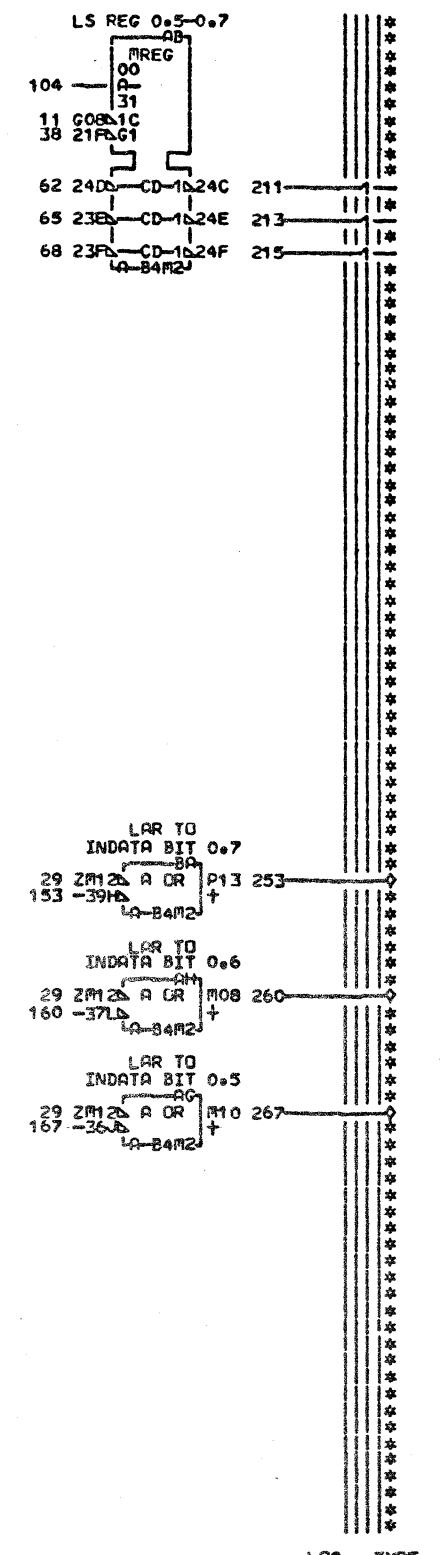
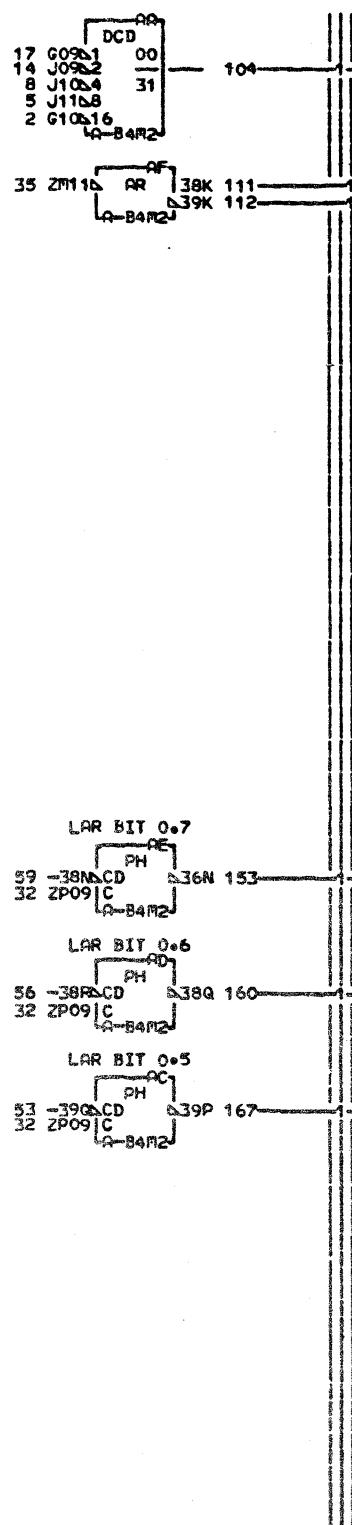
204 - CARRY LA FROM BITS 0.2-0.4 DD2  
LDG976 LDG977

205 + CARRY LA FROM BITS 0.2-0.4 — DD6  
LDG976 LDH016

LDC. TYPE  
P-8412 6802

3-BIT CARRY LOOKAHEAD  
 BITS 0-2=0-4  
 E.C.-HISTORY E MACH.3705  
 DATE LAST EC IBM CORP-SCD DHO  
 10-14-80 344270 P.N. 1852897 00

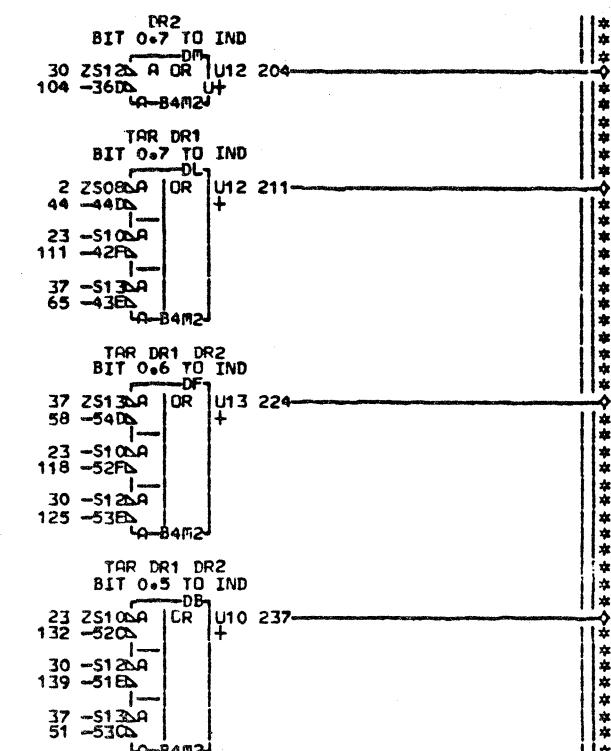
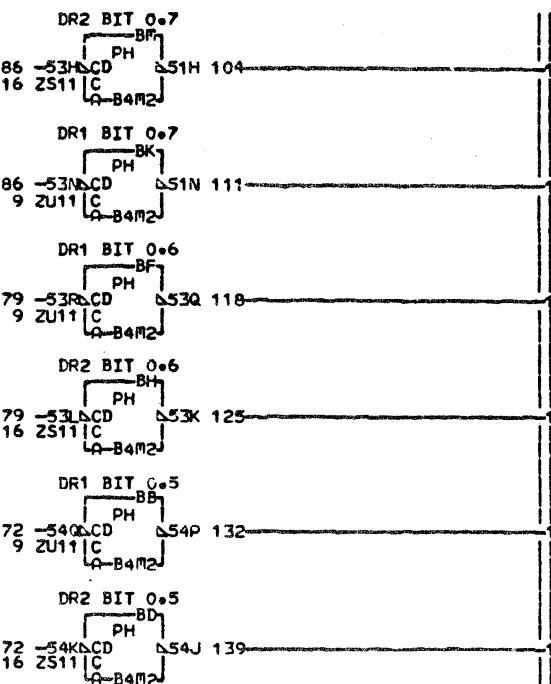
- SELECT LS REG GROUP 1+2 CC006AU4- 2  
 - SELECT LS REG GROUP 1+3 CC006AV4- 5  
 - SELECT LS REG BIT 01+2+3 CC006AW4- 8  
 - WRITE LS CC006BJ4- 11  
 - SELECT LS REG 0+1+4+5 CC006BK4- 14  
 - SELECT LS REG BIT 01+2+4+6 CC006BL4- 17  
 + FORCE ERROR IN BIT 7 CK002DA2- 20  
 + FORCE ERROR IN BIT 6 CK002DB2- 23  
 + FORCE ERROR IN BIT 5 CK002DC2- 26  
 - GATE INPUT 74 CQ004FJ6- 29  
 + SET LAR CS001DM2- 32-3  
 - GATE Y BUS TO B REC CS004ED2- 35  
 - SELECT FLOAT DJ002002- 38  
 - FLOAT DJ002003- 41  
 - Y BUS BIT 0.5 DJ011DC4- 44  
 - Y BUS BIT 0.6 DJ011DH4- 47  
 - Y BUS BIT 0.7 DJ011DL4- 50  
 - SAR BIT 0.5 DJ011EC6- 53  
 - SAR BIT 0.6 DJ011EH6- 56  
 - SAR BIT 0.7 DJ011EK6- 59  
 - Z REG BIT 0.5 DJ014FB2- 62  
 - Z +EG BIT 0.6 DJ014FB7- 63  
 - Z REG BIT 0.7 DJ014FK2- 68  
 + SET A REG AND B REG DJ015EA2- 71



000 DJ002  
 311 - B REG BIT 0.5 DJ017-DH2  
 314 - B REG BIT 0.6 DJ017-DH5  
 317 - B REG BIT 0.7 DJ017-DH8  
 267 + LAR TO INDATA BIT 0.5 CU011-EB2  
 260 + LAR TO INDATA BIT 0.6 CL011-ED2  
 253 + LAR TO INDATA BIT 0.7 CU011-EF2  
 446 + B REG BIT 0.5 TO ALU 1- DJ008-EJ2  
 439 + B REG BIT 0.6 TO ALU 1- DJ009-EL2  
 432 + B REG BIT 0.7 TO ALU 1- DJ010-EN2  
 425 + B REG BIT 0.5 TO ALU 0- DJ008 DJ016  
 419 + B REG BIT 0.6 TO ALU 0- DJ009 DJ016  
 404 + B REG BIT 0.7 TO ALU 0- FN2 DJ010 DJ016  
 473 + TIE UP GF4 DJ003 DJ010

B REG LAR AND LOCAL STORE	
BITS 0.5 0.6 0.7	
ECo-HISTORY	EACH=3705
FRAME	01
DATE 10-14-80	LAST EC 344270
IBM CORP+SCD	DJ002
PoNo. 1859588	000

- GATE TAR TO Y BUS CS004FJ2- 2  
 + SET DR1 CS007FC6- 9-3  
 + SET DR2 CS007FD6- 16-3  
 - GATE DISP REG 1 TO IND CU001EK6- 23-3  
 - GATE DISPL REG 2 TO IND CU001EL6- 30-3  
 - GATE TAR TO IND CU001EM6- 37-3  
 + TIE JP DJ002GF4- 44  
 - TAR BIT 0.5 DJ011AC6- 51  
 - TAR BIT 0.6 DJ011AC6- 58  
 - TAR BIT 0.7 DJ011AM6- 65  
 - Z REG BIT 0.5 DJ014FB2- 72-2  
 - Z REG BIT 0.6 DJ014FB7- 79-2  
 - Z REG BIT 0.7 DJ014FK2- 86-2



000 DJ003  
237 + TAR DR1 DR2 BIT 0.5 TO IND DB2  
4AP013

224 + TAR DR1 DR2 BIT 0.6 TO IND DP2  
4AP013

211 + TAR DR1 BIT 0.7 TO IND AP013-DL2

204 + DR2 BIT 0.7 TO IND AP013-DM2

LOC. TYPE  
4-B4M2 6802

CCU DISPLAY REGISTERS 1 AND 2	
BITS 0.5 0.6 AND 0.7	
E.C.-HISTORY E.MACH-3705	
FRAME	01
DATE	LAST EC
10-14-80	344270
IBM CORP./SCD	DJ003
P/N. 1659589 000	

DJ003  
000

+ ALU AND CONTROL BYTE 0 — CA004EB6- 2-2

+ ALU OR CONTROL BYTE 0 — CP004EF6- 12-2

+ ALU ADD CONTROL BYTE 0 — CA004EJ6- 22-2

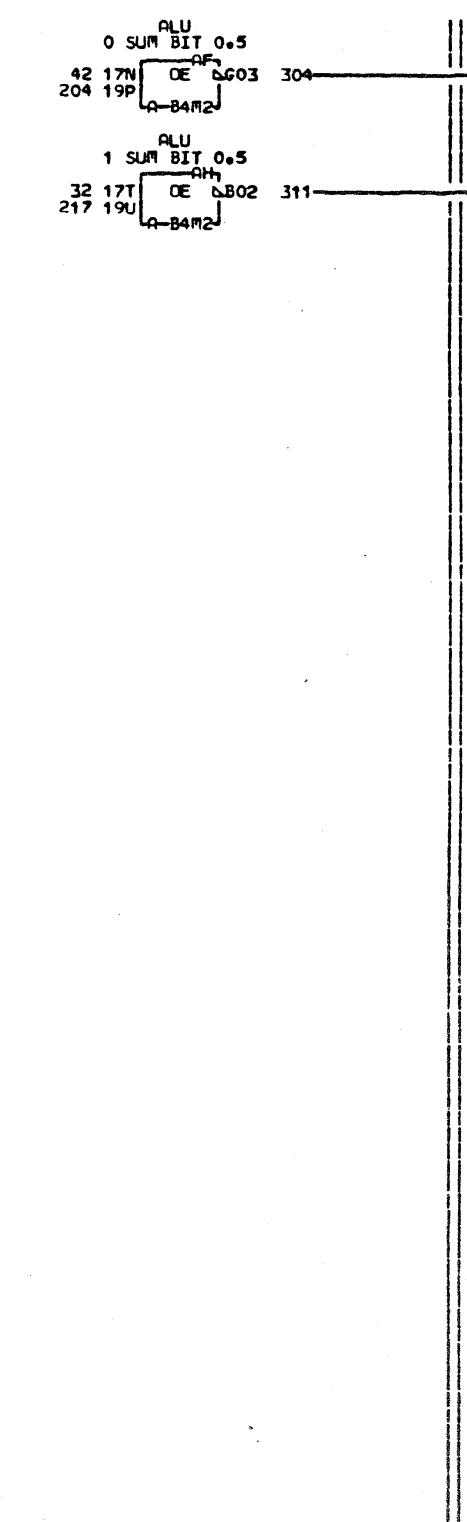
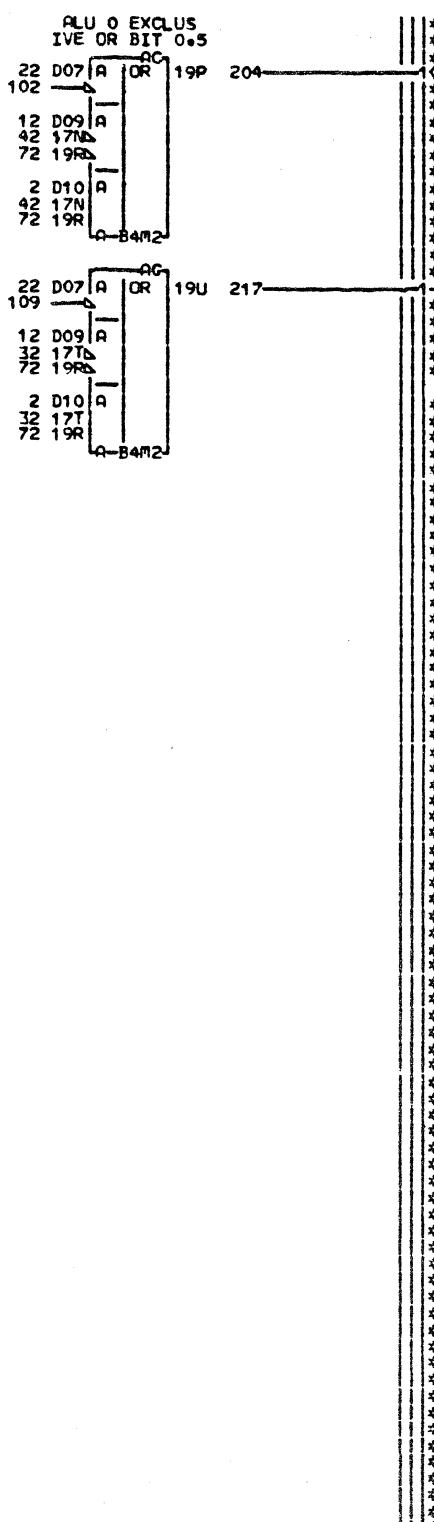
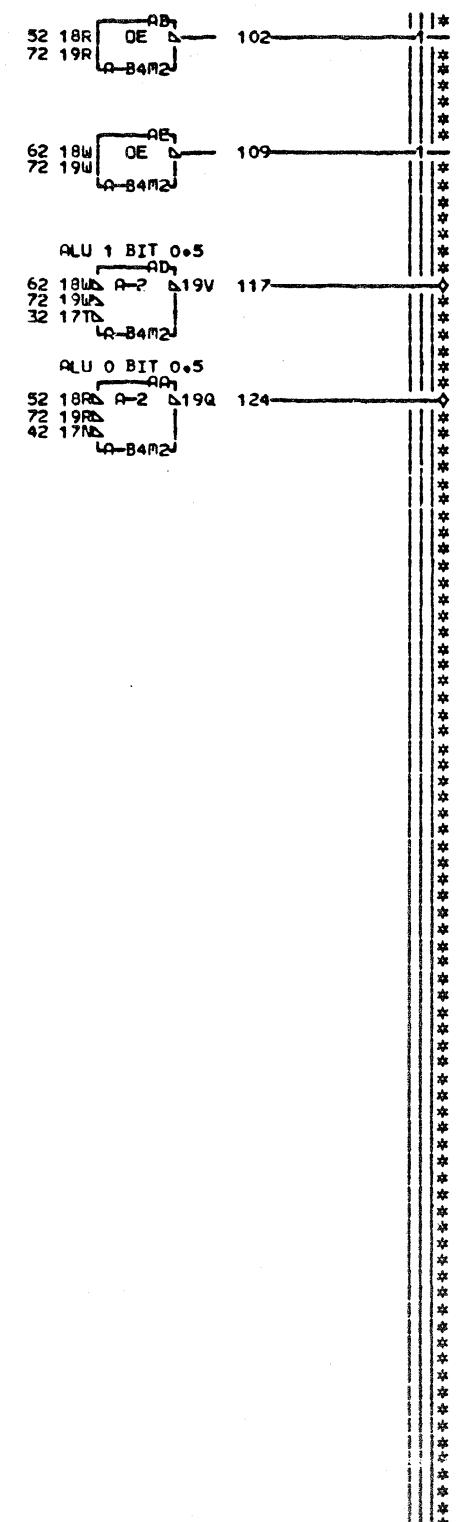
+ B REG BIT 0.5 TO ALU 1 — DJ002EJ2- 32-21

+ B REG BIT 0.5 TO ALU 0 — DJ002FJ2- 42-21

+ ALU 0 CARRY BIT 0.6 — DJ009DA6- 52-2

+ ALU 1 CARRY BIT 0.6 — DJ009EG6- 62-2

+ A REG BIT 0.5 — DJ015FD6- 72-44



000 DJ008  
124 + ALU 0 CARRY BIT 0.5 — DJ016-DA6  
204 + ALU 0 EXCLUSIVE OR BIT 0.5 — ED2  
217 + ALU 1 CARRY BIT 0.5 — DJ016-EG6  
304 - ALU 0 SUM BIT 0.5 — FF2  
400 DJ014 LD004  
311 - ALU 1 SUM BIT 0.5 — CR2  
400 DJ014 LD016

DJ008  
000

LCC. TYPE  
A-B4M2 6802

ALU 0 AND ALU 1	BIT 0.5	E-C-HISTORY	E-FACH-3705
FRAME	01	IBM CORP-SCD	DJ008
DATE	LAST EC	10-14-80 344270	PoNo 1859590
			000

+ ALU AND CONTROL BYTE 0 — CA004EB6— 2-2

+ ALU OR CONTROL BYTE 0 — CA004EF6— 12-2

+ ALU ADD CONTROL BYTE 0 — CA004EJ6— 22-2

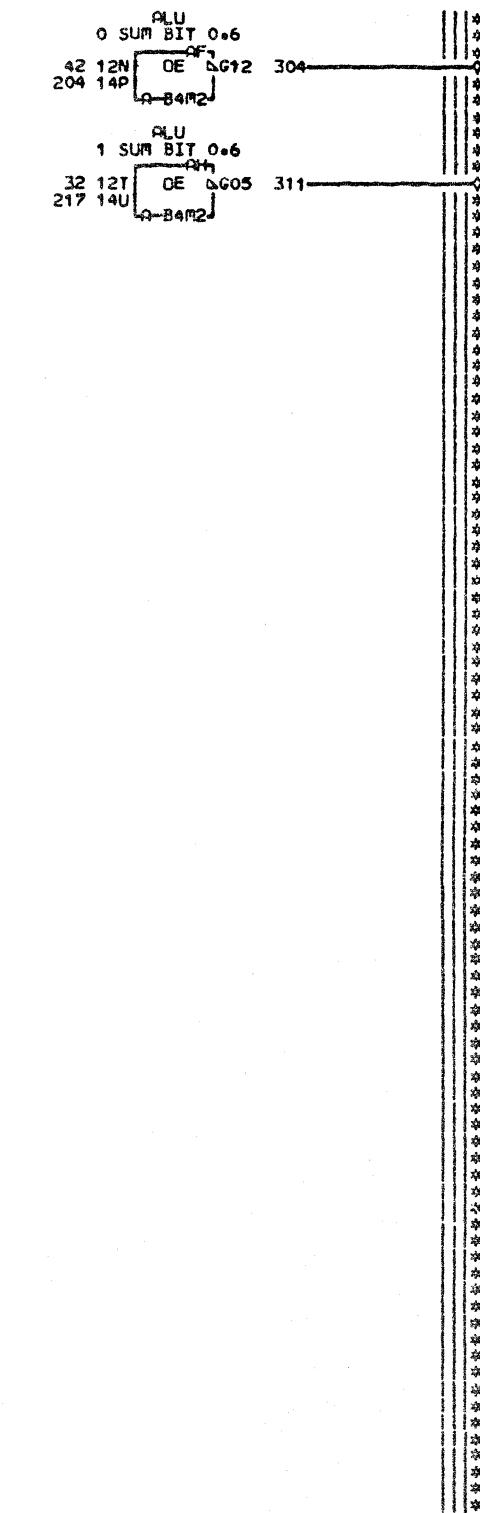
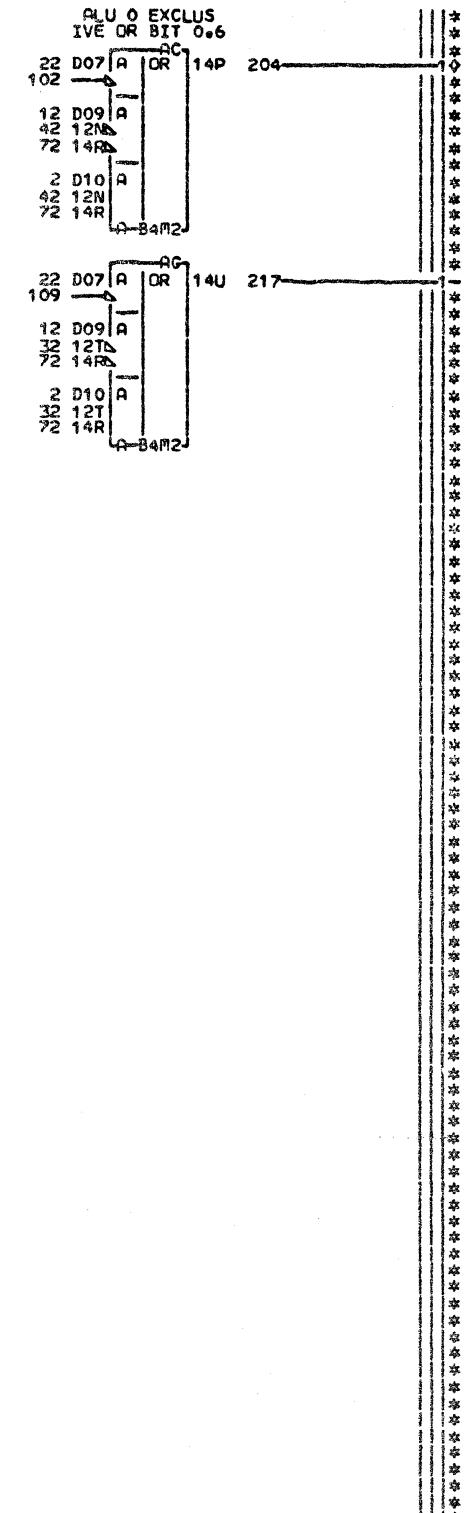
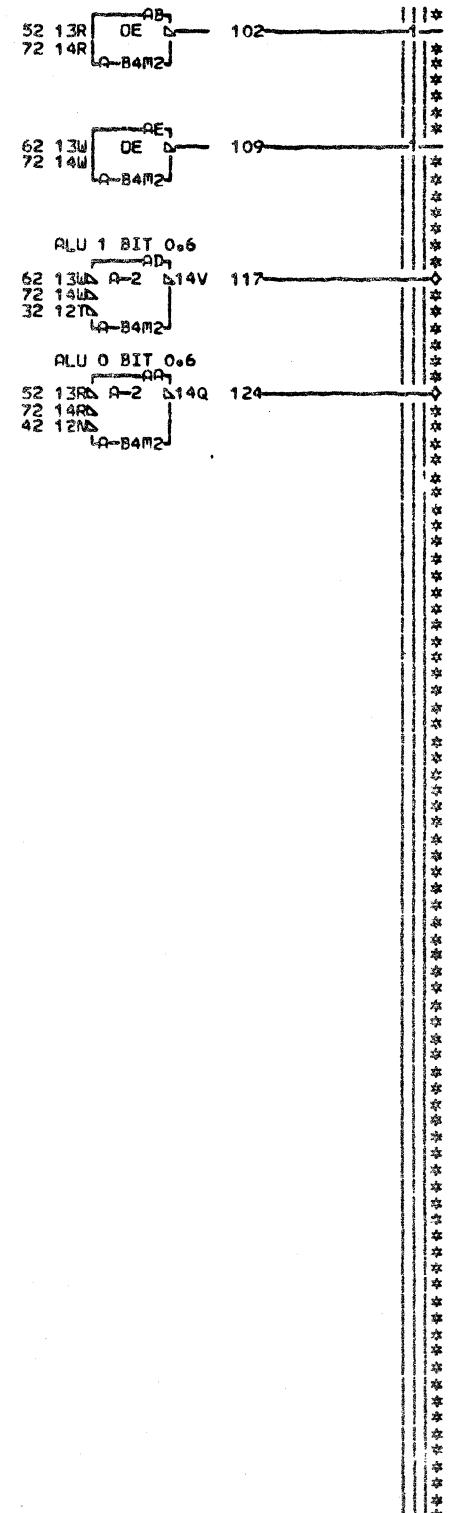
+ B REG BIT 0-6 TO ALU 1 — DJ002EL2— 32-121

+ B REG BIT 0-6 TO ALU 0 — DJ002FL2— 42-121

+ ALU 0 CARRY BIT 0-7 — DJ010DA6— 52-2

+ ALU 1 CARRY BIT 0-7 — DJ010EG6— 62-2

+ P REG BIT 0-5 — DJ015FH6— 72-44-



000 DJ009  
124 + ALU 0 CARRY BIT 0-6 — DJ008-DA6

204 + ALU 0 EXCLUSIVE OR BIT 0-6 — ED2  
LDJ014

117 + ALU 1 CARRY BIT 0-6 — DJ008-EG6

304 — ALU 0 SUM BIT 0-6 — FF2  
LDJ014 LDJ016 LDM004

311 — ALU 1 SUM BIT 0-6 — GM2  
LDJ014 LDJ016

LCC. TYPE  
Q-B4M2 6802

ALU 0 AND ALU 1 BIT 0-6		E.C.-HISTORY	MACH.3705
FRAME	01		
DATE	LAST EC		
10-14-80	344270	IBM CORP-SCD	DJ009
		PoN. 1859591	000

DJ009  
000

+ ALU AND CONTROL BYTE 0 — CA004EB6— 2-2

+ ALU OR CONTROL BYTE 0 — CR004EF6— 12-2

+ ALU ADD CONTROL BYTE 0 — CA004EJ6— 22-2

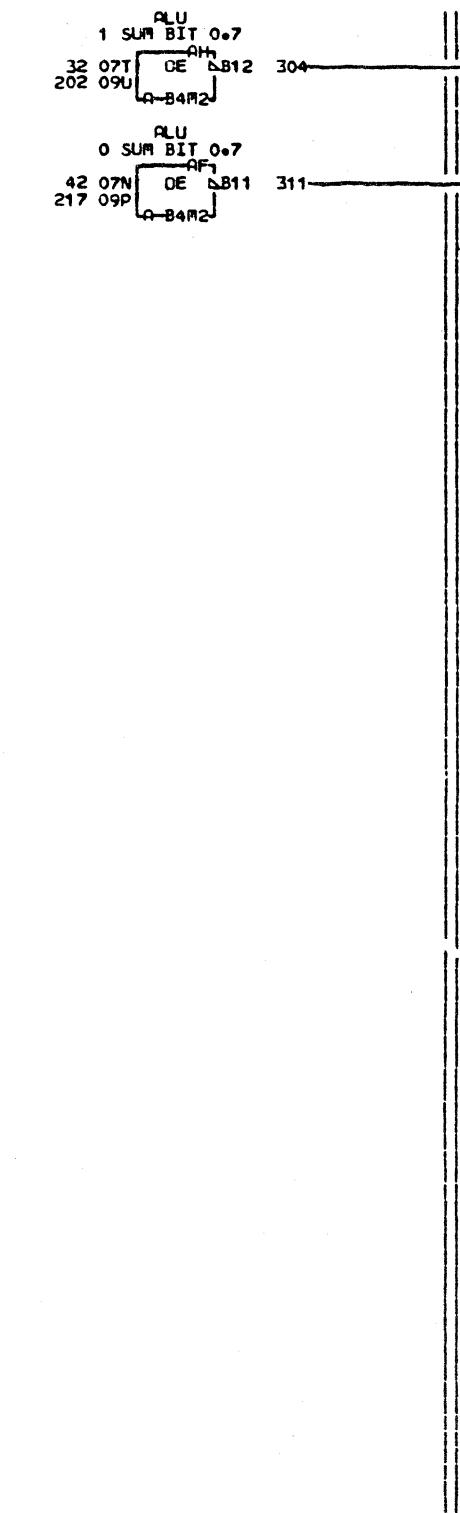
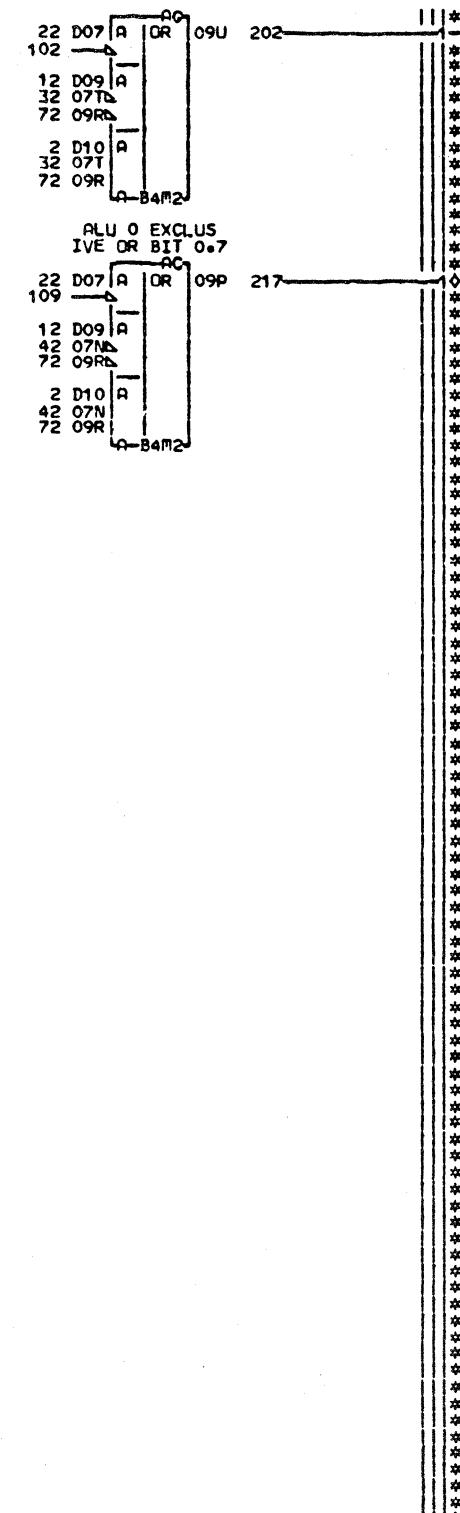
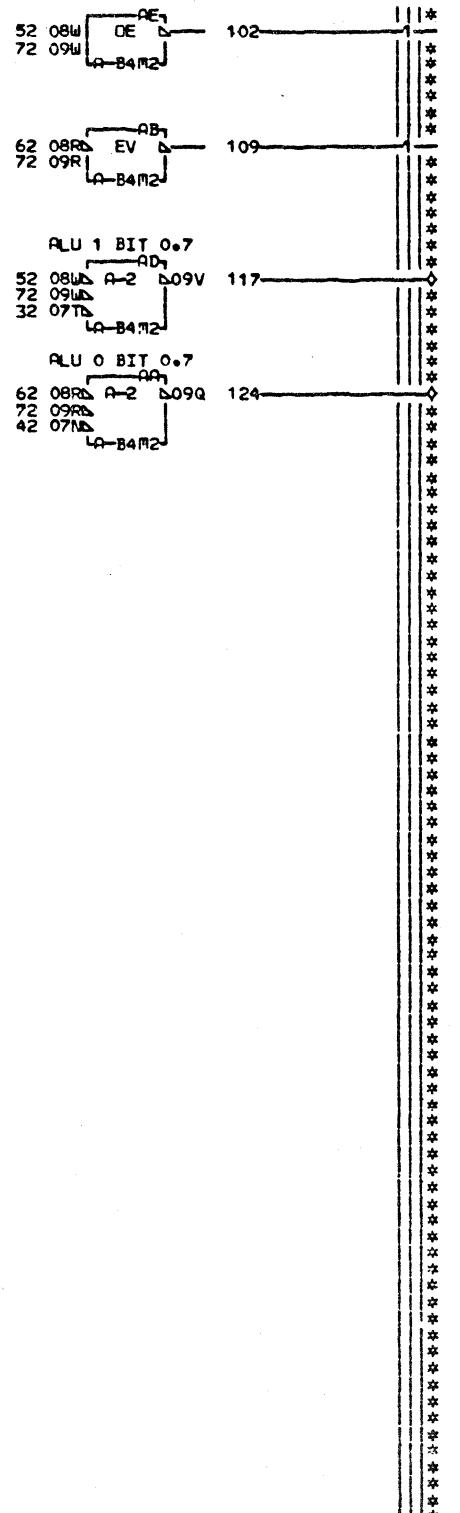
+ B REG BIT 0.7 TO ALU 1 — DJ002EN2— 32-121

+ B REG BIT 0.7 TO ALU 0 — DJ002FN2— 42-121

+ TIE UP — DJ002GF4— 52-2

- FLOAT — DJ010001— 62-2

+ A REG BIT 0.7 — DJ015FM6— 72-44



000 DJ010  
124 + ALU 0 CARRY BIT 0.7 — DJ009-DA6

217 + ALU 0 EXCLUSIVE OR BIT 0.7 — ED2  
LDJ014

117 + ALU 1 CARRY BIT 0.7 — DJ009-EG6

311 - ALU 0 SUM BIT 0.7 — FF2  
LDJ014 LDJ016 LDM004

304 - ALU 1 SUM BIT 0.7 — GM2  
LDJ014 LDJ016

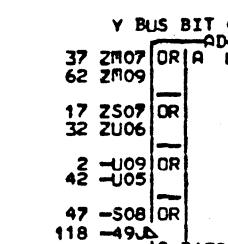
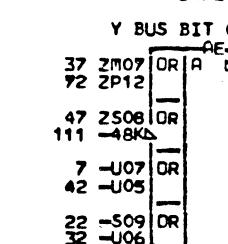
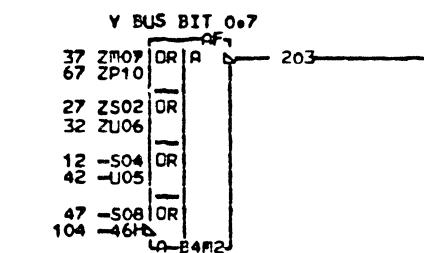
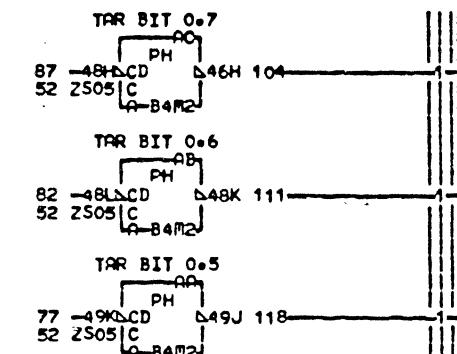
LOC. TYPE  
A-B4M2 6802

DJ010  
000

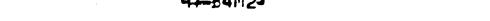
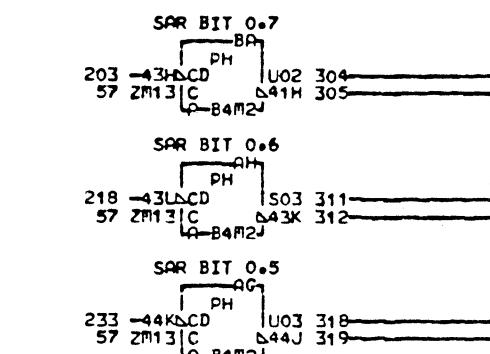
ALU 0 AND ALU 1	BIT 0.7	EACH=3705
E-C-HISTORY		FRAME 01
DATE 10-14-80	LAST EC 344270	IBM CORP-SCD DJ010
		P.N. 1859592 000

+ INBUS BYTE 0 BIT 5 AA0001DC6# 211  
 + INBUS BYTE 0 BIT 6 AA0001DD1# 7  
 + INBUS BYTE 0 BIT 7 AA0001DD3# 12  
 + ADBUS BIT 0.5 AA0003DC6# 17  
 + ADBUS BIT 0.6 AA0003DD1# 22  
 + ADBUS BIT 1.7 AA0003DD3# 27  
 - GATE ADBUS TO Y BUS CS004CA6- 32  
 - GATE CCU INDATA TO Y BUS CS004DB2- 37  
 - GATE INBUS TO Y BUS CS004FG2- 42  
 - GATE TAR TO Y BUS CS004FJ2- 47  
 + SET TAR CS007CH2- 52  
 + SET SAR CS007EB2- 57  
 + CCU INDATA BIT 0.5 CU011DL4- 62  
 + CCU INDATA BIT 0.7 CU011DM4- 67  
 + CCU INDATA BIT 0.6 CU011DN4- 72  
 - Z REG BIT 0.5 DJ014FB2- 77  
 - Z REG BIT 0.6 DJ014FB7- 82  
 - Z REG BIT 0.7 DJ014FK2- 87

EDGE CONN.  
 2 RESISTOR A-B4M2S09  
 A-B4M2U09  
 7 RESISTOR A-B4M2U07  
 12 RESISTOR A-B4M2S04  
 17 RESISTOR A-B4M2S07  
 22 RESISTOR



LOC. TYPE  
A-B4M2 6802



000 DJ011  
118 - TAR BIT 0.5 DJ003-AC6

111 - TAR BIT 0.6 DJ003-AG6

104 - TAR BIT 0.7 DJ003-AM6

233 - Y BUS BIT 0.5 DJ002-DC4

218 - Y BUS BIT 0.6 DJ002-DH4

203 - Y BUS BIT 0.7 DJ002-DL4

318 + SAR BIT 0.5 EC2  
OCV001 LCW001 LCU011 LCU013  
LDS001 LDT001 LDU001 LDV001

319 - SAR BIT 0.5 EC6  
LDJ002 LDJ015

311 + SAR BIT 0.6 EH2  
OCV001 LCW001 LCU011 LCU013  
LDS001 LDT001 LDU001 LDV001

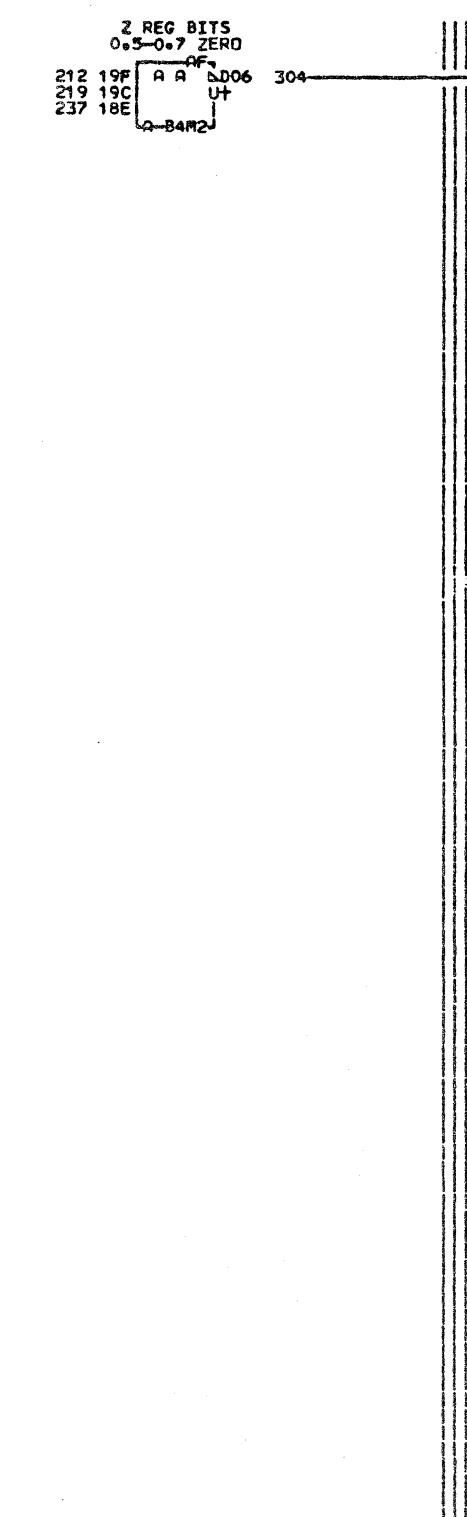
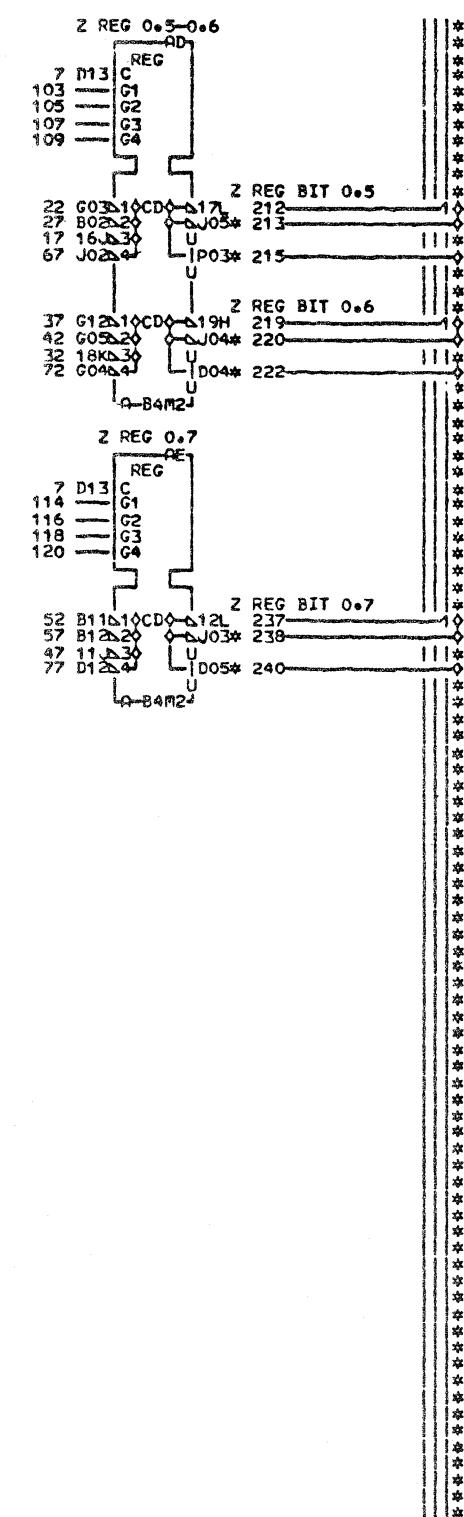
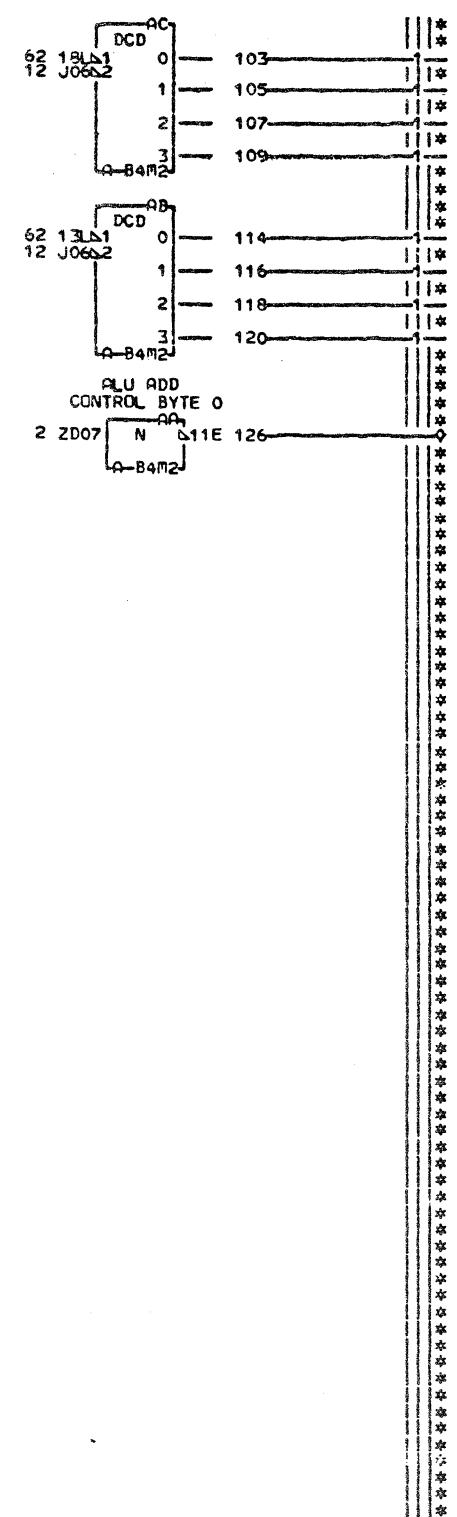
312 - SAR BIT 0.6 EH6  
LDJ002 LDJ015

304 + SAR BIT 0.7 EK2  
OCV001 LCW001 LCU012 LCU013  
LDS001 LDT001 LDU001 LDV001

305 - SAR BIT 0.7 EK6  
LDJ002 LDJ015

SAR TAR AND Y BUS ASSEMBLERS  
BITS 0.5-0.7  
E.C. HISTORY — E MACH 3705  
344270 FRAME 01  
DATE LAST EC IBM CORP+SCD DJ011  
06-02-81 344828 P.N. 1859593 000

+ ALU ADD CONTROL BYTE 0 → CA004EJ6 2-1  
 + T2+T3 SET Z-REG BYTE 0 → CC006FG2 7-2  
 - Z BUS BITS 0.0-0.7 SELECT 2 → DG976CB2 12-2  
 + ALU 0 EXCLUSIVE OR BIT 0.5 → DJ008ED2 17-  
 - ALU 0 SUM BIT 0.5 → DJ008FF2 22-  
 - ALU 1 SUM BIT 0.5 → DJ008GM2 27-  
 + ALU 0 EXCLUSIVE OR BIT 0.6 → DJ009ED2 32-  
 - ALU 0 SUM BIT 0.6 → DJ009FF2 37-  
 - ALU 1 SUM BIT 0.6 → DJ009GM2 42-  
 + ALU 0 EXCLUSIVE OR BIT 0.7 → DJ010ED2 47-  
 - ALU 0 SUM BIT 0.7 → DJ010FF2 52-  
 - ALU 1 SUM BIT 0.7 → DJ010GM2 57-  
 - Z BUS BITS 0.5-0.7 SELECT A → DJ016GL6 62-2  
 - ALU 0 SUM BIT 1.5 → DM008FF2 67-  
 - ALU 0 SUM BIT 1.6 → DM009FF2 72-1  
 - ALU 0 SUM BIT 1.7 → DM010FF2 77-1



000 DJ014  
 126 - ALU ADD CONTROL BYTE 0 → DJ016-AC2  
 212 - Z REG BIT 0.5 → DJ002 4DJ003 4DJ011 FB2  
 219 - Z REG BIT 0.6 → DJ002 4DJ003 4DJ011 FB7  
 237 - Z REG BIT 0.7 → DJ002 4DJ003 4DJ011 FK2  
 213 - Z BUS BIT 0.5 → CK002 CR001 4CV001 4DG976 GB6  
 215 + OUTBUS BIT 0.5 → AA001-GC2  
 220 - Z BUS BIT 0.6 → CK002 CR001 4CU014 4CV001 GF6  
 222 + OUTBUS BIT 0.6 → AA001-GG2  
 238 - Z BUS BIT 0.7 → CK002 CR001 4CU014 4CX009 GK6  
 240 + OUTBUS BIT 0.7 → AA001-GL2  
 304 - Z REG BITS 0.5-0.7 ZERO DG974-GM6

DJ014

000

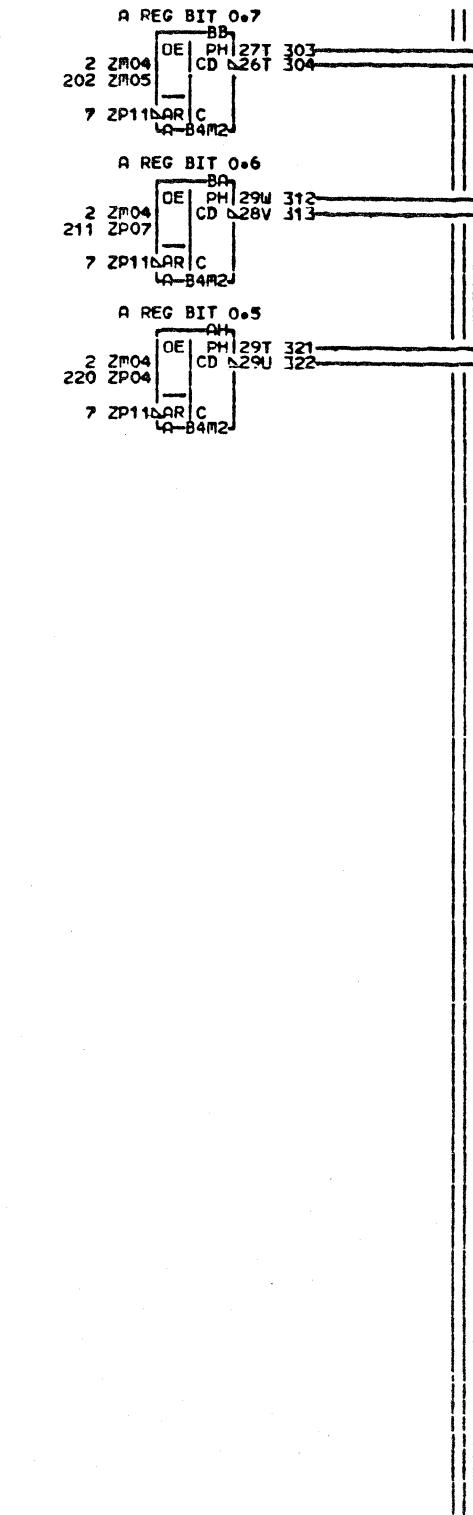
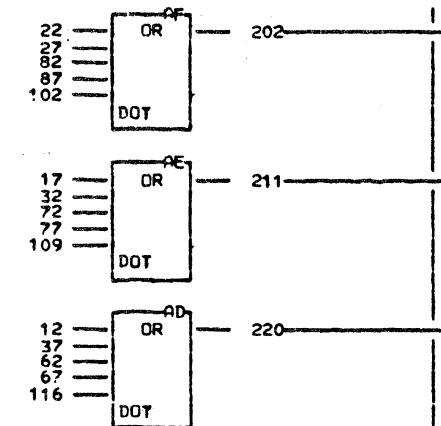
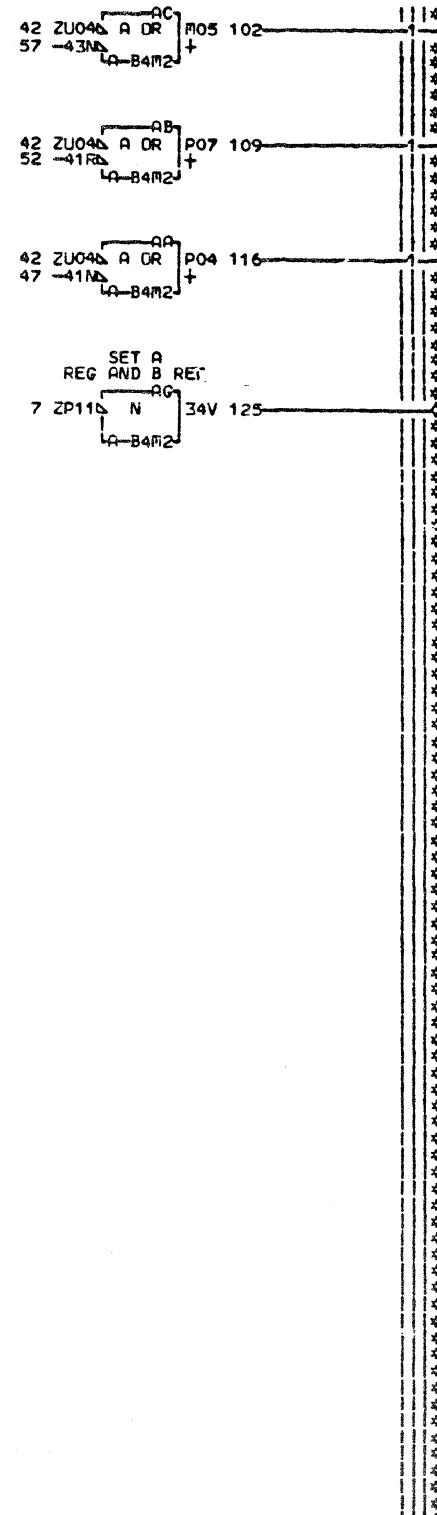
EDGE CONN.  
 213 A-B4E1D11  
 01A-B3E6D02  
 215 A-B4U4B10  
 220 A-B4E1D13  
 01A-B3E6D04  
 222 A-B4U4B12  
 238 A-B4E1E11  
 01A-B3E6E02  
 240 A-B4U4B13

LOC. TYPE  
A-B4F2 6802

ALU 0 ALU 1 AND Z REG BITS 0.5-0.7		MACH 3705
E-C HISTORY		FRAME 01
IBM CORP SCD	DJ014	P/N 1859594 000

DATE LAST EC  
10-14-80 344270

- COMPLEMENT A BUS CA004DD2- 2-3  
 - TO+T1 TIME SET A-B REGS CC007HK4- 7-1-3  
 + FORCE A BUS BIT 0.5 CF002DH2- 12-  
 + FORCE A BUS BIT 0.6 CF002DJ2- 17-  
 + FORCE A BUS BIT 0.7 CF002DK2- 22-  
 + FORCE ADCON BIT 0.7 CF002DL6- 27-1  
 + FORCE ADCON BIT 0.6 CF002DM6- 32-1  
 + FORCE ADCON BIT 0.5 CF002DN6- 37-1  
 - GATE SAR TO A BUS CS004BK6- 42-3  
 - SAR BIT 0.5 DJ011EC6- 47-1  
 - SAR BIT 0.6 DJ011EH6- 52-  
 - SAR BIT 0.7 DJ011EK6- 57-  
 + SHIFT RIGHT BIT 0.5 TO A BUS DP994BB2- 62-1  
 + SDR BIT 0.5 TO A BUS DP994BC2- 67-1  
 + SHIFT RIGHT BIT 0.6 TO A BUS DP994BE2- 72-1  
 + SDR BIT 0.6 TO A BUS DP994BF2- 77-1  
 + SHIFT RIGHT BIT 0.7 TO A BUS DP994BH2- 82-1  
 + SDR BIT 0.7 TO A BUS DP994BJ2- 87-1



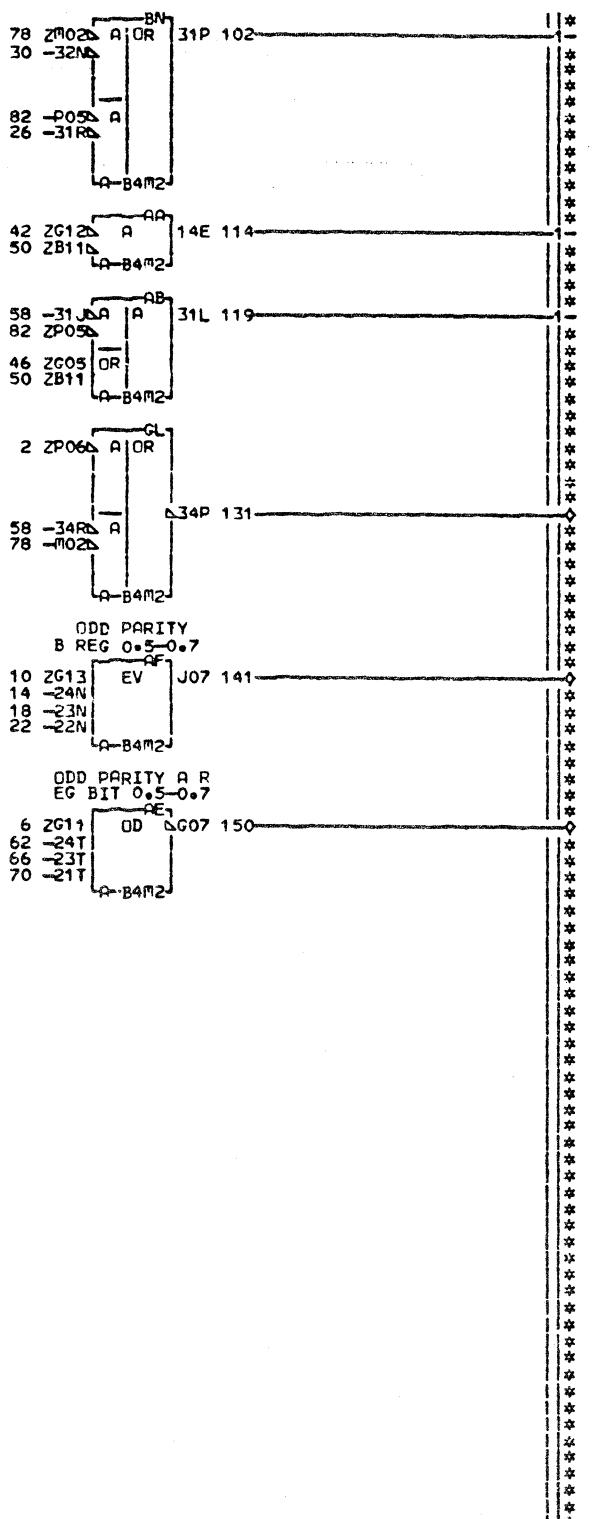
000 DJ015  
 125 + SET A REG AND B REG DJ002-EA2  
 321 - A REG BIT 0.5 DJ017-FD2  
 322 + A REG BIT 0.5 FD6  
 4 DJ008 4 DJ016  
 312 - A REG BIT 0.6 DJ017-FH2  
 4 DJ009 4 DJ016  
 313 + A REG BIT 0.6 FH6  
 303 - A REG BIT 0.7 DJ017-FM2  
 304 + A REG BIT 0.7 FM6  
 4 DJ010 4 DJ016

LDC TYPE  
A-B4M2 6802

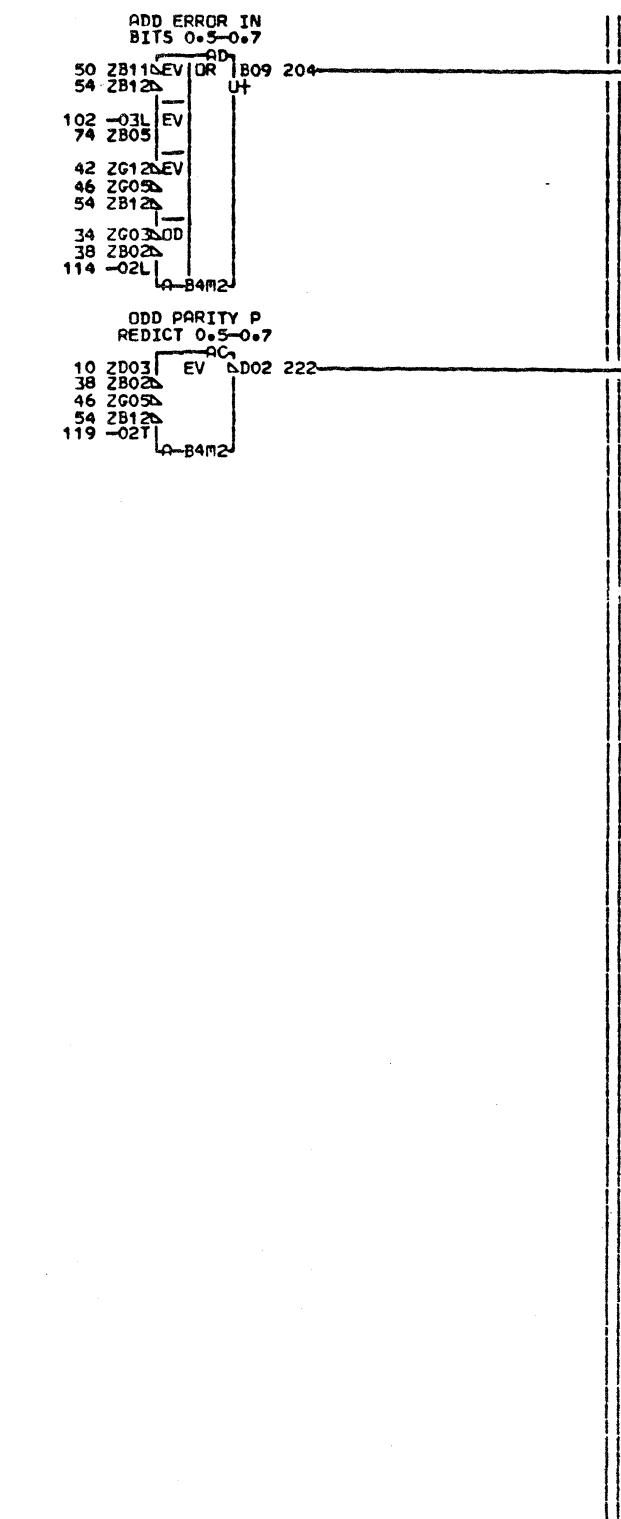
DJ015  
000

2 BUS ASSEMBLER	
BITS 0.5-0.7	
E-C-HISTORY E-MACH 3705	
FRAME	01
IBM CORP SCD	DJ015
DATE LAST EC 10-14-80 344270	
P.N. 1859595 000	

- CROSS LO TO HI CA003HL2- 2-1  
 + FORCE A REG PARITY ERROR CK001GB2- 6-1  
 + BYTE 0 TIE UP DG002GF4- 10-1  
 + B REG BIT 0.5 TO ALU 0 DJ002FJ2- 14-1  
 + B REG BIT 0.6 TO ALU 0 DJ002FL2- 18-1  
 + B REG BIT 0.7 TO ALU 0 DJ002FN2- 22-1  
 + ALU 0 CARRY BIT 0.5 DJ008DA6- 26-1  
 + ALU 1 CARRY BIT 0.5 DJ008EG6- 30-1  
 - ALU 0 SUM BIT 0.5 DJ008FF2- 34-1  
 - ALU 1 SUM BIT 0.5 DJ008GM2- 38-2  
 - ALU 0 SUM BIT 0.6 DJ009FF2- 42-11  
 - ALU 1 SUM BIT 0.6 DJ009GM2- 46-12  
 - ALU 0 SUM BIT 0.7 DJ010FF2- 50-21  
 - ALU 1 SUM BIT 0.7 DJ010GM2- 54-3  
 - ALU ADD CONTROL BYTE 0 DJ014AC2- 58-2  
 + A REG BIT 0.5 DJ015FD6- 62-1  
 + A REG BIT 0.6 DJ015FH6- 66-1  
 + A REG BIT 0.7 DJ015FM6- 70-1  
 + CARRY LA FROM BITS 0.5-0.7 DJ017DD6- 74-1  
 - CARRY LA FROM BITS 1.0-1.4 DK977EC2- 78-2  
 + CARRY LP FROM BITS 1.0-1.4 DK977EG6- 82-2



LCC TYPE  
R-B4P2 6802



ALU CHECK		BITS 0.5-0.7	
E.C.-HISTORY		E MACH.3705	
		FRAME	01
DATE	LAST EC	IBM CORP.SCD	DJ016
10-14-80 344270		P/N 1859596 COO	

DJ016  
000

- B REG BIT 0.5-----DJ002DH2- 2-32

- B REG BIT 0.6-----DJ002DH5- 12-32

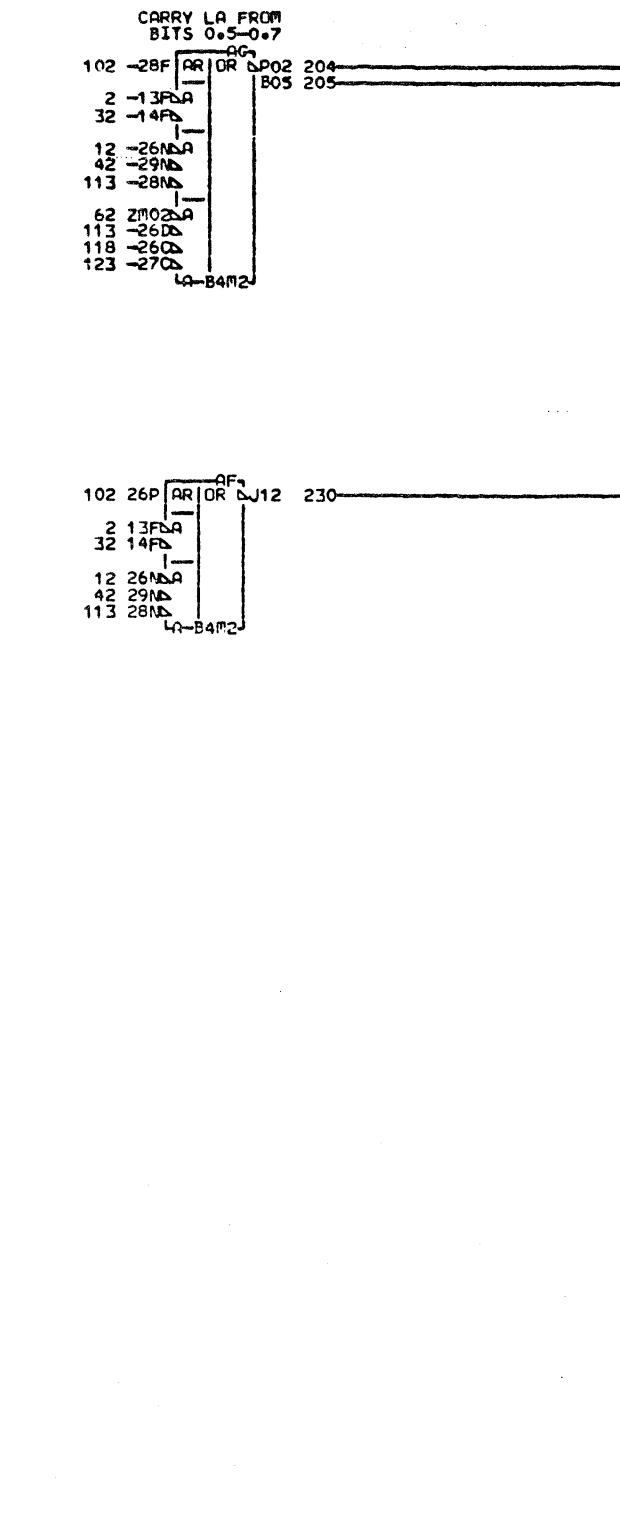
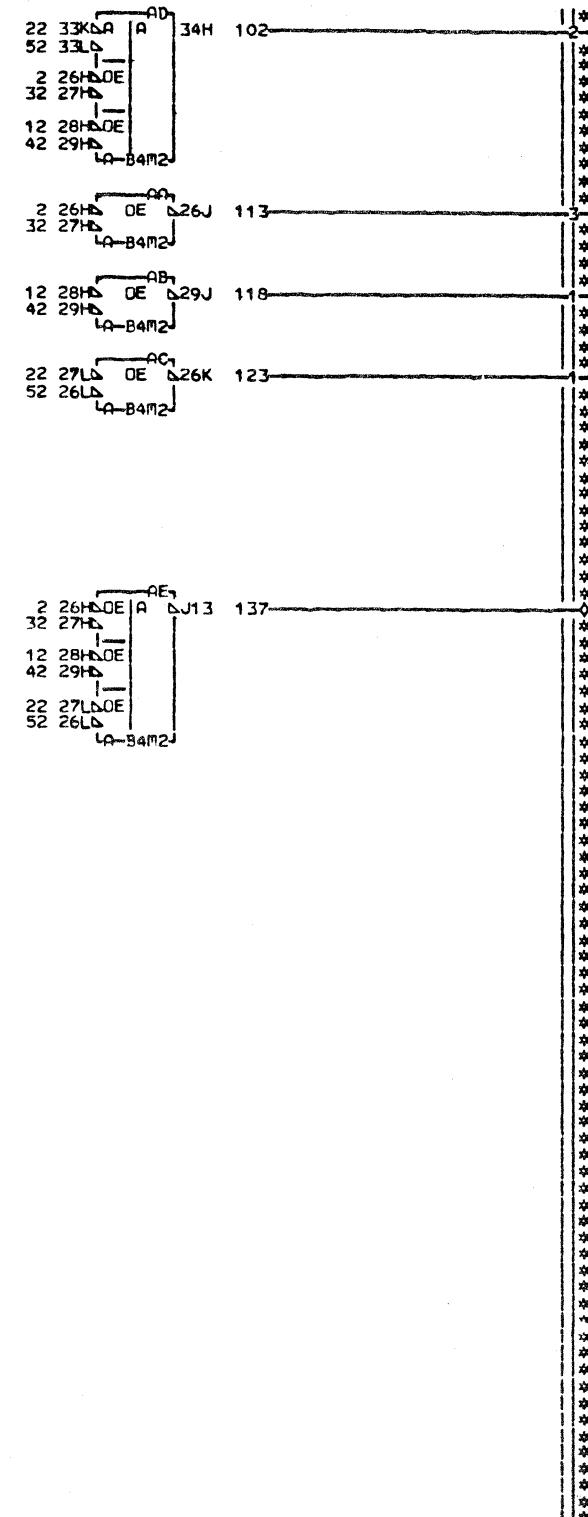
- B REG BIT 0.7-----DJ002DH8- 22-3-

- A REG BIT 0.5-----DJ015FD2- 32-32

- A REG BIT 0.6-----DJ015FH2- 42-32

- A REG BIT 0.7-----DJ015FM2- 52-3-

- CARRY LA FROM BITS 1.0-1.4----DK977EC2- 62-1

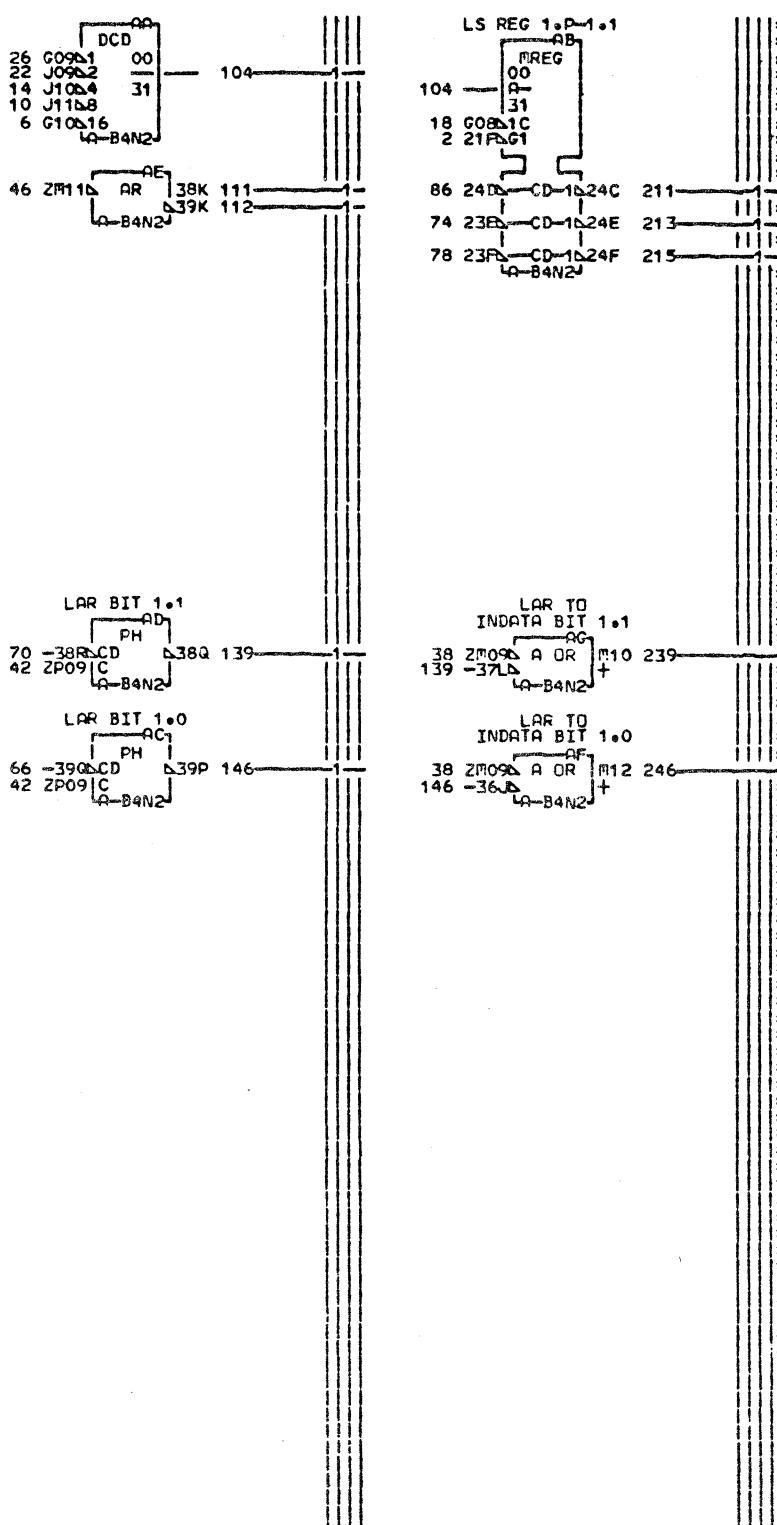


LOC. TYPE  
P-8472 680

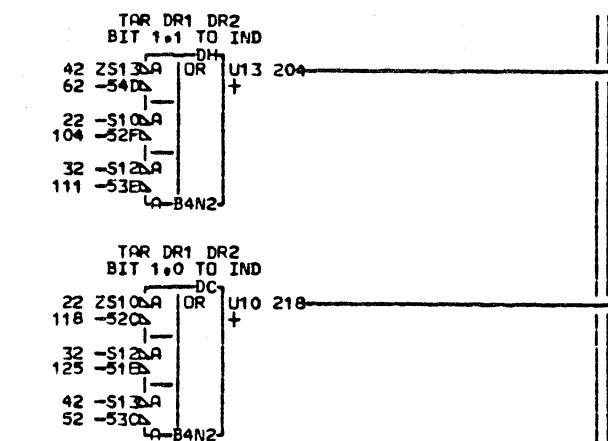
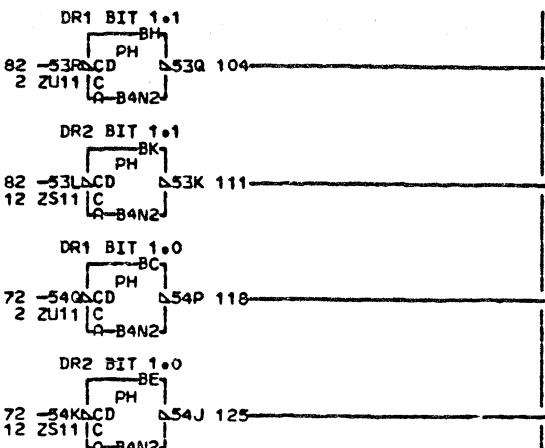
DJ017  
000

3-BIT CARRY LOOKAHEAD  
 BITS 0.5-0.7  
 E.C.-HISTORY — E. FACH.3705  
 DATE LAST EC  
 10-14-80 344270

- FLOAT AU001GH2- 2-1  
 - SELECT LS REG GROUP 1+2 CC006AU4- 6-1  
 - SELECT LS REG GROUP 1+3 CC006AV4- 10-1  
 - SELECT LS REG BIT 0+1+2+3 CC006AU4- 14-1  
 - WRITE LS CC006BJ4- 18-1  
 - SELECT LS REG 0+1+4+5 CC006BK4- 22-1  
 - SELECT LS REG BIT 0+2+4+6 CC006BL4- 26-1  
 + FORCE ERROR IN BIT 1 CK002DK2- 30-1  
 + FORCE ERROR IN BIT 0 CK002DL2- 34-1  
 - GATE INPUT 74 CQ004FJ6- 38-2  
 + SET LAR CS001DM2- 42-2  
 - GATE Y BUS TO B REG CS004ED2- 46-1  
 - FLOAT DK002001- 50-1  
 - Y BUS BIT 1+P DK971DB4- 54-1  
 - Y BUS BIT 1+0 DK971DF4- 58-1  
 - Y BUS BIT 1+1 DK971DK4- 62-1  
 - SAR BIT 1+0 DK971E66- 66-1  
 - SAR BIT 1+1 DK971EL6- 70-1  
 - Z REG BIT 1+0 DK974DB2- 74-1  
 - Z REG BIT 1+1 DK974DB7- 78-1  
 + SET A REG AND B REG DK975EA2- 82-1  
 - Z BUS BIT 1+P DK976BA2- 86-1



+ SET DR1— CS007FC6— 2-2  
 + SET DR2— CS007FD6— 12-2  
 - GATE DISP REG 1 TO IND— CU001EK6— 22-2  
 - GATE DISPL REG 2 TO IND— CU001EL6— 32-2  
 - GATE TAR TO IND— CU001EM6— 42-2  
 - TAR BIT 1<sub>0</sub>— DK971AC6— 52-  
 - TAR BIT 1<sub>1</sub>— DK971AL6— 62-  
 - Z REG BIT 1<sub>0</sub>— DK974DB2— 72-2  
 - Z REG BIT 1<sub>1</sub>— DK974DB7— 82-2



000 DK003  
 218 + TAR DR1 DR2 BIT 1<sub>0</sub> TO IND— DC2  
 LAP014  
 204 + TAR DR1 DR2 BIT 1<sub>1</sub> TO IND— DH2  
 LAP014

LOC. TYPE  
 P-B4N2 6801

CCU DISPLAY REGISTERS 1 AND 2	
BITS 1 <sub>0</sub> AND 1 <sub>1</sub>	
E-C HISTORY	MACH 3705
FRAME 01	
DATE 10-14-80	LAST EC 344270
IBM CORP SCD	DK003
P/N 1859599 000	

DK003  
000

+ ALU AND CONTROL BYTE 1 — CA004EC6— 2-2

+ ALU OR CONTROL BYTE 1 — CA004EG6— 12-2

+ ALU ADD CONTROL BYTE 1 — CA004EK6— 22-2

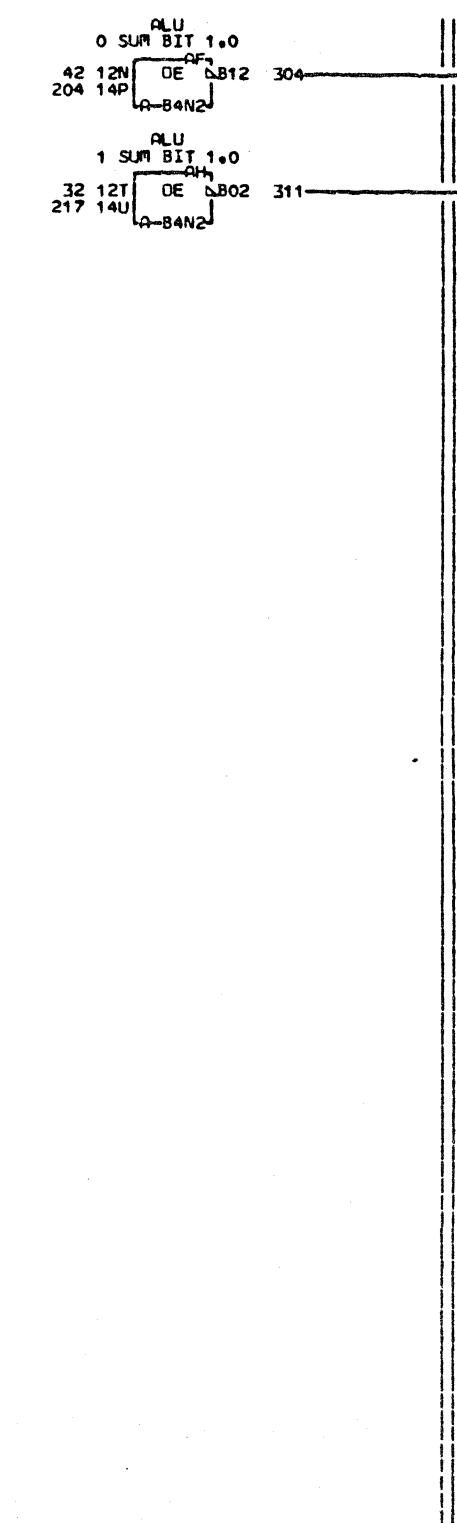
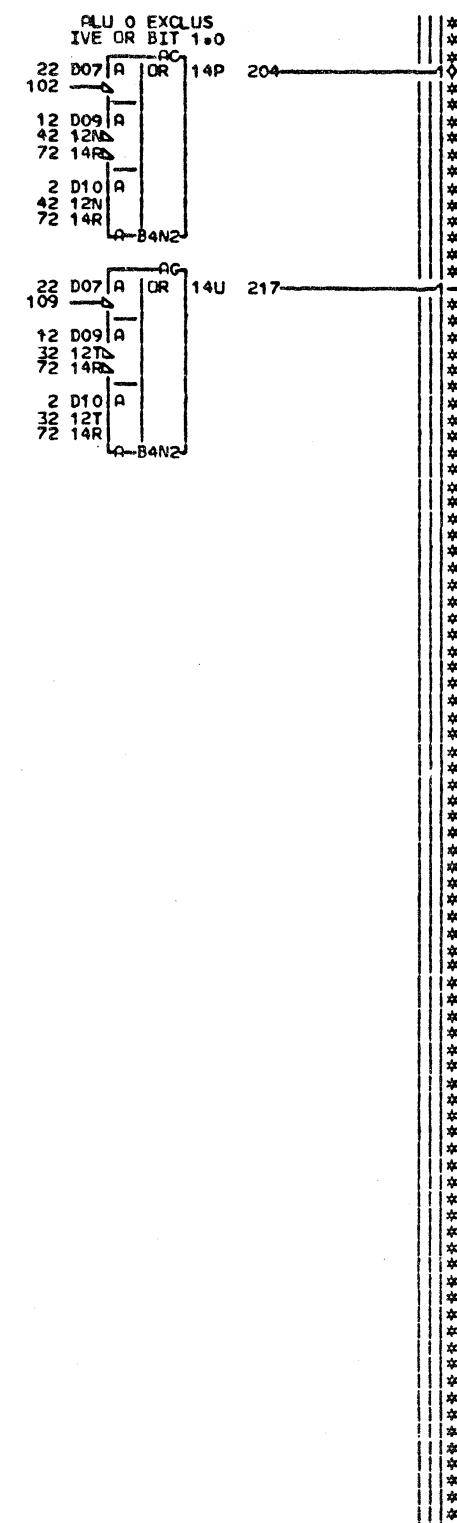
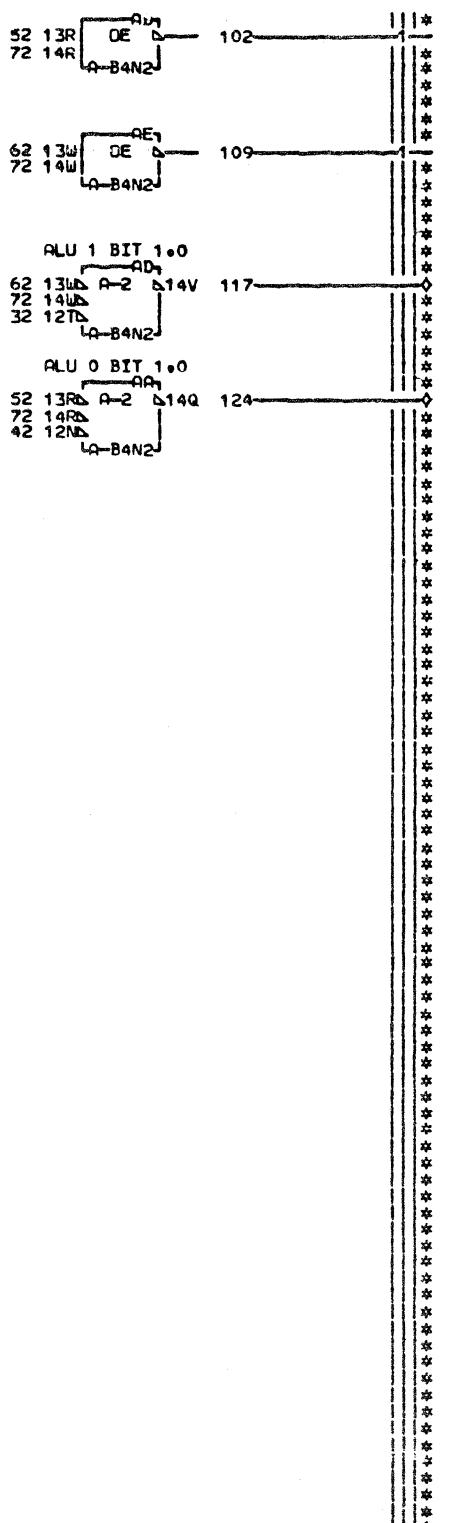
+ B REG BIT 1<sub>0</sub> TO ALU 1 — DK002EK2— 32-21

+ B REG BIT 1<sub>0</sub> TO ALU 0 — DK002FK2— 42-121

+ ALU 0 CARRY BIT 1<sub>0</sub> — DK009DA6— 52-2

+ ALU 1 CARRY BIT 1<sub>0</sub> — DK009EG6— 62-2

+ A REG BIT 1<sub>0</sub> — DK975GH6— 72-44



000 DK008  
124 + ALU 0 CARRY BIT 1<sub>0</sub> — DK976-DA6

204 + ALU 0 EXCLUSIVE OR BIT 1<sub>0</sub> — ED2  
4-DK974

117 + ALU 1 CARRY BIT 1<sub>0</sub> — DK976-EG6

304 - ALU 0 SUM BIT 1<sub>0</sub> — FF2  
4-DG974 LDK974 LDK976

311 - ALU 1 SUM BIT 1<sub>0</sub> — GM2  
4-DK974 LDK976

LOC. TYPE  
A-B4N2 6801

ALU 0 AND ALU 1 BIT 1 <sub>0</sub> —E-C-HISTORY—E-MACH-3705		
FRAME 01		
DATE 10-14-80	LAST EC 344270	DK008
IBM CORP. SCD	P.N. 1859600	000

DK008  
000

+ ALU AND CONTROL BYTE 1 — CA004EC6- 2-2

+ ALU OR CONTROL BYTE 1 — CA004EG6- 12-2

+ ALU ADD CONTROL BYTE 1 — CA004EK6- 22-2

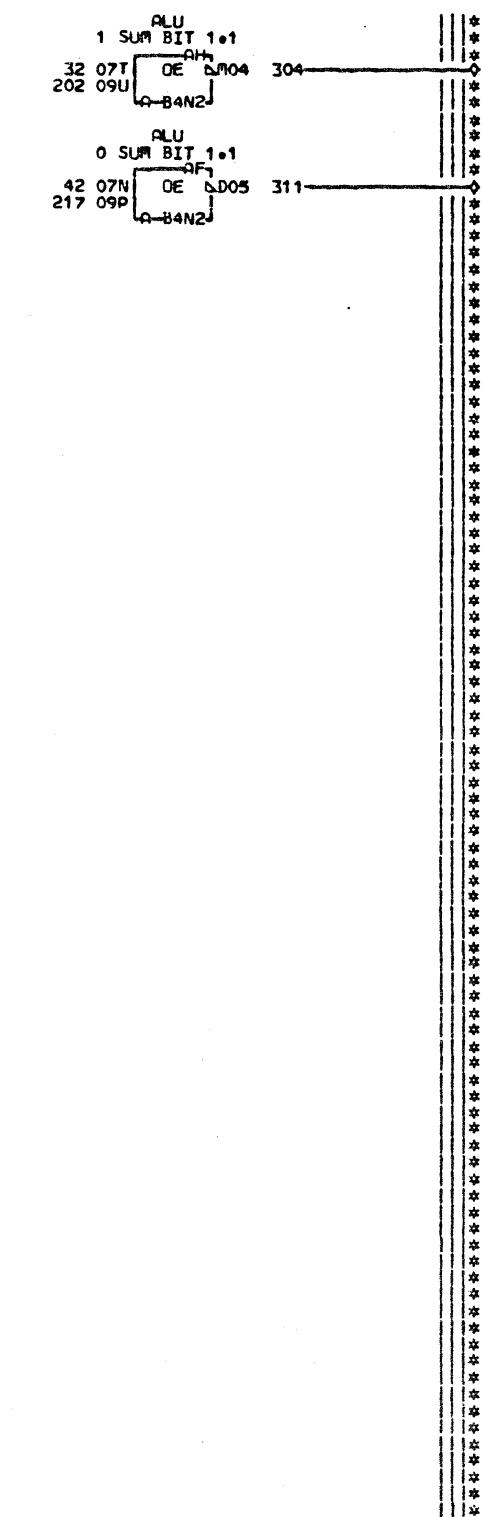
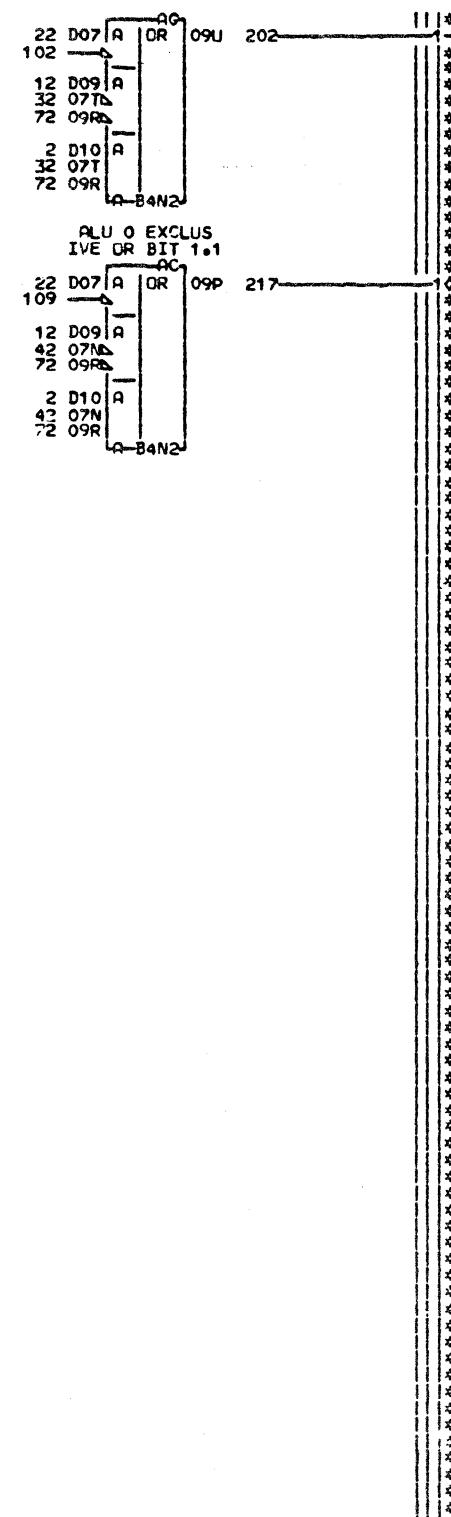
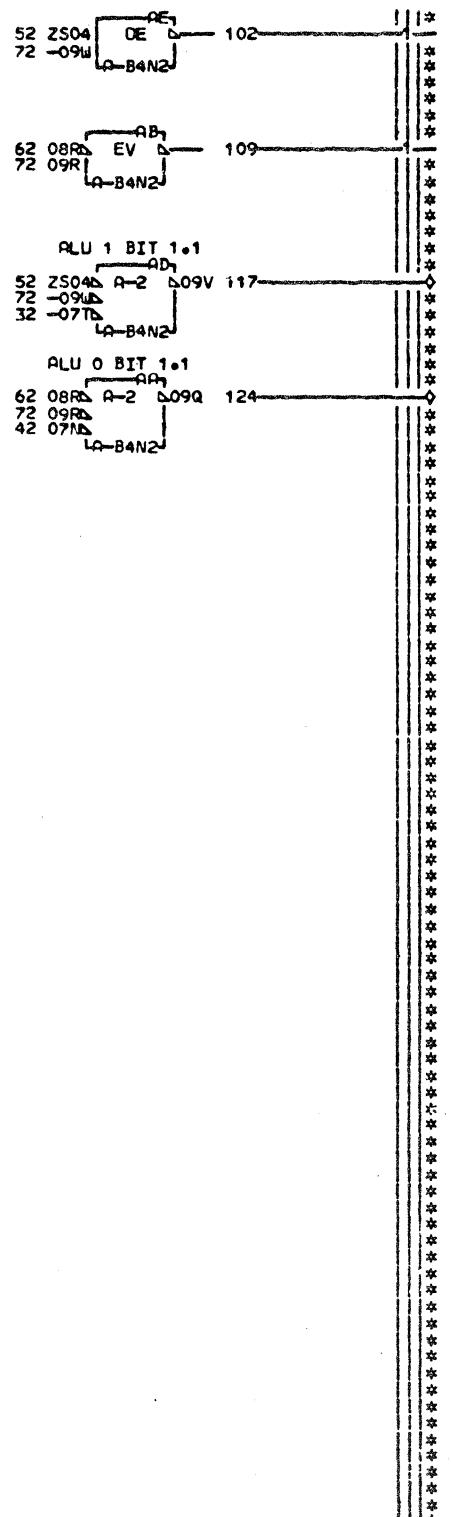
+ B REG BIT 1·1 TO ALU 1 — DK002EM2- 32-21

+ B REG BIT 1·1 TO ALU 0 — DK002FM2- 42-121

+ TIE UP — DK002GF4- 52-2

- FLOAT — DK009001- 62-2

+ A REG BIT 1·1 — DK975GM6- 72-44

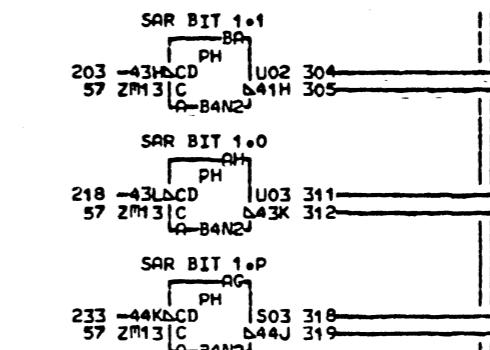
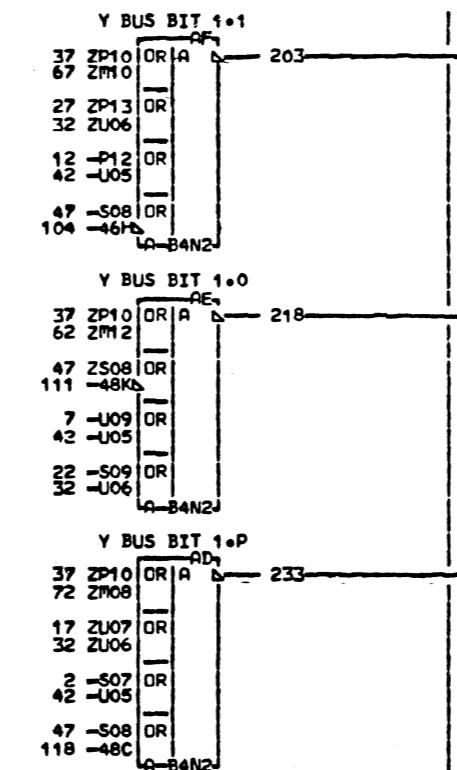
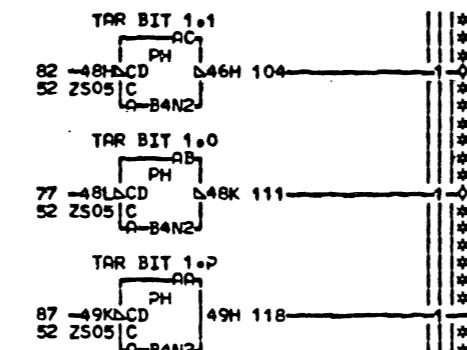


DK009  
000

LOC. TYPE  
A-B4N2 6801

ALU 0 AND ALU 1 BIT 1·1	
E-C HISTORY	E-MACH 3705
FRAME	01
IBM CORP SCD	DK009
DATE LAST EC 10-14-80 344270	
P/N 1859601 000	

+ INBUS BYTE 1 BIT P AA001DD5# 2-1  
 + INBUS BYTE 1 BIT 0 AA001DD7# 7-1  
 + INBUS BYTE 1 BIT 1 AA001DE2# 12-1  
 + ADBUS BIT 1.P AA003DD5# 17-1  
 + ADBUS BIT 1.0 AA003DD7# 22-1  
 + ADBUS BIT 1.1 AA003DE2# 27-1  
 - GATE ADBUS TO Y BUS CS004CA6- 32-3  
 - GATE CCU INDATA TO Y BUS CS004DB2- 37-7  
 - GATE INBUS TO Y BUS CS004FG2- 42-3  
 - GATE TAR TO Y BUS CS004FJ2- 47-7  
 + SET TAR CS007CH2- 52-3  
 + SET SAR CS007EB2- 57-3  
 + CCU INDATA BIT 1.0 CU012DC4- 62-1  
 + CCU INDATA BIT 1.1 CU012DD4- 67-1  
 + CCU INDATA BIT 1.P CU013GF6# 72-1  
 - Z REG BIT 1.0 DK974DB2- 77-1  
 - Z REG BIT 1.1 DK974DB7- 82-1  
 - Z BUS BIT 1.P DK976BA2- 87-1



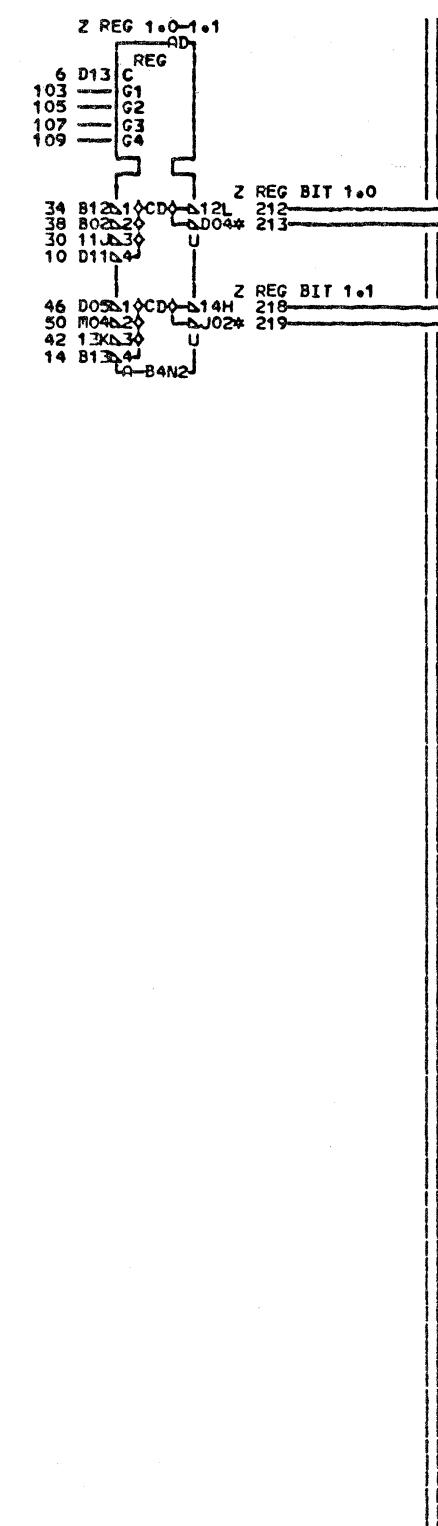
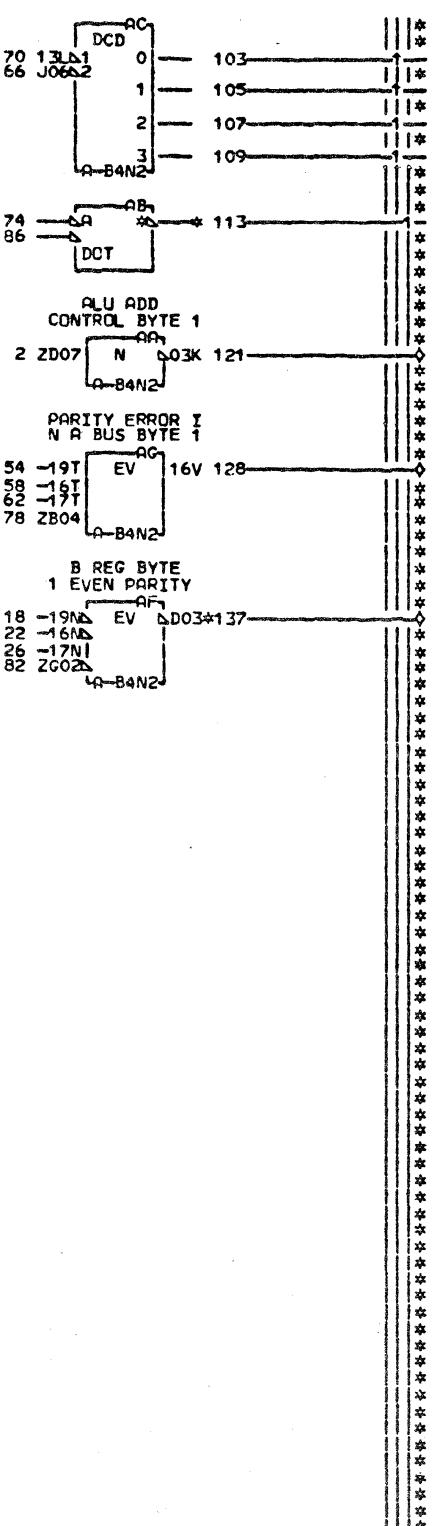
000 DK971  
 111 - TAR BIT 1.0 DK003-A6  
 104 - TAR BIT 1.1 DK003-A6  
 233 - Y BUS BIT 1.P DK002-DB4  
 218 - Y BUS BIT 1.0 DK002-DF4  
 203 - Y BUS BIT 1.1 DK002-DK4  
 318 + SAR BIT 1.P DS001-EC2  
 319 - SAR BIT 1.P DK975-EC6  
 311 + SAR BIT 1.0 EC2  
 OCW002 LCW012 LCW014 LD5001  
 LDT001 LDU001 LDV001  
 312 - SAR BIT 1.0 ED6  
 LDK002 LDK975  
 304 + SAR BIT 1.1 EL2  
 OCW002 LCW012 LCW014 LD5001  
 LDT001 LDU001 LDV001  
 305 - SAR BIT 1.1 EL6  
 LDK002 LDK975

EDGE CONN.  
 2 RESISTOR A-B4N2S09  
 A-B4N2S07  
 7 RESISTOR A-B4N2P13  
 A-B4N2U09  
 12 RESISTOR A-B4N2P12  
 17 RESISTOR A-B4N2U07  
 22 RESISTOR

LOC. TYPE  
 A-B4N2 6801

SAR TAR AND Y BUS ASSEMBLER			
BITS 1.P-1.1			
E.C. HISTORY E MACH 3705			
344270			
FRAME 01			
DATE 06-02-81	LAST EC 344828	IBM CORP.SCD DK971	P.N. 1859602 000

+ ALU ADD CONTROL BYTE 1—CA004EK6— 2-1  
 + T2+T3 SET Z-REG BYTE 1—CC006FG3\* 6-1  
 - ALU 0 SUM BIT 0-0—DG008FF2— 10-1  
 - ALU 0 SUM BIT 0-1—DG009FF2— 14-1  
 - B REG BIT 1-P—DK002DH2— 18-1  
 - B REG BIT 1-0—DK002DH\*— 22-1  
 + B REG BIT 1-1 TO PLU 0—DK002FM2— 26-1  
 + ALU 0 EXCLUSIVE OR BIT 1-0—DK008ED2— 30-1  
 - ALU 0 SUM BIT 1-0—DK008FF2— 34-1  
 - ALU 1 SUM BIT 1-0—DK008GM2— 38-1  
 + ALU 0 EXCLUSIVE OR BIT 1-1—DK009ED2— 42-1  
 - ALU 0 SUM BIT 1-1—DK009FF2— 46-1  
 - ALU 1 SUM BIT 1-1—DK009GM2— 50-1  
 + A REG BIT 1-P—DK975GD6— 54-1  
 + A REG BIT 1-0—DK975GH6— 58-1  
 + A REG BIT 1-1—DK975GM6— 62-1  
 - Z BUS BITS 1-0-1-7 SELECT 2—DK976CB2— 66-1  
 - Z BUS BITS 1-0-1-1 SELECT 1—DK976CC2— 70-1  
 - Z BUS BITS 1-2-1-4 ZERO—DL004GM6— 74-1  
 + DDD PARITY A REG BIT 1-2-1-7—DL006FA2— 78-1  
 - DDD PARITY B REG 1-2-1-7—DL006FD2— 82-1  
 - Z BUS BITS 1-5-1-7 ZERO—DP004GM6— 86-1



000 DK974  
121 - ALU ADD CONTROL BYTE 1—DK976-AC2

212 - Z REG BIT 1-0—  
LDK002 LDK003 LDK971—DB2

218 - Z REG BIT 1-1—  
LDK002 LDK003 LDK971—DB7

213 - Z BUS BIT 1-0—  
QCK001 LCR001 LCU005 LCX009—  
LDK976 LDQ001 EB6

219 - Z BUS BIT 1-1—  
QCK001 LCP002 LCR001 LCU006—  
LCU014 LCX009 LDK976 LDQ001 EH6

304 - Z BUS BYTE 1 ZERO—  
CZ001-FK6

137 - B REG BYTE 1 EVEN PARITY—  
LCK003 LCG002 GG2

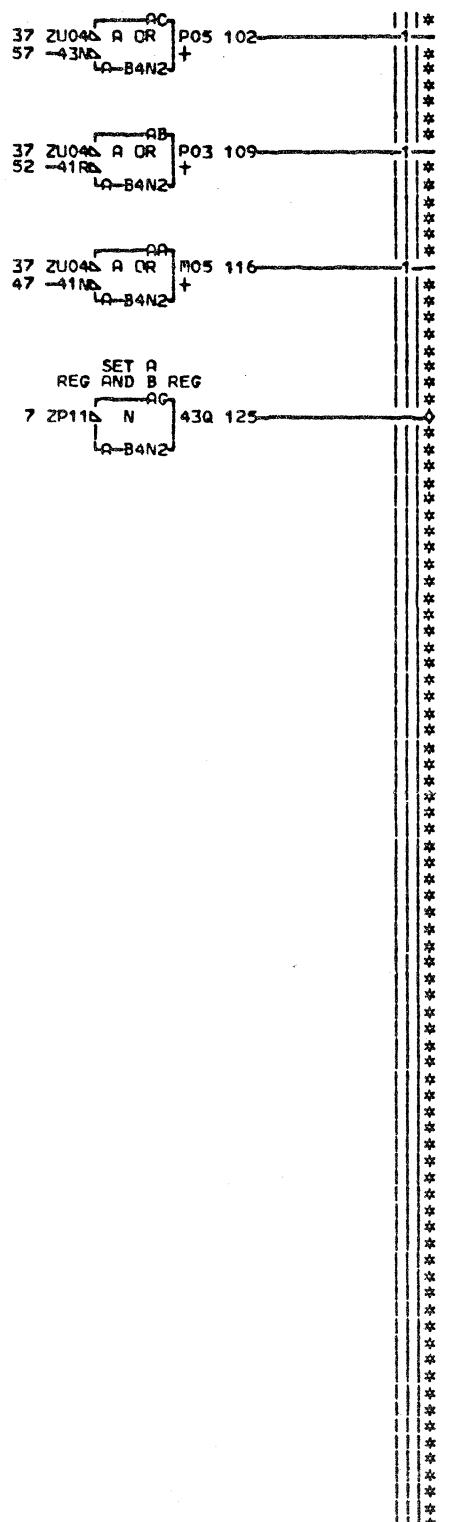
128 + PARITY ERROR IN A BUS BYTE 1—GM2  
LDK976

EDGE CONN. 01A-B3F6B04  
6 RESISTOR 304 RESISTOR  
A-B4N2D13 A-B3G2J05  
113 RESISTOR 01A-B4N6D04  
A-B4N2D02 01A-B3M1D13  
137 P-B4K6H04  
01A-B3K1A13  
213 P-B4F1B11  
01A-B3F6B02  
219 P-B4F1B13

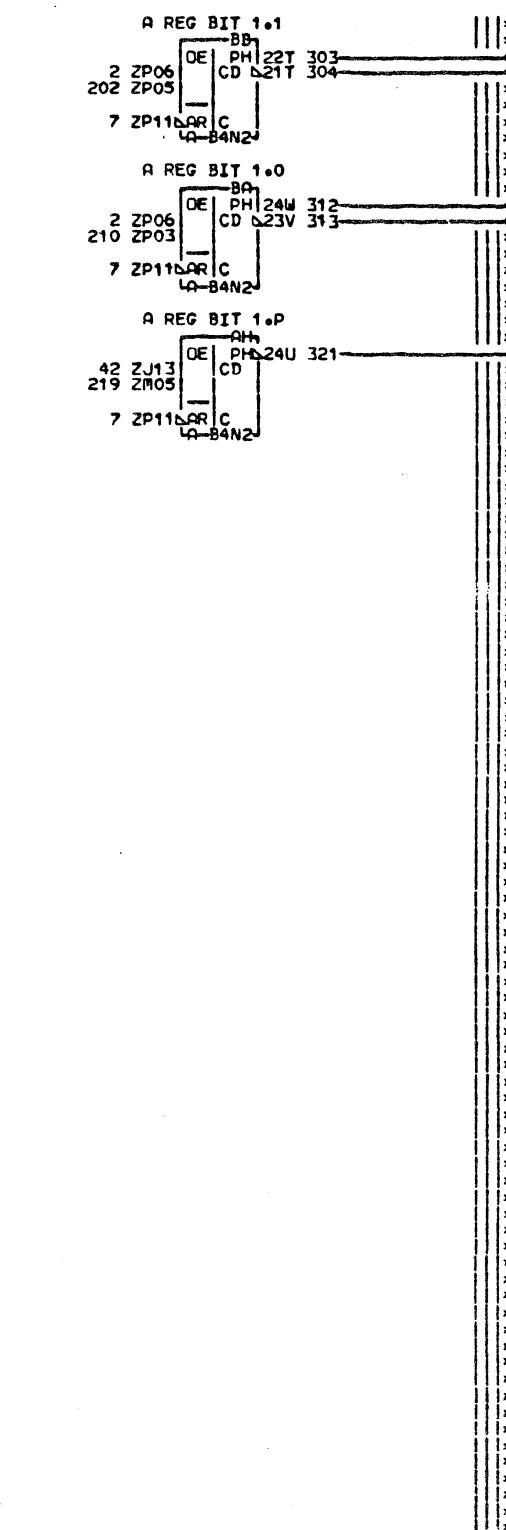
LDC. TYPE  
P-B3G2 Y702  
A-B4N2 6801

ALU 0 ALU 1 AND Z REG BITS 1-P-1-1	E-C-HISTORY	E-MACH-3705
	FRAME	01
	IBR CORP-SCD	DK974
DATE 10-14-60	LAST EC 344270	P.N. 1639603 000

- COMPLEMENT A BUS CA004DD2- 2  
 - TO+T1 TIME SET A-B REGS CC007HK4- 7-3  
 + FORCE ADCON BIT 1=0 CF001AA6- 12-1  
 + FORCE A BUS BIT 1=0 CF001BB2- 17-1  
 + FORCE A BUS BIT 1=1 CF001BC2- 22-1  
 + FORCE A BUS BIT 1=P CF001BM6- 27-1  
 + SDR BIT 1=P TO A BUS CF003FL2- 32-1  
 - GATE SAR TO A BUS CS004BK6- 37-3  
 - COMPLEMENT A BUS BIT 1=P CS006CD6- 42-1  
 - SAR BIT 1=P DK971EC6- 47-1  
 - SAR BIT 1=0 DK971EC6- 52-1  
 - SAR BIT 1=1 DK971EL6- 57-1  
 + SHIFT RIGHT BIT 1=0 TO A BUS DQ003BB2- 62-1  
 + SDR BIT 1=0 TO A BUS DQ003BC2- 67-1  
 + SHIFT RIGHT BIT 1=1 TO A BUS DQ003CD2- 72-1  
 + SDR BIT 1=1 TO A BUS DQ003CE2- 77-1  
 + BIT N=P TO A BUS DR994CL2- 82-1



LOC. TYPE  
A-B4N2 6801

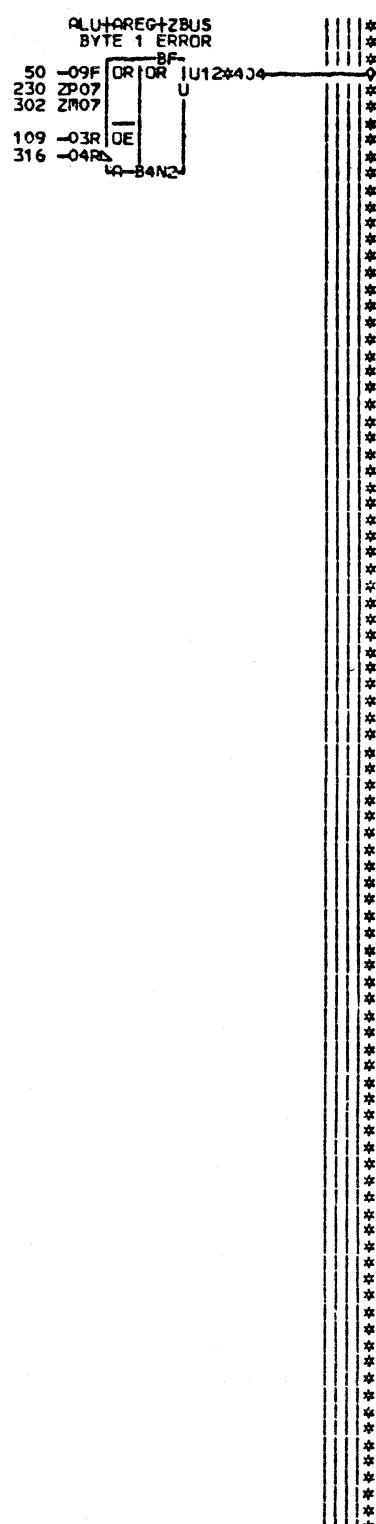
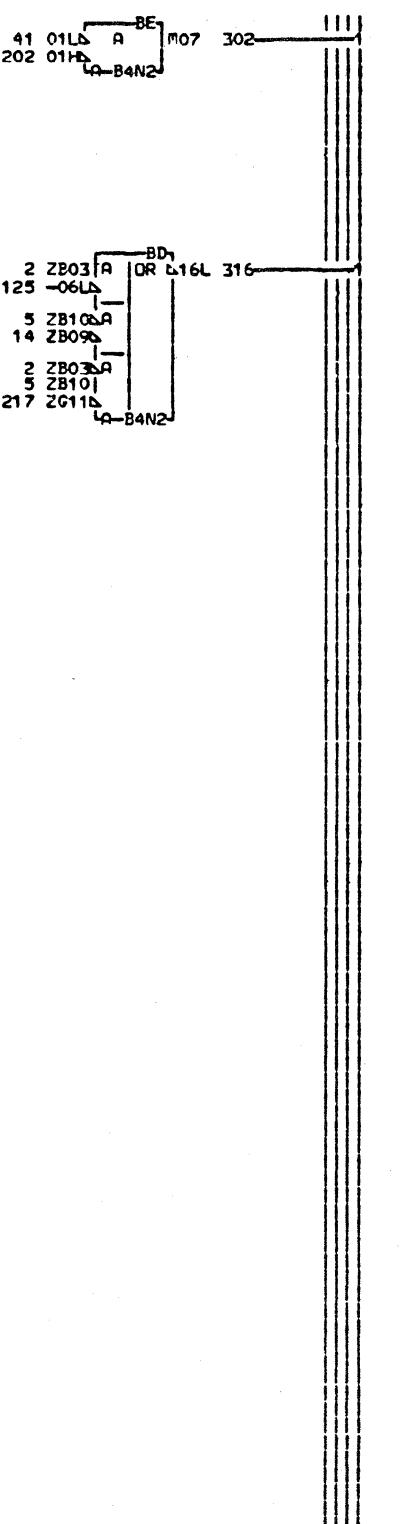
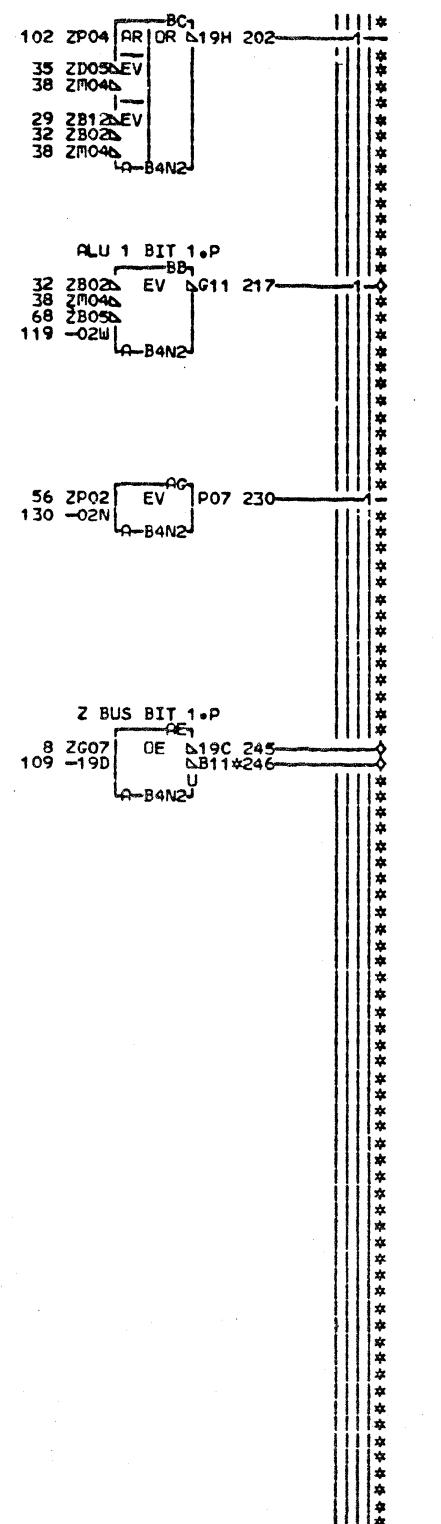
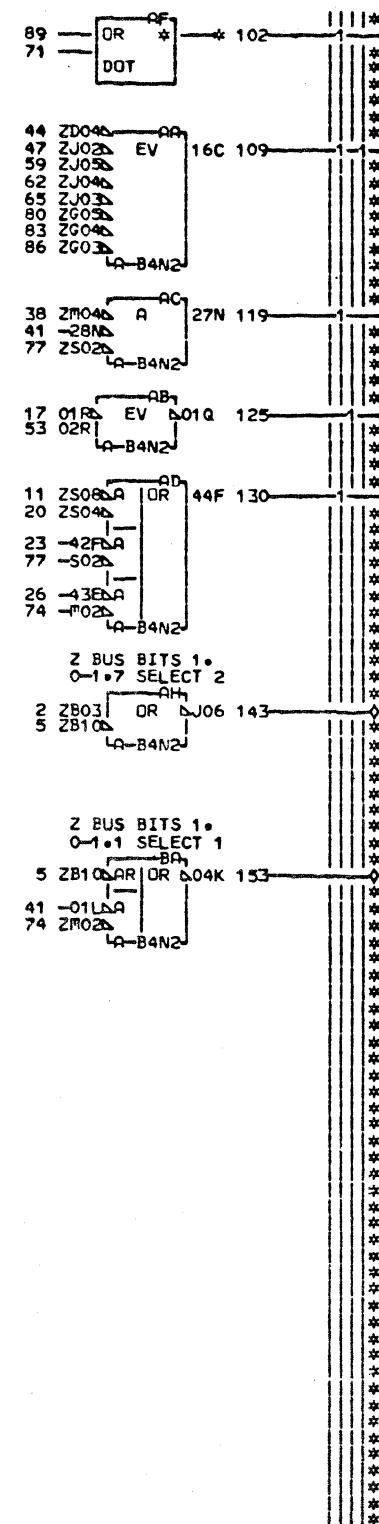


000 DK975  
 125 + SET A REG AND B REG DK002-EA2  
 321 + A REG BIT 1=P CD6  
 4DK974 4DK976  
 312 - A REG BIT 1=0 DK977-GH2  
 313 + A REG BIT 1=0 GH6  
 4DK008 4DK974  
 303 - A REG BIT 1=1 DK977-GM2  
 304 + A REG BIT 1=1 GM6  
 4DK009 4DK974

A BUS ASSEMBLER	BITS 1=P-1=1	E MACH-3705
-E.C. HISTORY		
FRAME	01	
DATE LAST EC		DK975
10-14-80 344270		P.N. 1859604 000

DK975  
000

+ EXCLUSIVE OR ALL CA003HE6 2-1-2  
 - CROSS HI TO LO CA003HH2 5-2-2  
 + FORCE Z BUS PARITY ERROR CK001GF2 8-1  
 - GATE TAR TO Y BUS CS004FJ2 11-1  
 - ALU 1 BIT 0.P DG976CG2 14-1  
 - B REG BIT 1.P DK002DH2 17-1  
 + TIE UP DK002GF4 20-1  
 + ALU 0 CARRY BIT 1.0 DK008DA6 23-1  
 + ALU 1 CARRY BIT 1.0 DK008EG6 26-1  
 - ALU 0 SUM BIT 1.0 DK008FF2 29-1  
 - ALU 1 SUM BIT 1.0 DK008GM2 32-2  
 - ALU 0 SUM BIT 1.1 DK009FF2 35-1  
 - ALU 1 SUM BIT 1.1 DK009GM2 38-13  
 - ALU ADD CONTROL BYTE 1 DK974AC2 41-2-1  
 - Z BUS BIT 1.0 DK974EB6 44-1  
 - Z BUS BIT 1.1 DK974EH6 47-1  
 + PARITY ERROR IN A BUS BYTE 1-DK974GM2- 50-  
 + A REG BIT 1.P DK975GD6 53-1  
 + CARRY LA FROM BITS 1.0-1.4 DK977EG6 56-1  
 - Z BUS BIT 1.2 DL004GB6 59-1  
 - Z BUS BIT 1.3 DL004GF6 62-1  
 - Z BUS BIT 1.4 DL004GK6 65-1  
 - ODD PARITY PREDICT 1.2-1.7 DL006DF8 68-1  
 + ADD ERROR IN BITS 1.2-1.4 DL006DJ6 71-1  
 - CARRY LA FROM BITS 1.2-1.4 DL007DD2 74-2  
 + CARRY LA FROM BITS 1.2-1.4 DL007DD6 77-2  
 - Z BUS BIT 1.5 DM004GB6 80-1  
 - Z BUS BIT 1.6 DM004GF6 83-1  
 - Z BUS BIT 1.7 DM004GK6 86-1  
 + ADD ERROR IN BITS 1.5-1.7 DM006DJ6 89-1



000 DK976  
245 - Z BUS BIT 1.P LDK002 LDK971 BA2

143 - Z BUS BITS 1.0-1.7 SELECT 2 CD2 LDK974 LD004 LD004

153 - Z BUS BITS 1.0-1.1 SELECT 1 CC2 LDK974

217 - ALU 1 BIT 1.P DG976-CG2

246 - Z BUS BIT 1.P LDR992 EA6

404 + ALU+REG+ZBUS BYTE 1 ERROR EJ6 CK003

ALU CHECK BITS 1.P-1.1	E.C.-HISTORY	E.MACH-3705
FRAME 01	DATE 10-14-80	LAST EC 344270
IBM CORP-SCD	P.N. 1859605	DK976
LOC. TYPE		
R-B3N2 6819		
R-B4N2 6801		

+ T2+T3 SET Z-REG BYTE 1 — CC006FG3— 2-1

- B REG BIT 1.0 — DK002DH5— 12-11

- B REG BIT 1.1 — DK002DH8— 22-11

- A REG BIT 1.0 — DK975GH2— 32-

- A REG BIT 1.1 — DK975GM2— 42-11

- CARRY PROPOGATE 1.2-1.4 — DL007CJ6— 52-

- CARRY GENERATE 1.2-1.4 — DL007DB2— 62-

- CARRY LA FROM BITS 1.2-1.4 — DL007DD2— 72-

12 19U<sub>A</sub> DE 19V 102 — 12  
32 18U<sub>A</sub> LA-B4N2

22 19R<sub>B</sub> DE 19Q 109 — 2  
42 18R<sub>B</sub> LA-B4N2

2 ZD13 N 29V 116 —  
A-B4N2

32 28H<sub>A</sub> R OR 29K 202 —  
12 29H<sub>A</sub>

42 29L<sub>B</sub> R OR 102 28K<sub>B</sub>  
LA-B4N2

CARRY LA FROM  
BITS 1.0-1.4  
202 -29E AR OR 603 304  
62 ZG120A  
102 -29P0  
109 -29R0  
52 ZG120A  
72 ZM020A  
102 -26D0  
109 -26O0  
LA-B4N2

305 ZP025 A OR GK  
116 -27H<sub>A</sub> 1002 305  
2 -D13 R J12#406  
406 -J125 A LA-B4N2

000 DK977  
304 - CARRY LA FROM BITS 1.0-1.4 — EG2  
LDJ016 LDJ017

305 + CARRY LA FROM BITS 1.0-1.4 — EG6  
LDJ016 LDK976

406 + 1.0 CARRY HOLDOVER — GK6  
LCZ002 LCZ003

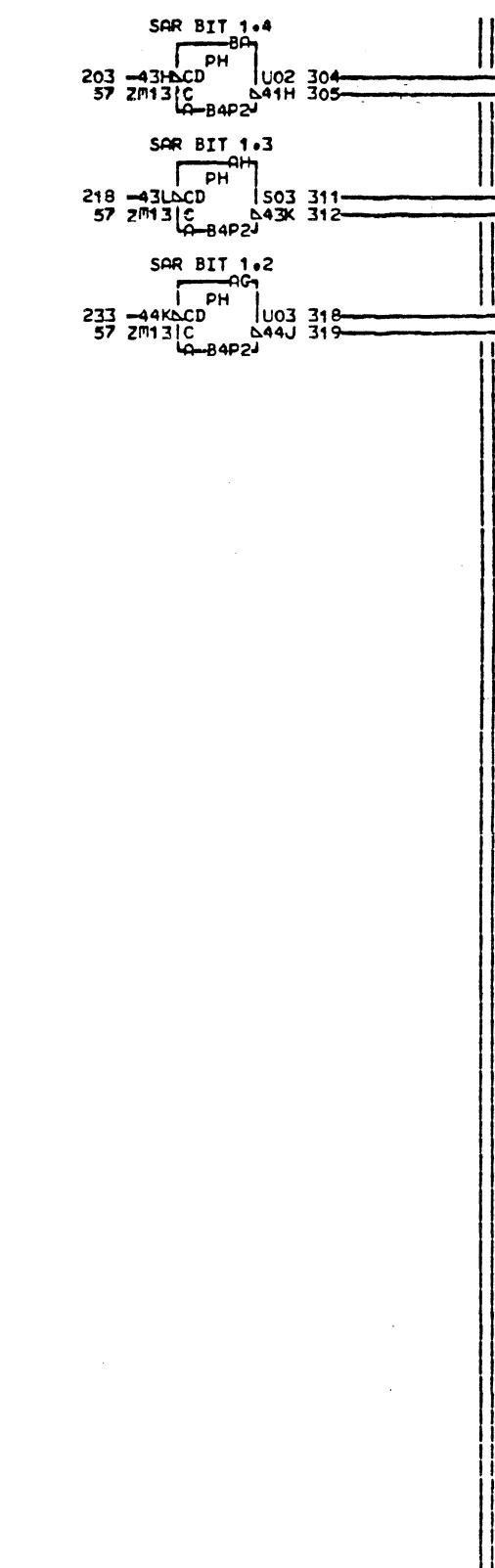
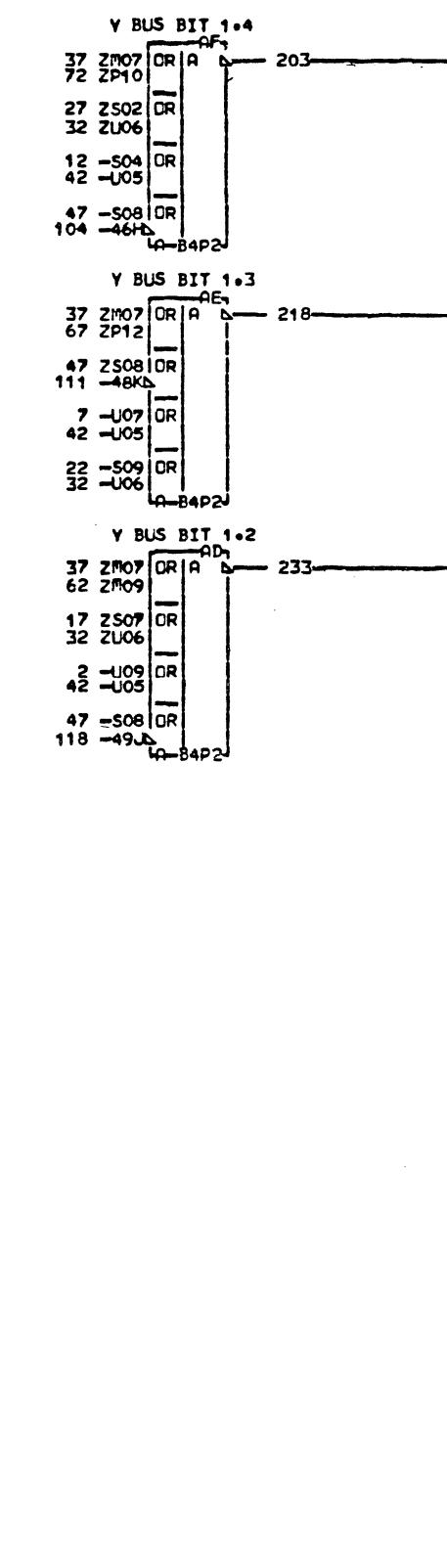
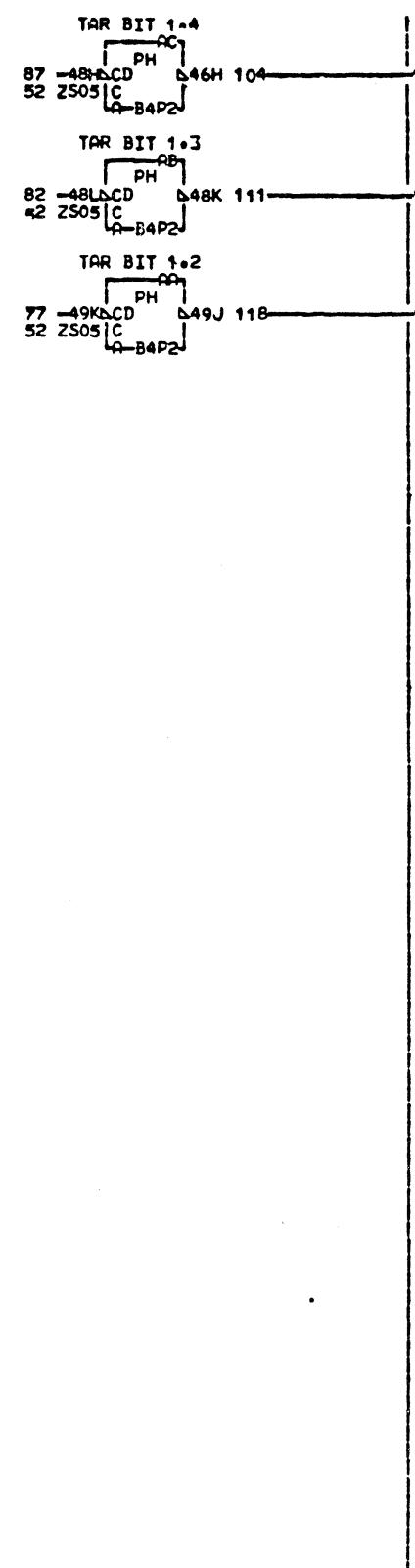
EDGE CONN.  
406 A-B4K1B13  
01P-B3K6B04

LOC. TYPE  
A-B4N2 6801

5-BIT CARRY LOOKHEAD	
BITS 1.0-1.1	
E.C.—HISTORY—E MACH.3705	
FRAME	01
DATE	LAST EC
1C-14-80	344270
IBM CORP. SCD	DK977
P.o.N.	1859606
000	

DK977  
000

+ INBUS BYTE 1 BIT 2 AA001DE4# 2  
 + INBUS BYTE 1 BIT 3 AA001DE6# 7  
 + INBUS BYTE 1 BIT 4 AA001DF1# 12  
 + ADBUS BIT 1.2 AA003DE4# 17  
 + ADBUS BIT 1.3 AA003DE6# 22  
 + ADBUS BIT 1.4 AA003DF1# 27  
 - GATE ADBUS TO Y BUS CS004CA6- 32-3  
 - GATE CCU INDATA TO Y BUS CS004D52- 37-3  
 - GATE INBUS TO Y BUS CS004FG2- 42-3  
 - GATE TAR TO Y BUS CS004FJ2- 47-3  
 + SET TAR CS007CH2- 52-3  
 + SET SAR CS007EB2- 57-3  
 + CCU INDATA BIT 1.2 CU012DE4- 62-1  
 + CCU INDATA BIT 1.3 CU012DG4- 67-1  
 + CCU INDATA BIT 1.4 CU012DJ4- 72-1  
 - Z REG BIT 1.2 DL004FB2- 77-1  
 - Z REG BIT 1.3 DL004FB7- 82-1  
 - Z REG BIT 1.4 DL004FK2- 87-1



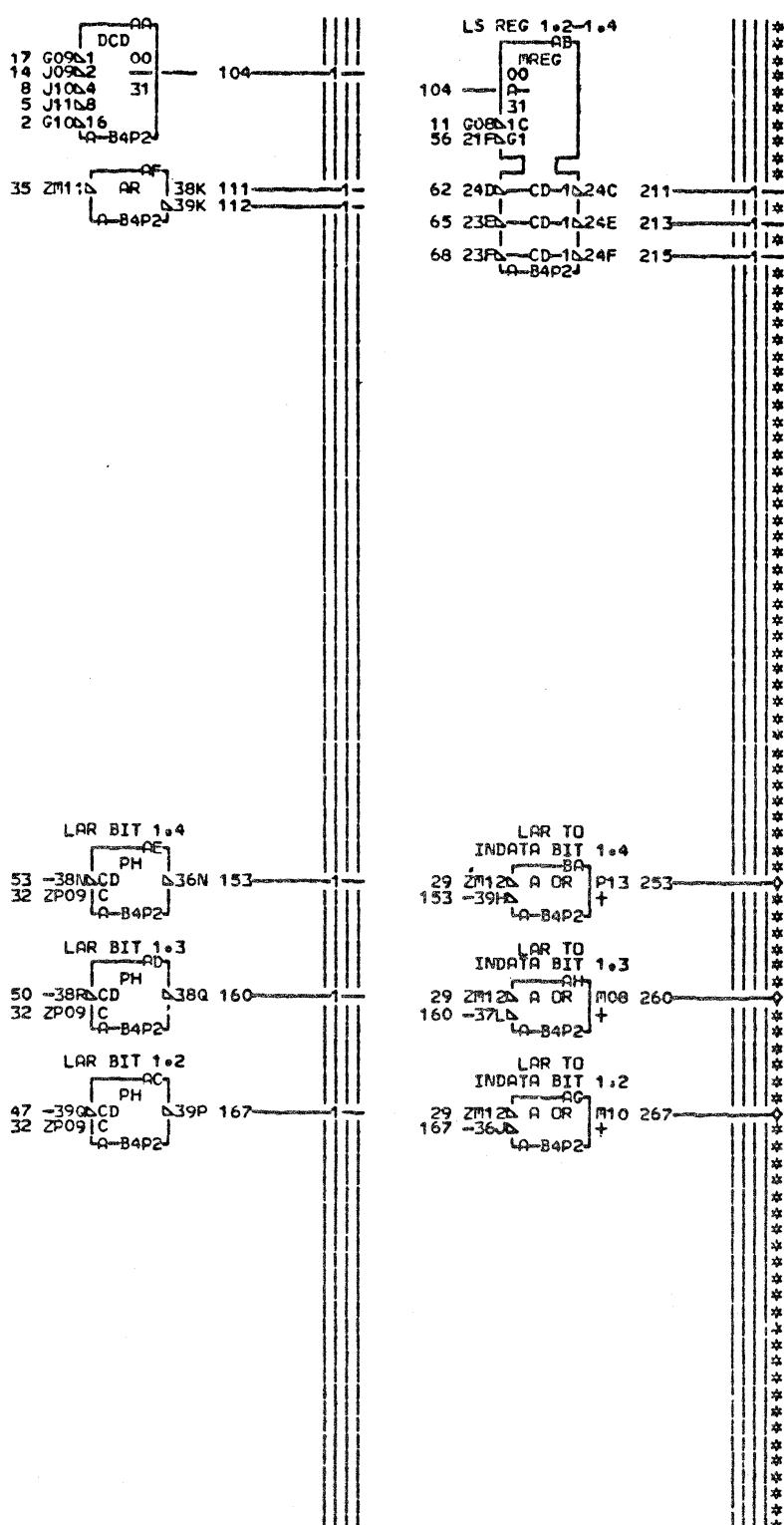
000 DL001  
 118 - TAR BIT 1.2 DL003-AC6  
 111 - TAR BIT 1.3 DL003-AG6  
 104 - TAR BIT 1.4 DL003-AM6  
 233 - Y BUS BIT 1.2 DL002-DC4  
 218 - Y BUS BIT 1.3 DL002-DH4  
 203 - Y BUS BIT 1.4 DL002-DL4  
 318 + SAR BIT 1.2 EC2  
 0CW002 LCU012 LCU014 LDU001 LDV001  
 LDT001 LDU001 LDV001  
 319 - SAR BIT 1.2 EC6  
 LDU002 LDV005  
 311 + SAR BIT 1.3 EH2  
 0CW002 LCU012 LCU014 LDU001 LDV001  
 LDT001 LDU001 LDV001  
 312 - SAR BIT 1.3 EH6  
 LDU002 LDV005  
 304 + SAR BIT 1.4 EK2  
 0CW002 LCU012 LCU014 LDU001 LDV001  
 LDT001 LDU001 LDV001  
 305 - SAR BIT 1.4 EK6  
 LDU002 LDV005

EDGE CONN.  
 2 RESISTOR A-B4P2S09  
 27 RESISTOR A-B4P2S02  
 7 RESISTOR A-B4P2U09  
 12 RESISTOR A-B4P2U07  
 17 RESISTOR A-B4P2S04  
 22 RESISTOR A-B4P2S07

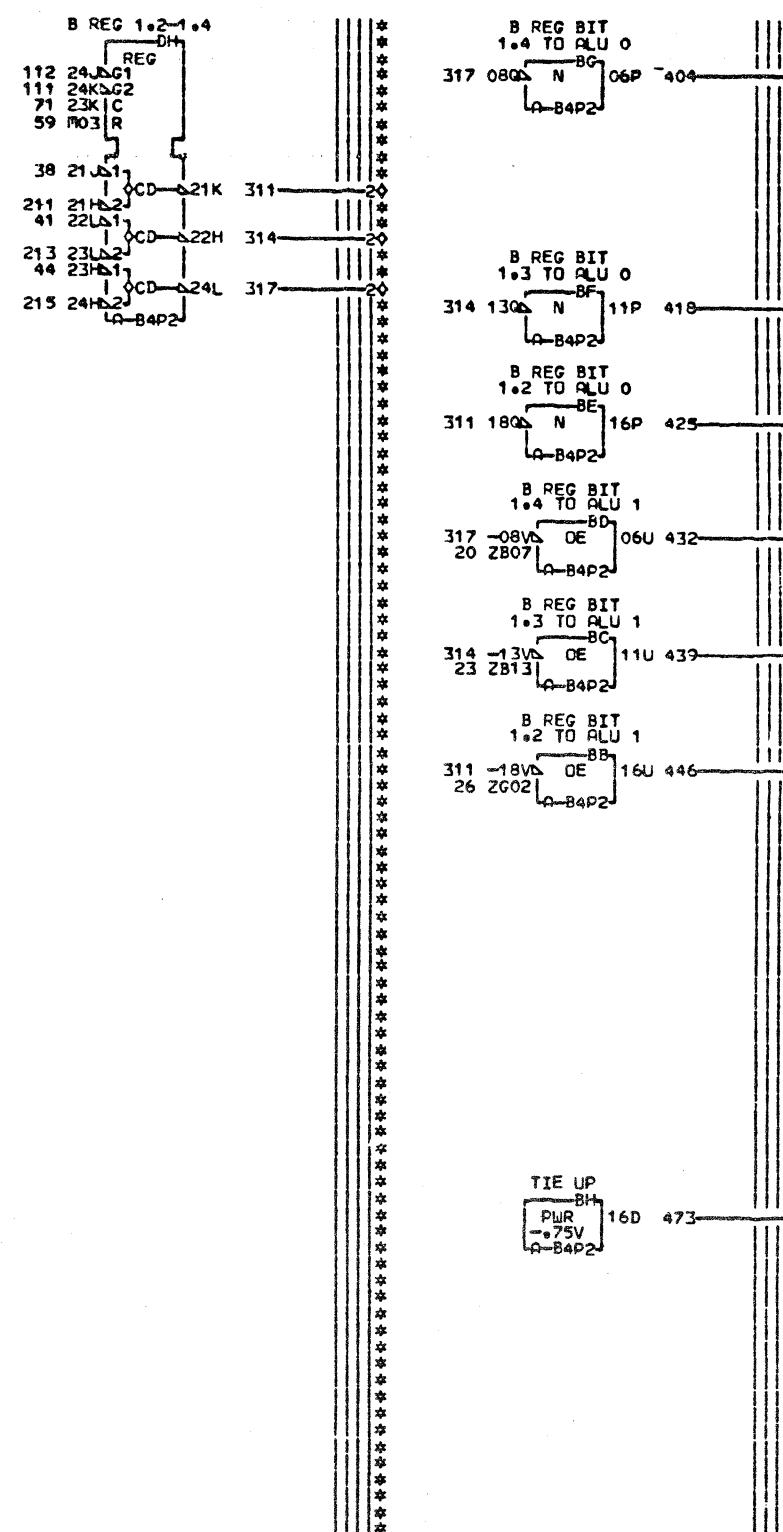
LCC. TYPE  
 A-B4P2 6802

SAR, TAR AND Y BUS ASSEMBLERS  
 BITS 1.2-1.4  
 E.C. HISTORY E MACH. 3705  
 344270  
 FRAME 01  
 IBM CORP. SCD DL001  
 DATE LAST EC 06-02-81 344828 P.N. 1859607 000

- SELECT LS REG GROUP 1+2	CC006AU4	2-1
- SELECT LS REG GROUP 1+3	CC006AV4	5-1
- SELECT LS REG BIT 0+1+2+3	CC006AW4	8-1
- WRITE LS	CC006BJ4	11-1
- SELECT LS REG 0+1+4+5	CC006BK4	14-1
- SELECT LS REG BIT 0+2+4+6	CC006BL4	17-1
+ FORCE ERROR IN BIT 4	CK002DD2	20-1
+ FORCE ERROR IN BIT 3	CK002DH2	23-1
+ FORCE ERROR IN BIT 2	CK002DJ2	26-1
- GATE INPUT 74	CQ004FJ6	29-3
+ SET LAR	CS001DM2	32-3
- GATE Y BUS TO B REG	CS004ED2	35-1
- Y BUS BIT 1+2	DL001DC4	38-1
- Y BUS BIT 1+3	DL001DH4	41-1
- Y BUS BIT 1+4	DL001DL4	44-1
- SAR BIT 1+2	DL001EC6	47-1
- SAR BIT 1+3	DL001EH6	50-1
- SAR BIT 1+4	DL001EK6	53-1
- SELECT FLDAT	DL002002	56-1
- FLDAT	DL002003	59-1
- Z REG BIT 1+2	DL004FB2	62-1
- Z REG BIT 1+3	DL004FB7	65-1
- Z REG BIT 1+4	DL004FK2	68-1
+ SET A REG AND B REG	DL005EA2	71-1

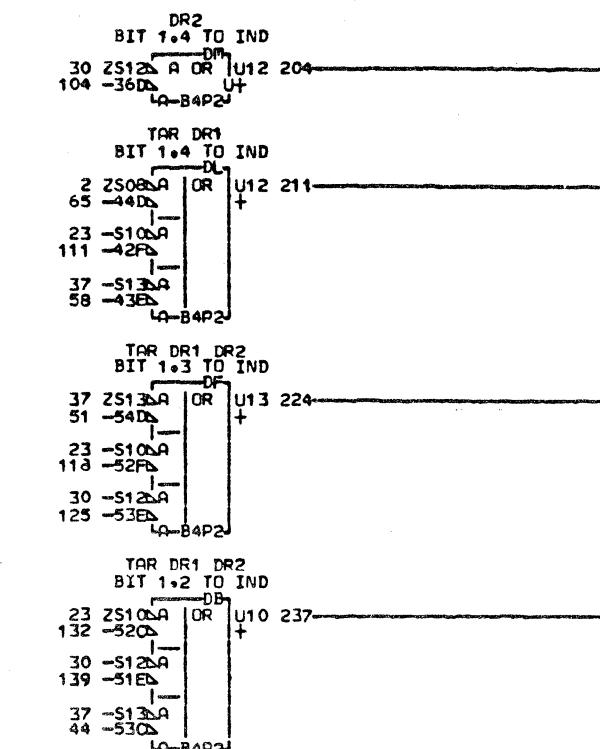
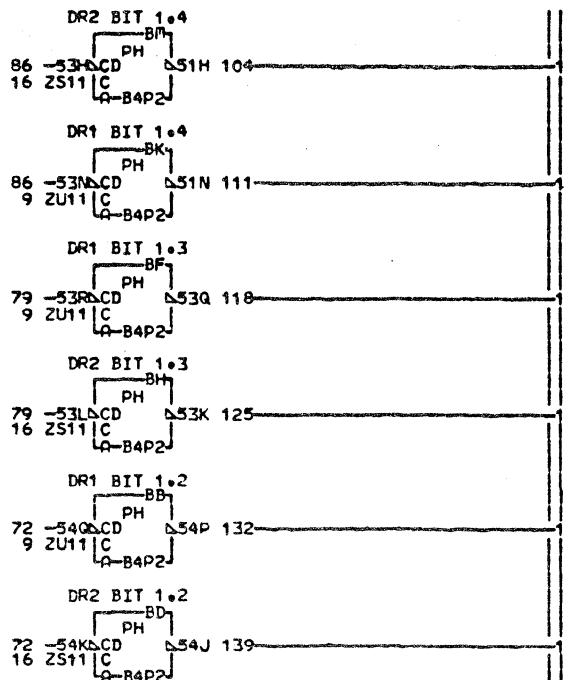


LOC. TYPE  
A-B4P2 6802



B REG LAR AND LOCAL STORE	
BITS 1 <sub>0</sub> 2 1 <sub>0</sub> 3 1 <sub>0</sub> 4	
<del>E.C.-HISTORY</del>	
E.PACH-3705	
DATE	LAST EC
10-14-80	344270
FRAME	01
IBM CORP-SCD	DL002
P.o.N.	1859608
	000

- GATE TAR TO Y BUS CS004FJ2- 2  
 + SET DR1 CS007FC6- 9-3  
 + SET DR2 CS007FD6- 16-3  
 - GATE DISP REG 1 TO IND CU001EK6- 23-3  
 - GATE DISPL REG 2 TO IND CU001EL6- 30-3  
 - GATE TAR TO IND CU001EM6- 37-3  
 - TAR BIT 1-2 DL001AC6- 44  
 - TAR BIT 1-3 DL001AG6- 51  
 - TAR BIT 1-4 DL001AME- 58  
 + TIE UP DL002GF4- 65  
 - Z REG BIT 1-2 DL004FB2- 72-2  
 - Z REG BIT 1-3 DL004FB7- 79-2  
 - Z REG BIT 1-4 DL004FK2- 86-2



000 DL003  
237 + TAR DR1 DR2 BIT 1-2 TO IND DB2  
LA-B4P2

224 + TAR DR1 DR2 BIT 1-3 TO IND DF2  
LA-B4P2

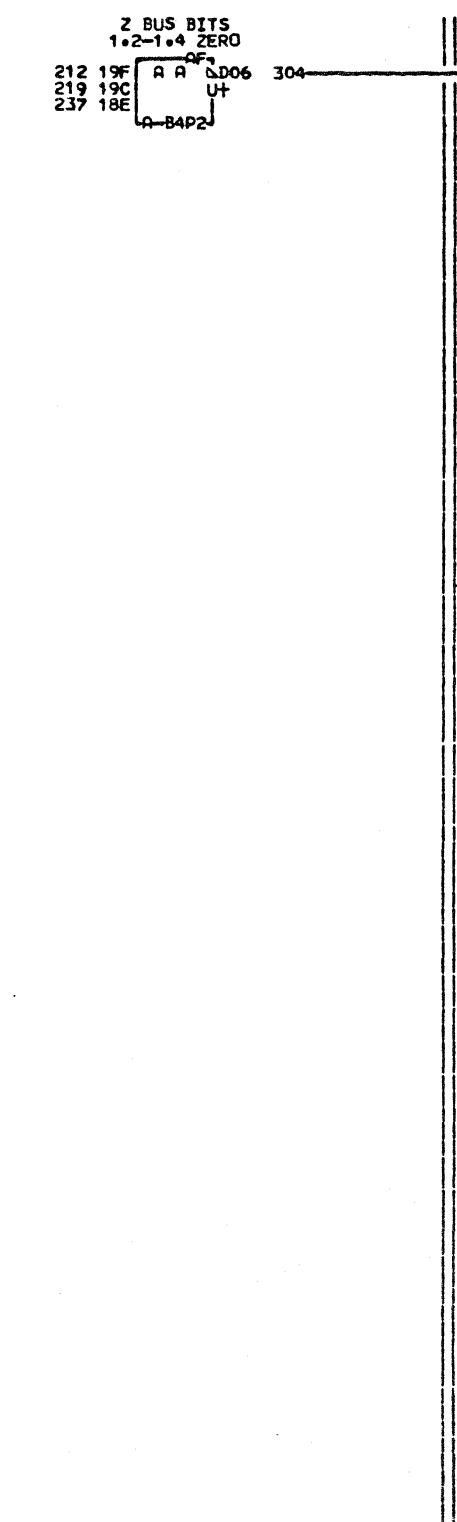
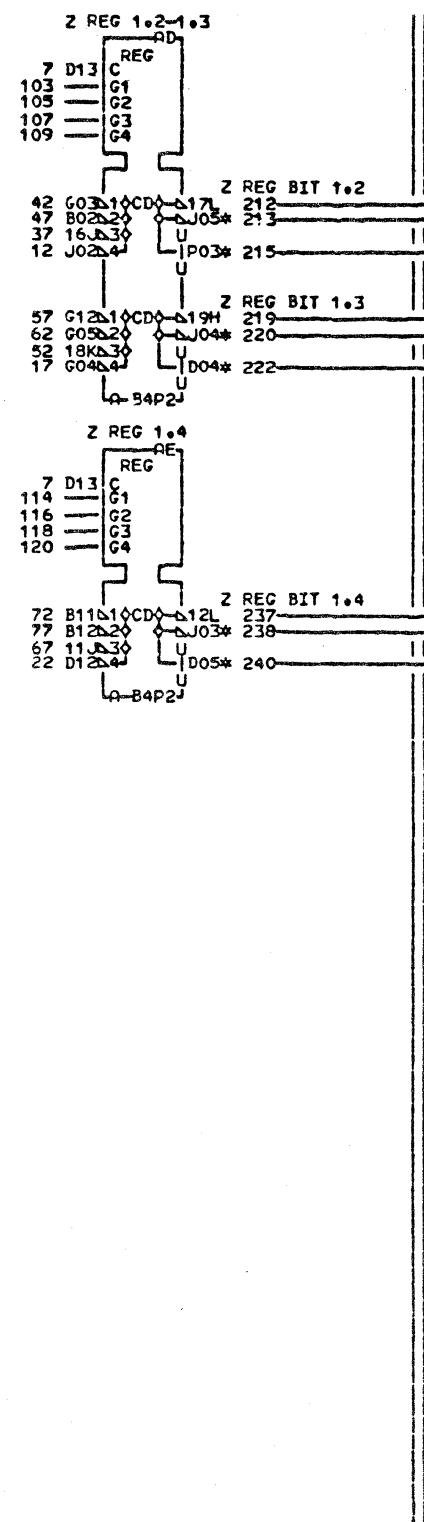
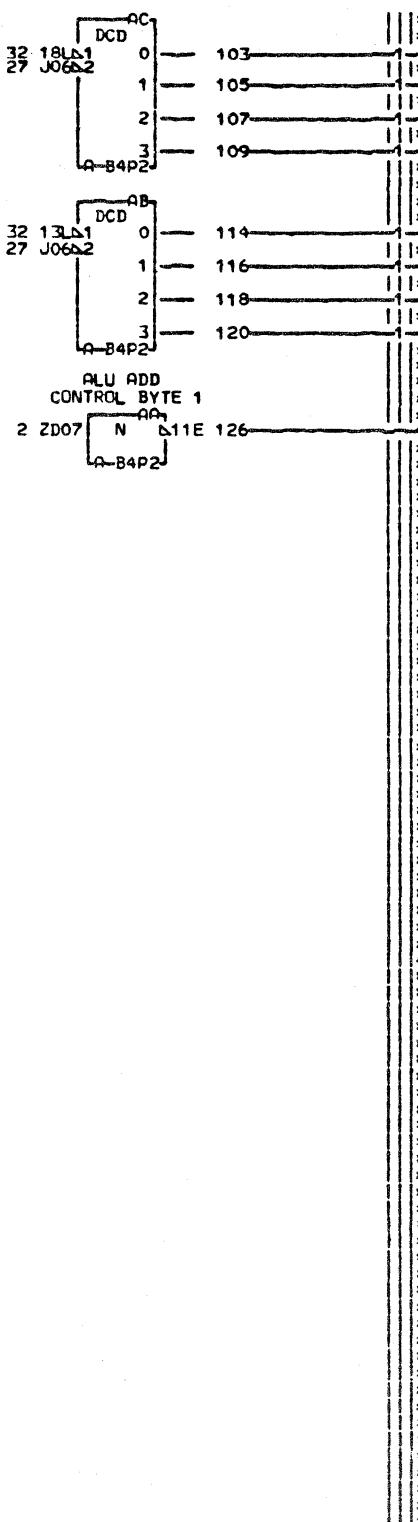
211 + TAR DR1 BIT 1-4 TO IND AP015-DL2

204 + DR2 BIT 1-4 TO IND AP015-DM2

LOC. TYPE  
LA-B4P2 6802

CCU DISPLAY REGISTERS 1 AND 2	
BITS 1-2 1-3 AND 1-4	
Ec HISTORY E-MACH-3705	
FRAME	01
DATE	LAST EC
10-14-80	344270
IBM CORP. SCD	DL003
P.N.	1859609
000	

+ ALU ADD CONTROL BYTE 1—CA004EK6— 2-1  
 + T2+T3 SET Z-REG BYTE 1—CC006FG3— 7-2  
 - ALU 0 SUM BIT 0.2—DH008FF2— 12-1  
 - ALU 0 SUM BIT 0.3—DH009FF2— 17-1  
 - ALU 0 SUM BIT 0.4—DH010FF2— 22-1  
 - Z BUS BITS 1.0-1.7 SELECT 2—DK976CB2— 27-2  
 - Z BUS BITS 1.2-1.4 SELECT A—DL006GL6— 32-2  
 + ALU 0 EXCLUSIVE OR BIT 1.2—DL008ED2— 37-1  
 - ALU 0 SUM BIT 1.2—DL008FF2— 42-1  
 - ALU 1 SUM BIT 1.2—DL008GM2— 47-1  
 + ALU 0 EXCLUSIVE OR BIT 1.3—DL009ED2— 52-1  
 - ALU 0 SUM BIT 1.3—DL009FF2— 57-1  
 - ALU 1 SUM BIT 1.3—DL009GM2— 62-1  
 + ALU 0 EXCLUSIVE OR BIT 1.4—DL010ED2— 67-1  
 - ALU 0 SUM BIT 1.4—DL010FF2— 72-1  
 - ALU 1 SUM BIT 1.4—DL010GM2— 77-1



000 DL004  
 126 - ALU ADD CONTROL BYTE 1—DL006—AC2  
 212 - Z REG BIT 1.2—  
 4DL001 4DL002 4DL003 FB2  
 219 - Z REG BIT 1.3—  
 4DL001 4DL002 4DL003 FB7  
 237 - Z REG BIT 1.4—  
 4DL001 4DL002 4DL003 FK2  
 213 - Z BUS BIT 1.2—  
 CK001 CP002 LCR001 LCU005  
 CK005 CX006 CX009 DK976  
 DQ001  
 215 + OUTBUS BIT 1.2— AA001-GC2  
 220 - Z BUS BIT 1.3— GF6  
 CK001 CP002 LCR001 LCU005  
 CV001 CX006 CX009 DK976  
 DQ002  
 222 + OUTBUS BIT 1.3— AA001-GG2  
 238 - Z BUS BIT 1.4— GK6  
 CP002 CR001 LCU005 CU014  
 CV061 CX006 CX009 DK976  
 DQ002  
 240 + OUTBUS BIT 1.4— AA001-GL2  
 304 - Z BUS BITS 1.2-1.4 ZERO DK974-GM6

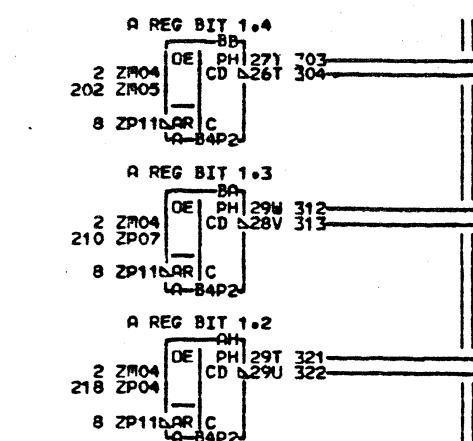
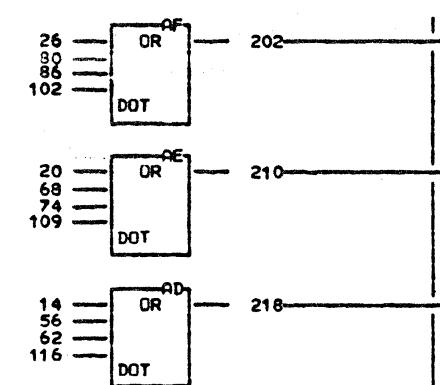
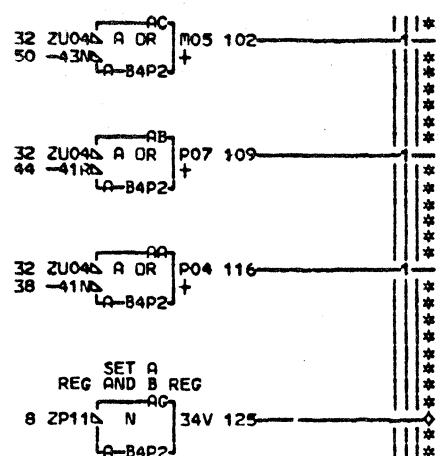
ELC04  
000

EDGE CONN.  
 213 P-B4F1C11  
 01A-B3F6C02  
 215 P-B4U4D06  
 220 P-B4F1C13  
 01A-B3F6C04  
 222 P-B4U4D07  
 238 P-B4F1D11  
 01A-B3F6D02  
 240 P-B4U4D09

LOC. TYPE  
P-B4P2 6802

ALU 0 ALU 1 AND Z REG	
BITS 1.2-1.4	
E-C-HISTORY	E-MACH-3705
	FRAME 01
	IBM CORP-SCD DL004
DATE 10-14-80	LAST EC 344270
	P.N. 1859610 000

- COMPLEMENT A BUS CR004DD2- 2  
 - TOTI TIME SET A-B REGS CC007HK4- 9  
 + FORCE A BUS BIT 1.2 CF001BD2- 14  
 + FORCE A BUS BIT 1.3 CF001BE2- 20  
 + FORCE A BUS BIT 1.4 CF001BF2- 26  
 - GATE SAR TO A BUS CS004BK6- 32  
 - SAR BIT 1.2 DL001EC6- 38  
 - SAR BIT 1.3 DL001EH6- 44  
 - SAR BIT 1.4 DL001EK6- 50  
 + SHIFT RIGHT BIT 1.2 TO A BUS DQ003DF2- 56  
 + SDR BIT 1.2 TO A BUS DQ003DG2- 62  
 + SHIFT RIGHT BIT 1.3 TO A BUS DQ003EH2- 68  
 + SDR BIT 1.3 TO A BUS DQ003EJ2- 74  
 + SHIFT RIGHT BIT 1.4 TO A BUS DQ003FK2- 80  
 + SDR BIT 1.4 TO A BUS DQ003FL2- 86



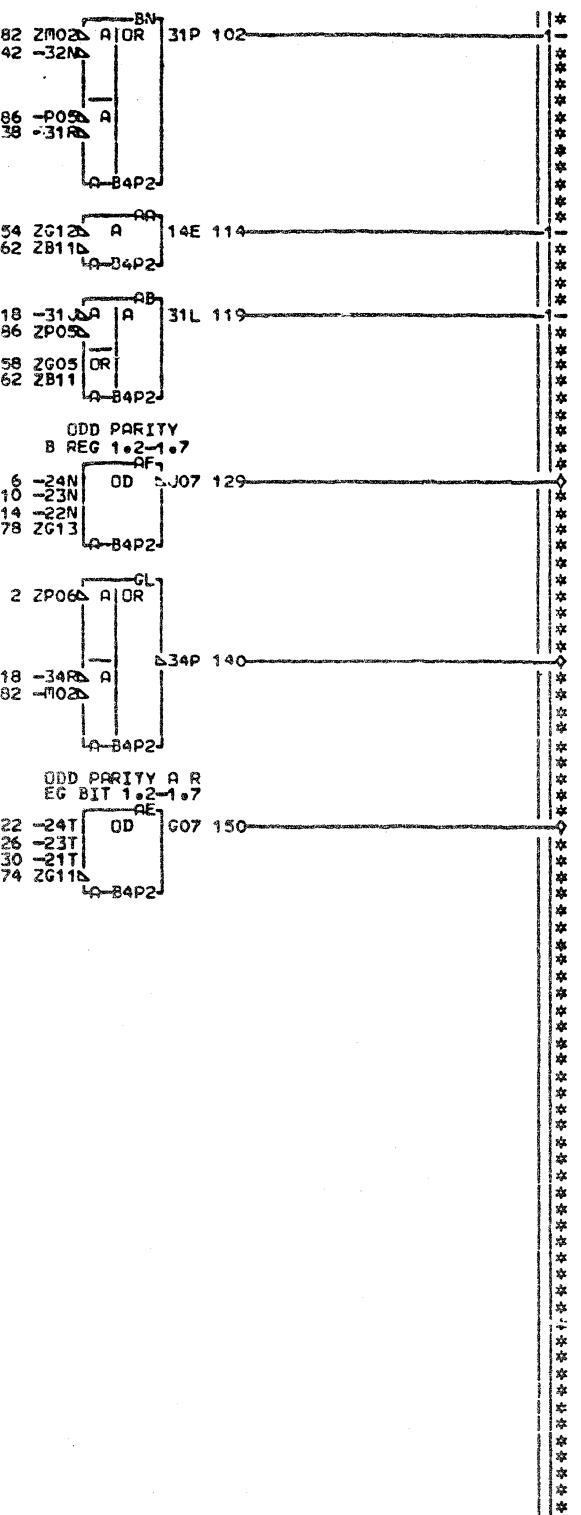
000 DL005  
 125 + SET A REG AND B REG DL002-EA2  
 321 - A REG BIT 1.2 DL007-FD2  
 322 + A REG BIT 1.2 DL006 DL008 FD6  
 312 - A REG BIT 1.3 DL007-FH2  
 313 + A REG BIT 1.3 DL006 DL009 FH6  
 303 - A REG BIT 1.4 DL007-FM2  
 304 + A REG BIT 1.4 DL006 DL010 FM6

LDC. TYPE  
A-B4P2 6802

DLO05  
000

A BUS ASSEMBLER	
BITS 1.2-1.4	
-E.C.-HISTORY-	E.MACH.3705
FRAME 01	
DATE 10-14-80	LAST EC 344270
IBM CORP.SCD	DL005
P.N. 1859611	000

- CROSS HI TO LO CR003HH2- 2-1  
 + B REG BIT 1.2 TO ALU 0 DL002FJ2- 6-1  
 + B REG BIT 1.3 TO ALU 0 DL002FL2- 10-1  
 + B REG BIT 1.4 TO ALU 0 DL002FN2- 14-1  
 - ALU ADD CONTROL BYTE 1 DL004AC2- 18-2  
 + A REG BIT 1.2 DL005FD6- 22-1  
 + A REG BIT 1.3 DL005FH6- 26-1  
 + A REG BIT 1.4 DL005FM6- 30-1  
 + CARRY LA FROM BITS 1.2-1.4 DL007DD6- 34-  
 + ALU 0 CARRY BIT 1.2 DL008DA6- 38-1  
 + ALU 1 CARRY BIT 1.2 DL008EC6- 42-1  
 - ALU 0 SUM BIT 1.2 DL008FF2- 46-1  
 - ALU 1 SUM BIT 1.2 DL008GM2- 50-2  
 - ALU 0 SUM BIT 1.3 DL009FF2- 54-1  
 - ALU 1 SUM BIT 1.3 DL009GM2- 58-2  
 - ALU 0 SUM BIT 1.4 DL010FF2- 62-21  
 - ALU 1 SUM BIT 1.4 DL010GM2- 66-3  
 - ODD PARITY PREDICT 1.5-1.7 DM006DF8- 70-1  
 - ODD PARITY A REG BIT 1.5-1.7 DM006FA2- 74-1  
 + ODD PARITY B REG 1.5-1.7 DM006FD2- 78-1  
 - CARRY LA FROM BITS 1.5-1.7 DM007DD2- 82-2  
 + CARRY LA FROM BITS 1.5-1.7 DM007DD6- 86-2



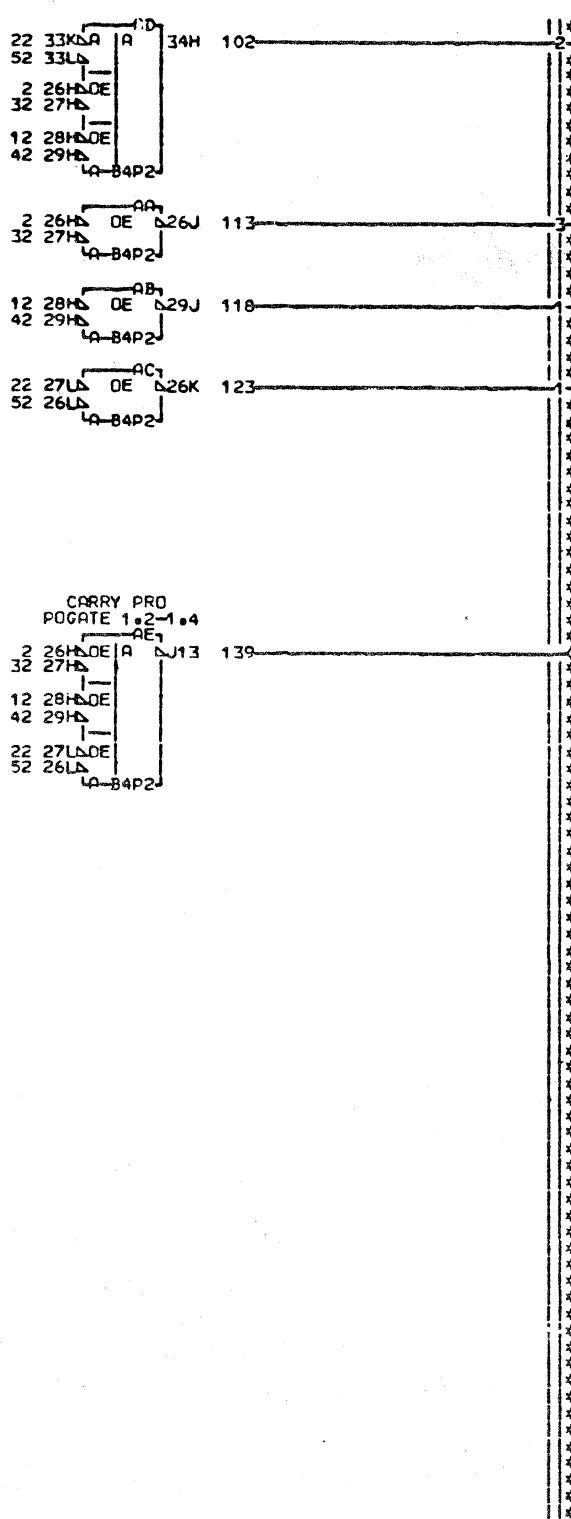
000 DL006  
 222 - ODD PARITY PREDICT 1.2-1.7 DF8  
 LDK976  
 204 + ADD ERROR IN BITS 1.2-1.4 DJ6  
 LDK976  
 150 + ODD PARITY A REG BIT 1.2-1.7 FA2  
 LDK974  
 129 - ODD PARITY B REG 1.2-1.7 FD2  
 LDK974  
 140 - Z BUS BITS 1.2-1.4 SELECT A GL6  
 DL004

ALU CHECK	BITS 1.2-1.4	E-C-HISTORY	E-MACH-3705
FRAME	01		
DATE	LAST EC	IBM CORP-SCD	DL006
10-14-80	344270	P.N. 1859612	000

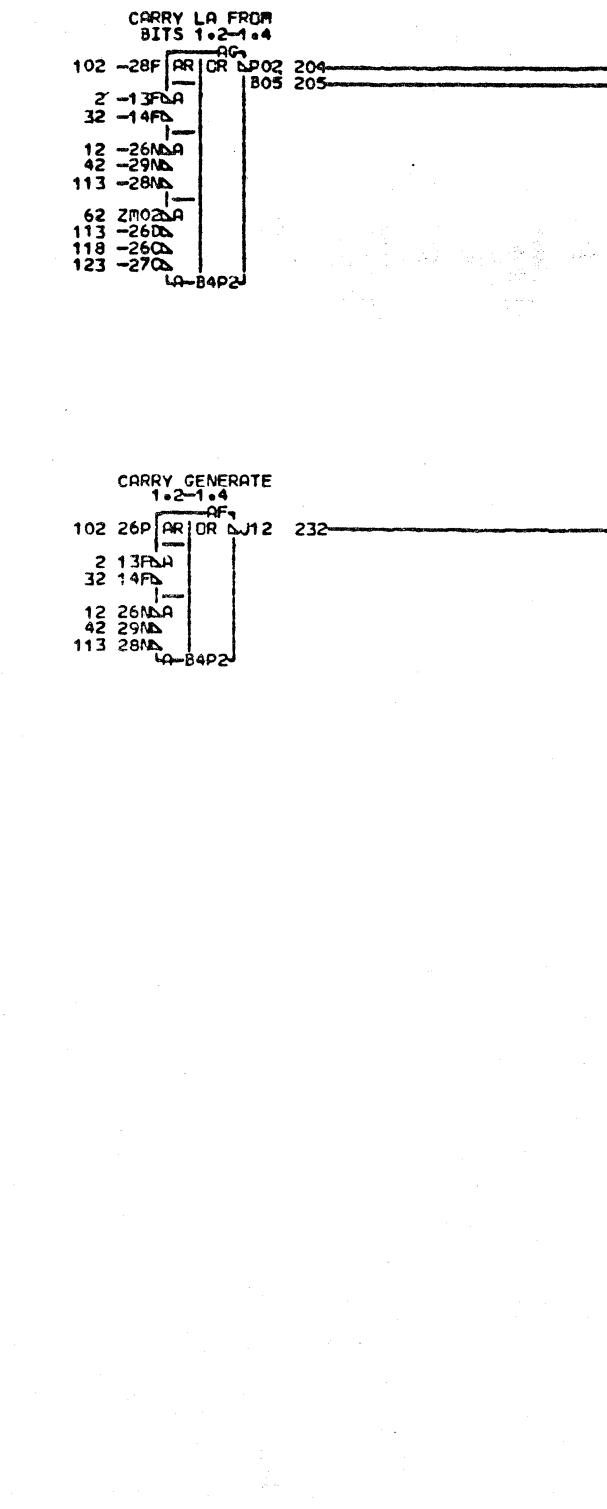
DL006  
000

LOC. TYPE  
A-B4P2 6802

- B REG BIT 1.2 — DL002DH2- 2-32  
 - B REG BIT 1.3 — DL002DH5- 12-32  
 - B REG BIT 1.4 — DL002DH8- 22-3  
 - A REG BIT 1.2 — DL005FD2- 32-32  
 - A REG BIT 1.3 — DL005FH2- 42-32  
 - A REG BIT 1.4 — DL005FM2- 52-3  
 - CARRY LA FROM BITS 1.5-1.7 — DM007DD2- 62-1



LOC<sub>6</sub> TYPE  
A-B4P2 6802



000 DL007  
 139 — CARRY PROPOGATE 1.2-1.4 DK977-CJ6  
 232 — CARRY GENERATE 1.2-1.4 DK977-DB2  
 204 — CARRY LA FROM BITS 1.2-1.4 DD2  
 205 + CARRY LA FROM BITS 1.2-1.4 DD6  
 DL006 DL006

3-BIT CARRY LOOKAHEAD	
BITS 1.2-1.4	
E.C. HISTORY — E.MACH.3705	
FRAME	01
IBM CORP./SCD	DL007
DATE LAST EC	
10-14-80 344270	P.N. 1859613 000

DL007  
000

+ ALU AND CONTROL BYTE 1 — CA004EC6— 2-2

+ ALU OR CONTROL BYTE 1 — CA004EG6— 12-2

+ ALU ADD CONTROL BYTE 1 — CA004EK6— 22-2

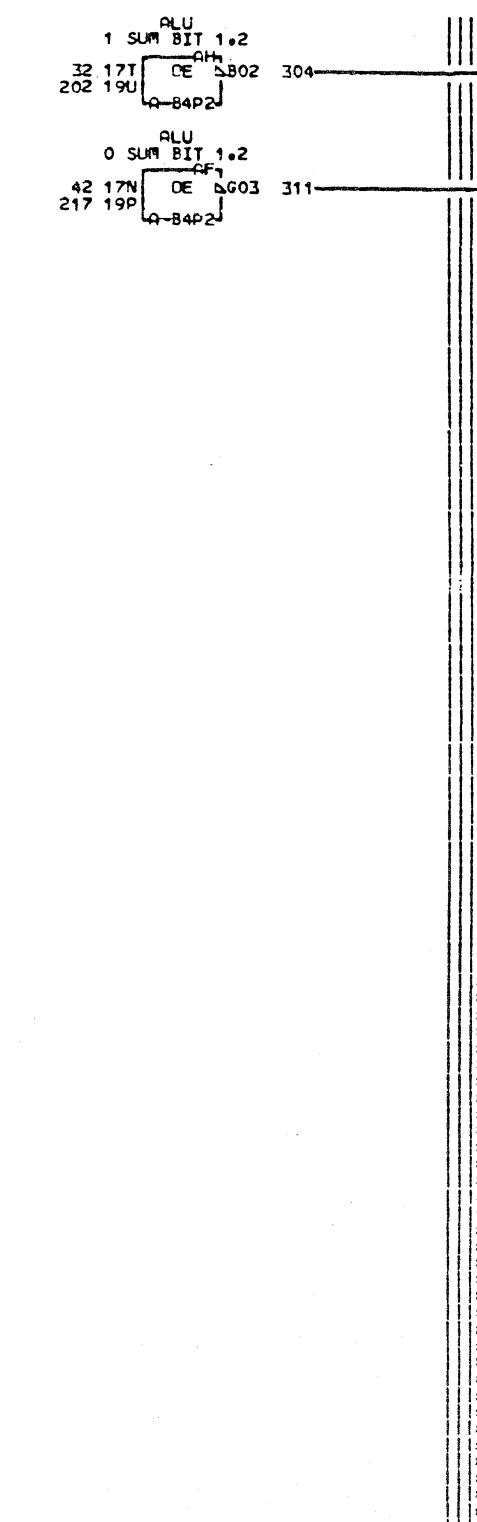
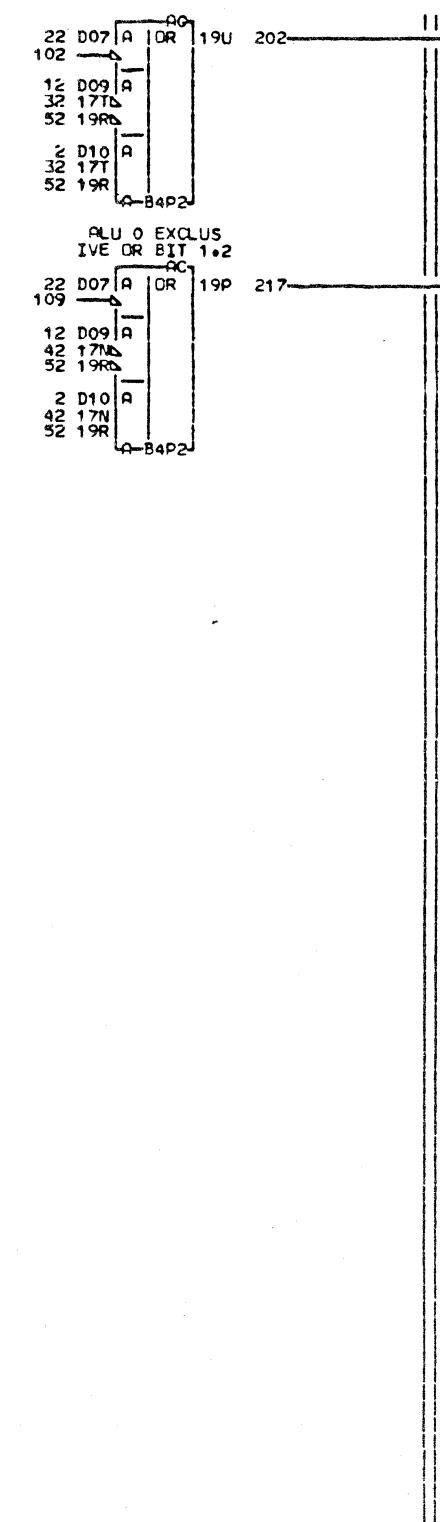
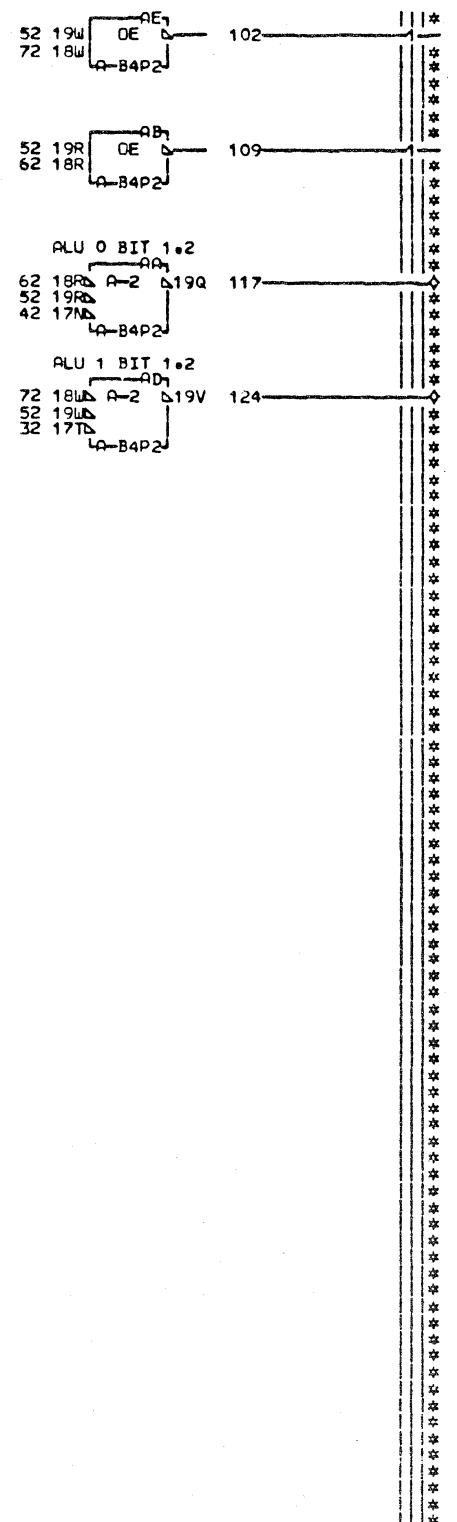
+ B REG BIT 1.2 TO ALU 1 — DL002EJ2— 32-121

+ B REG BIT 1.2 TO ALU 0 — DL002FJ2— 42-121

+ A REG BIT 1.2 — DL005FD6— 52-44

+ ALU 0 CARRY BIT 1.3 — DL009DA6— 62-2

+ ALU 1 CARRY BIT 1.3 — DL009EG6— 72-2



000 DL008  
117 + ALU 0 CARRY BIT 1.2 — DL006-DA6

217 + ALU 0 EXCLUSIVE OR BIT 1.2 — ED2  
LDL004

124 + ALU 1 CARRY BIT 1.2 — DL006-EG6

311 - ALU 0 SUM BIT 1.2 — FF2  
LDH014 LDL004 LDL006

304 - ALU 1 SUM BIT 1.2 — GM2  
LDL004 LDL006

LOC. TYPE  
A-B4P2 6802

ALU 0 AND ALU 1 BIT 1.2	E.C. HISTORY	MACH. 3705
		FRAME 01
		IBM CORP. SCD DL008
DATE 10-14-80	LAST EC 344270	P.N. 1859614 COO

DL008  
000

+ ALU AND CONTROL BYTE 1 — CA004EC6 — 2

+ ALU OR CONTROL BYTE 1 — CA004EG6 — 12

+ ALU ADD CONTROL BYTE 1 — CA004EK6 — 22

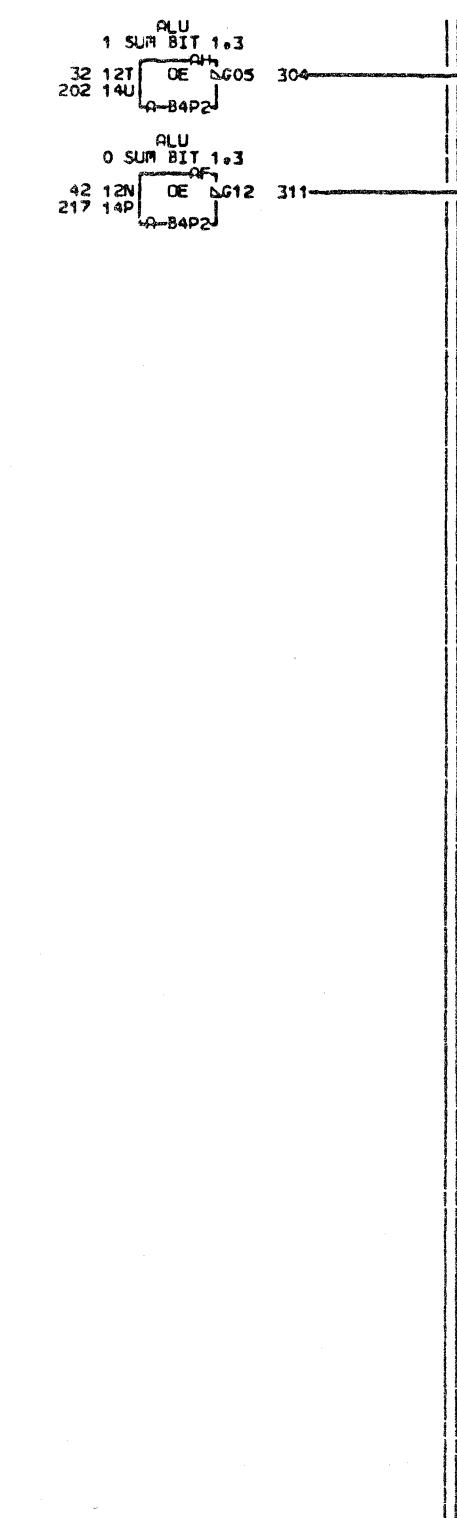
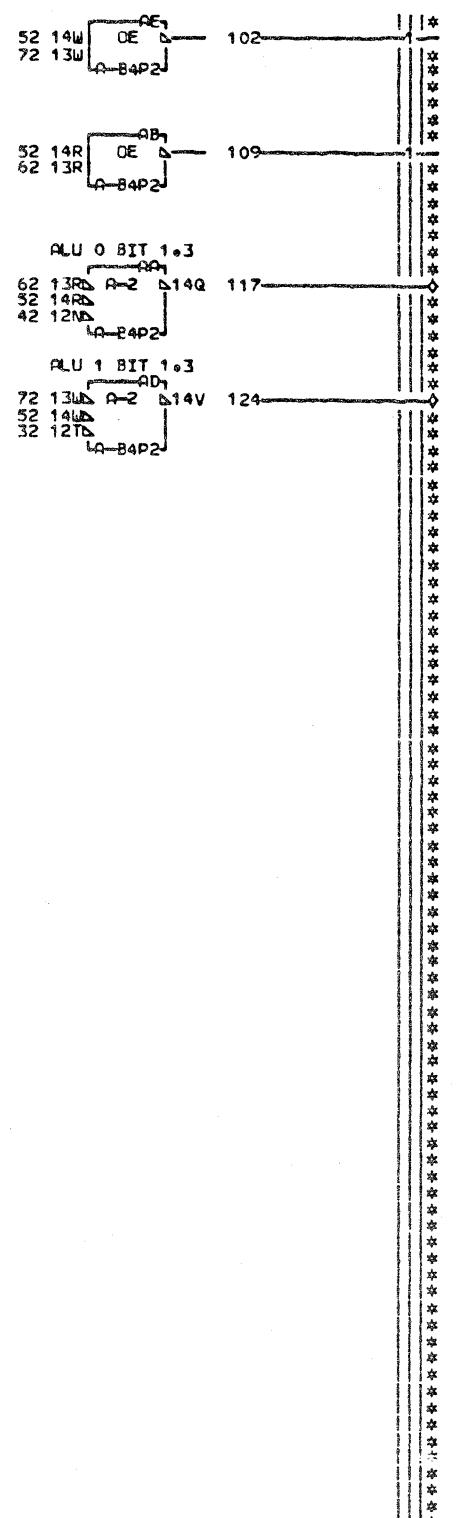
+ B REG BIT 1.3 TO PLU 1 — DL002EL2 — 32

+ B REG BIT 1.3 TO ALU 0 — DL002FL2 — 42

+ A REG BIT 1.3 — DL005FH6 — 52

+ ALU 0 CARRY BIT 1.4 — DL010DA6 — 62

+ ALU 1 CARRY BIT 1.4 — DL010EG6 — 72



000 DL009  
117 + ALU 0 CARRY BIT 1.3 — DL009-D46

217 + ALU 0 EXCLUSIVE OR BIT 1.3 — ED2  
4DL004

124 + ALU 1 CARRY BIT 1.3 — DL008-EG6

311 - ALU 0 SUM BIT 1.3 — FF2  
LDH014 LDLO04 LDLO06

304 - ALU 1 SUM BIT 1.3 — GM2  
4DL004 4DL006

LDC. TYPE  
A-B4P2 6802

DL009  
000

ALU 0 AND ALU 1 BIT 1.3	E-C-HISTORY	E-MACH-3705
		FRAME 01
		IBM CORP-SCD DL009
DATE LAST EC 10-14-80 344270	P.N. 1859615	000

+ ALU AND CONTROL BYTE 1 — CA004EC6— 2-2

+ ALU OR CONTROL BYTE 1 — CA004EG6— 12-2

+ ALU ADD CONTROL BYTE 1 — CA004EK6— 22-2

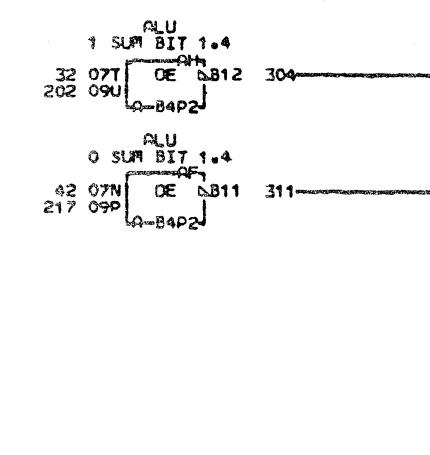
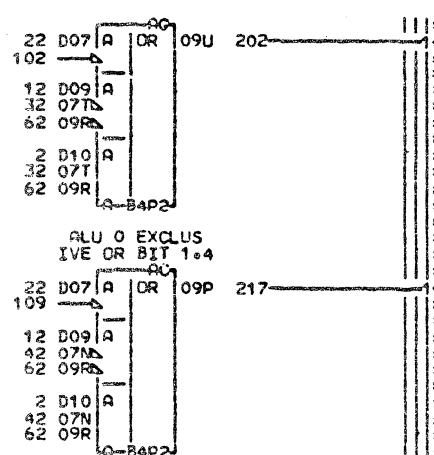
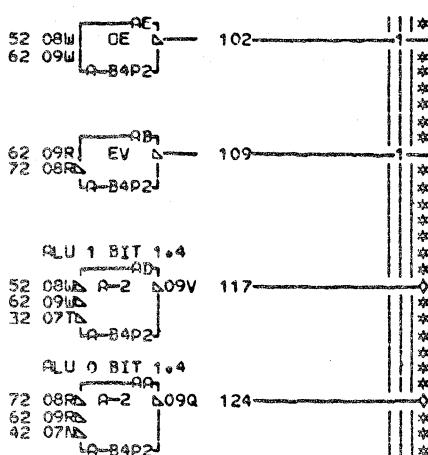
+ B REG BIT 1:4 TO ALU 1 — DL002EN2— 32-21

+ B REG BIT 1:4 TO ALU 0 — DL002FN2— 42-21

+ TIE UP — DL002GF4— 52-2

+ A REG BIT 1:4 — DL005FM6— 62-44

- FLOAT — DL010001— 72-2



000 DL010  
124 + ALU 0 CARRY BIT 1:4 — DL009-DA6

217 + ALU 0 EXCLUSIVE OR BIT 1:4 — ED2  
LDL004

117 + ALU 1 CARRY BIT 1:4 — DL009-EG6

311 + ALU 0 SUM BIT 1:4 — FF2  
LDH014 LD004 LDL006

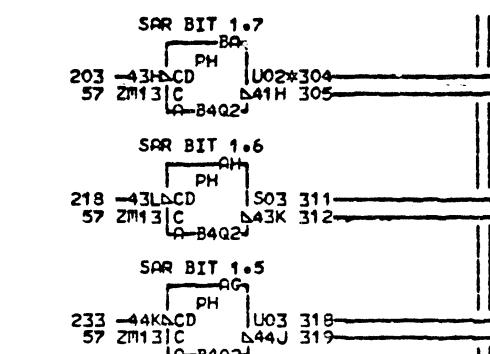
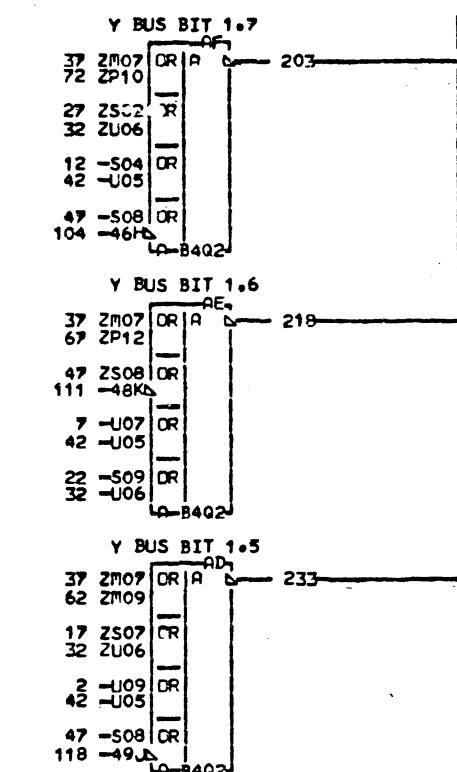
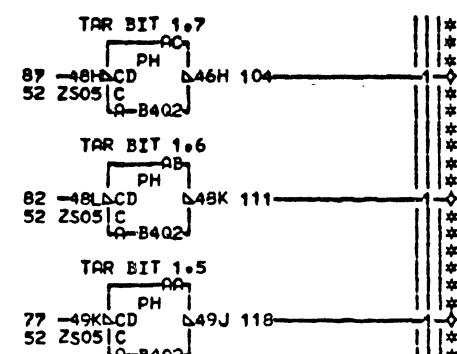
304 = ALU 1 SUM BIT 1:4 — GM2  
LDL004 LD006

LOC. TYPE  
P-B4P2 6B02

DL010  
COO

ALU 0 AND ALU 1 BIT 1:4		E1 MACH. 3705
E.C. HISTORY		
FRAME 01		
DATE 10-14-80	LAST EC 344270	IBM CORP. SCD DL010
PeN. 1859616 COO		

+ INBUS BYTE 1 BIT 5 — AA001DF3# 2  
 + INBUS BYTE 1 BIT 6 — AA001DF5# 7  
 ↓ INBUS BYTE 1 BIT 7 — AA001DF7# 12  
 + ADBUS BIT 1.5 — AA003DF3# 17  
 + ADBUS BIT 1.6 — AA003DF5# 22  
 + ADBUS BIT 1.7 — AA003DF7# 27  
 - GATE ADBUS TO Y BUS — CS004CA6- 32-  
 - GATE CCU INDATA TO Y BUS — CS004DB2- 37-  
 - GATE INBUS TO Y BUS — CS004FG2- 42-  
 - GATE TAR TO Y BUS — CS004FJ2- 47-  
 + SET TAR — CS007CH2- 52-  
 + SET SAR — CS007EB2- 57-  
 + CCU INDATA BIT 1.5 — CU012DK4- 62-  
 + CCU INDATA BIT 1.6 — CU013CJ4- 67-  
 + CCU INDATA BIT 1.7 — CU013CL4- 72-  
 - Z REG BIT 1.5 — DM004FB2- 77-  
 - Z REG BIT 1.6 — DM004FB7- 82-  
 - Z REG BIT 1.7 — DM004FK2- 87-



000 DM001  
118 - TAR BIT 1.5 — DM003-AC6

111 - TAR BIT 1.6 — DM003-A66

104 - TAR BIT 1.7 — DM003-AM6

233 - Y BUS BIT 1.5 — DM002-DC4

218 - Y BUS BIT 1.6 — DM002-DH4

203 - Y BUS BIT 1.7 — DM002-DL4

318 + SAR BIT 1.5 — EC2  
  |  
  + CW002 LCW012 LCW014 LDS001  
  + DT001 LDU001 LDV001

319 - SAR BIT 1.5 — EC6  
  |  
  + DM002 LD005

311 + SAR BIT 1.6 — EH2  
  |  
  + CW002 LCW012 LCW014 LDS001  
  + DT001 LDU001 LDV001

312 - SAR BIT 1.6 — EH6  
  |  
  + DM002 LD005

304 + SAR BIT 1.7 — EK2  
  |  
  + CA003 LCS002 LCS005 LCS006  
  + CW002 LCW012 LCW014 LDS001

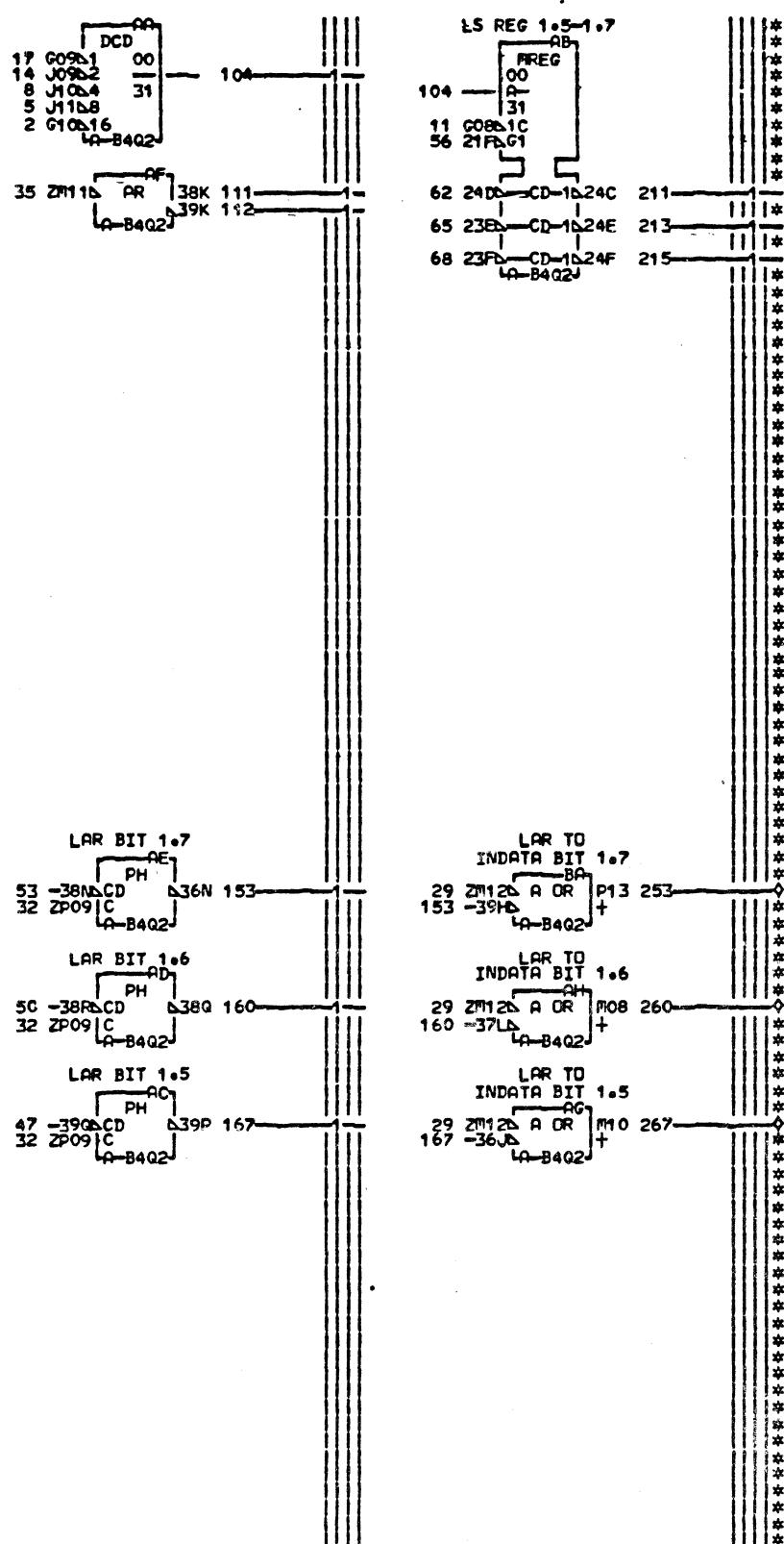
305 - SAR BIT 1.7 — EK6  
  |  
  + DM002 LD005

EDGE CONN.  
2 RESISTOR A-B4Q2S09  
A-B4Q2U09  
7 RESISTOR A-B4Q2U07  
A-B4Q2S04  
12 RESISTOR A-B4Q2S04  
17 RESISTOR A-B4Q2S07  
22 RESISTOR

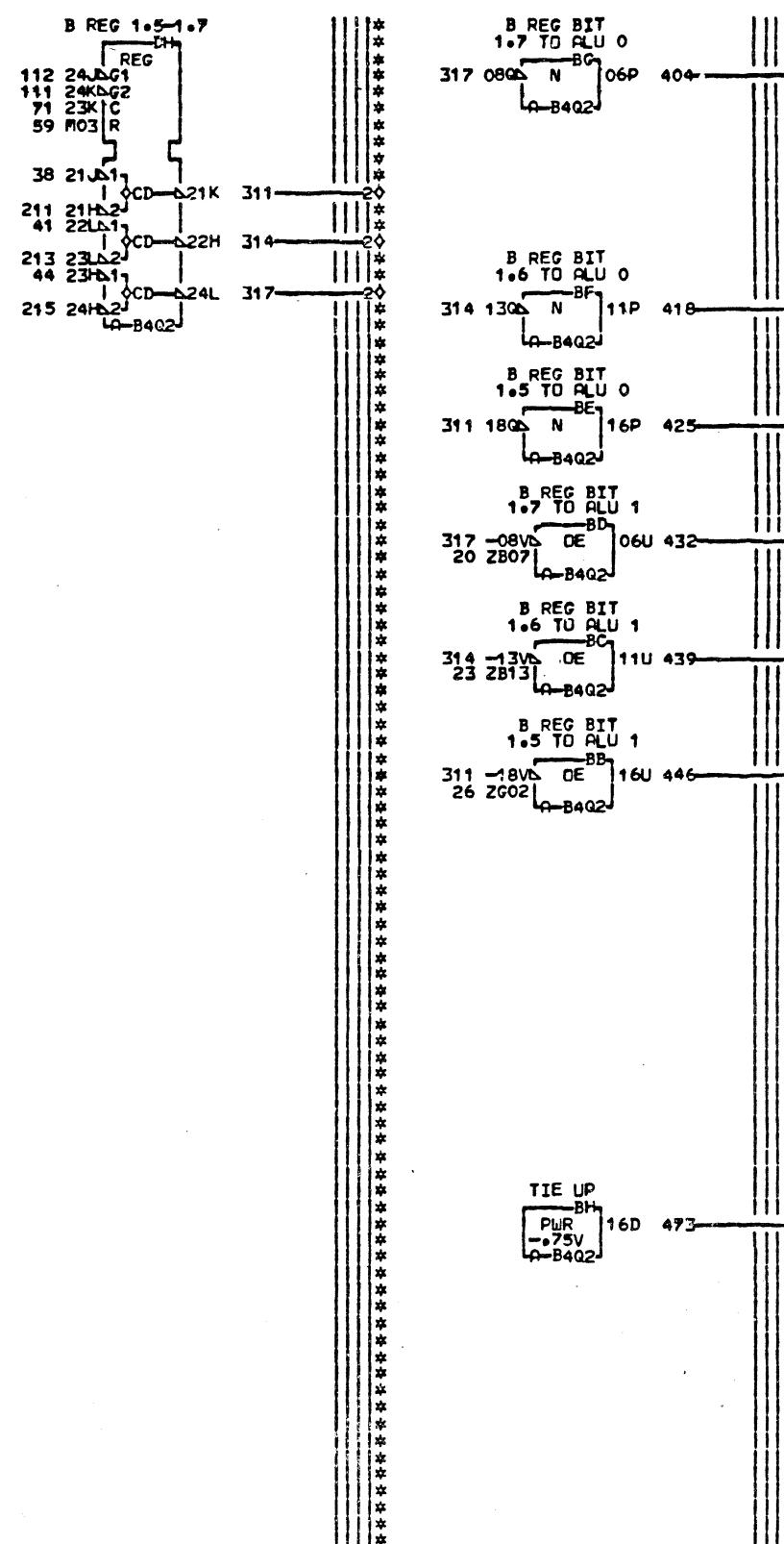
LOC. TYPE  
A-B4Q2 6802

SAR TAR AND Y BUS ASSEMBLERS	
BITS 1.5-1.7	
-E.C.-HISTORY	E-MACH-3705
344270	
FRAME	01
DATE	LAST EC
06-02-81	344828
IBM CORP.SCD	DM001
P.N.	1859617
	000

- SELECT LS REG GROUP 1+2 CC006AU4 2-1  
 - SELECT LS REG GROUP 1+3 CC006AV4 5-1  
 - SELECT LS REG BIT 0+1+2+3 CC006AW4 8-1  
 - WRITE LS CC006BJ4 11-1  
 - SELECT LS REG 0+1+4+5 CC006BK4 14-1  
 - SELECT LS REG BIT 0+2+4+6 CC006BL4 17-1  
 + FORCE ERROR IN BIT 7 CK002DA2 20-  
 + FORCE ERROR IN BIT 6 CK002DB2 23-  
 + FORCE ERROR IN BIT 5 CK002DC2 26-  
 - GATE INPUT 74 CQ004FJ6 29-7  
 + SET LAR CS001DM2 32-3  
 - GATE Y BUS TO B REG CS004ED2 35-  
 - Y BUS BIT 1.5 DM001DC4 38-  
 - Y BUS BIT 1.6 DM001DH4 41-  
 - Y BUS BIT 1.7 DM001DL4 44-  
 - SAR BIT 1.5 DM001EC6 47-  
 - SAR BIT 1.6 DM001EH6 50-  
 - SAR BIT 1.7 DM001EK6 53-  
 - SELECT FLOAT DM002002 56-1  
 - FLOAT DM002003 59-  
 - Z REG BIT 1.5 DM004FB2 62-1  
 - Z REG BIT 1.6 DM004FB7 65-1  
 - Z REG BIT 1.7 DM004FK2 68-1  
 + SET A REG AND B REG DM005EA2 71-1



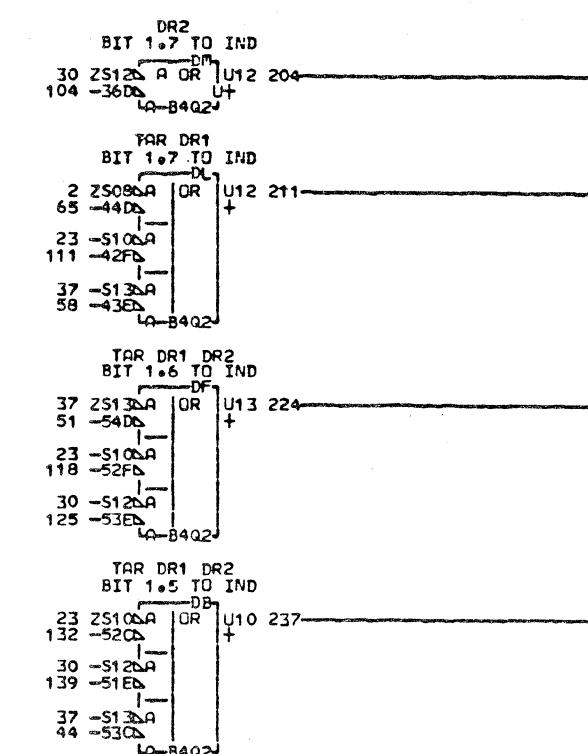
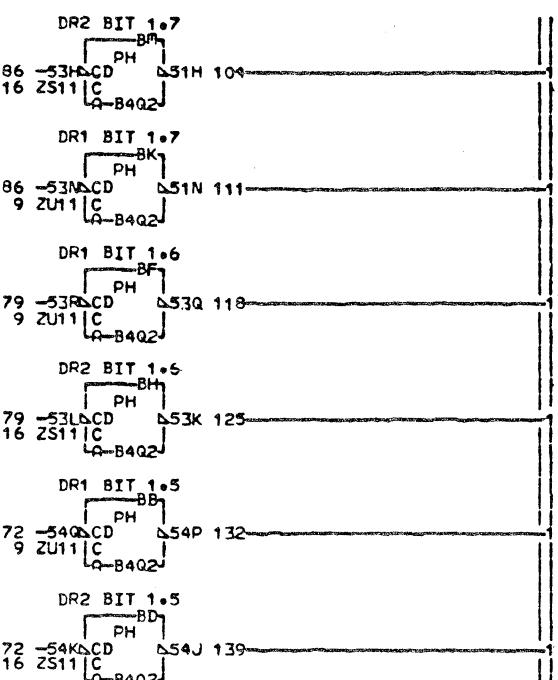
LDC. TYPE  
A-B402 6802



000 DM002  
 311 - B REG BIT 1.5 DM007-DH2  
 314 - B REG BIT 1.6 DM007-DH5  
 317 - B REG BIT 1.7 DM007-DH8  
 267 + LAR TO INDATA BIT 1.5 CU012-EB2  
 260 + LAR TO INDATA BIT 1.6 CU013-ED2  
 253 + LAR TO INDATA BIT 1.7 CU013-EF2  
 446 + B REG BIT 1.5 TO ALU 1 DM008-EJ2  
 439 + B REG BIT 1.6 TO ALU 1 DM009-EL2  
 432 + B REG BIT 1.7 TO ALU 1 DM010-EN2  
 425 + B REG BIT 1.5 TO ALU 0 DM006 DM008 FJ2  
 418 + B REG BIT 1.6 TO ALU 0 DM006 DM009 FL2  
 404 + B REG BIT 1.7 TO ALU 0 DM006 DM010 FN2  
 473 + TIE UP DM003 DM010 GF4

B REG LAR AND LOCAL STORE	
BITS 1.5 1.6 1.7	
E.O.C. HISTORY	E MACH 3705
344270	FRAME 01
DATE LAST EC IBM CORP. SCD DM002	
06-02-81 344828 P.N. 1859618 000	

- GATE TAR TO Y BUS CS004FJ2- 2  
 + SET DR1 CS007FC6- 9-3  
 + SET DR2 CS007FD6- 16-3  
 - GATE DISP REG 1 TO IND CU001EK6- 23-3  
 - GATE DISPL REG 2 TO IND CU001EL6- 30-3  
 - GATE TAR TO IND CU001EM6- 37-3  
 - TAR BIT 1.5 DM001AC6- 44-1  
 - TAR BIT 1.6 DM001AG6- 51-1  
 - TAR BIT 1.7 DM001AM6- 58-1  
 + TIE UP DM002GF4- 65-1  
 - Z REG BIT 1.5 DM004FB2- 72-2  
 - Z REG BIT 1.6 DM004FB7- 79-2  
 - Z REG BIT 1.7 DM004FK2- 86-2



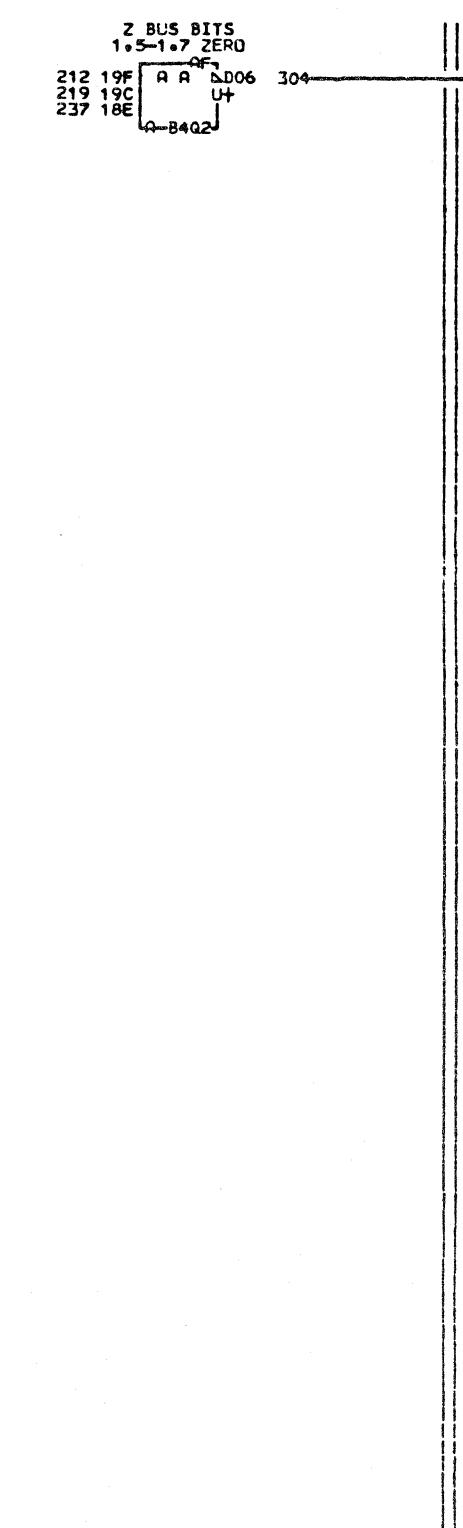
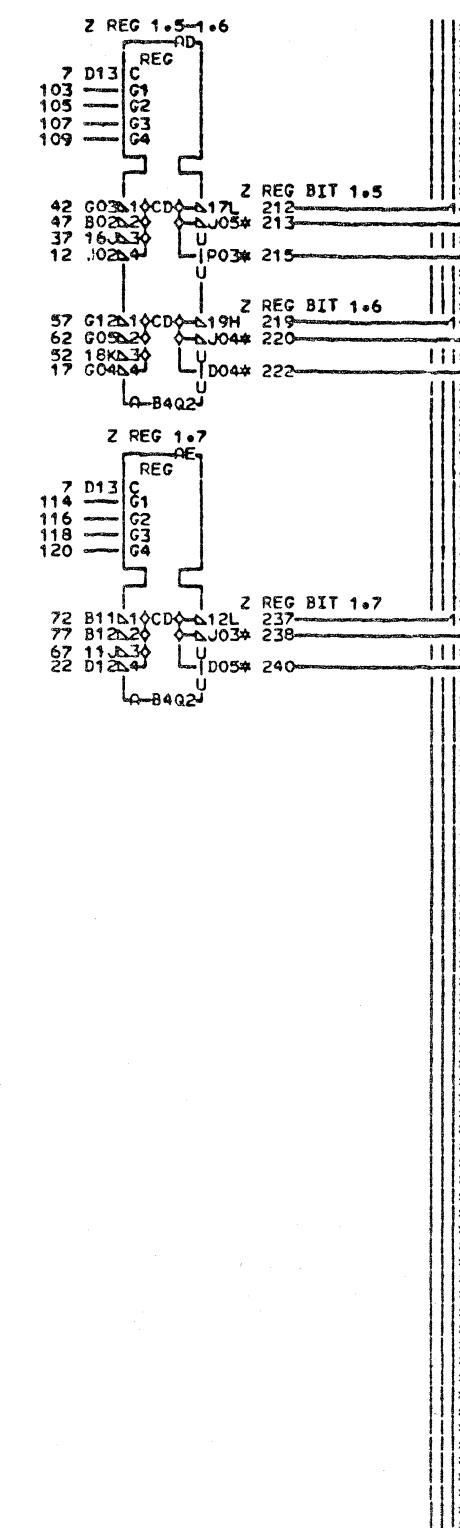
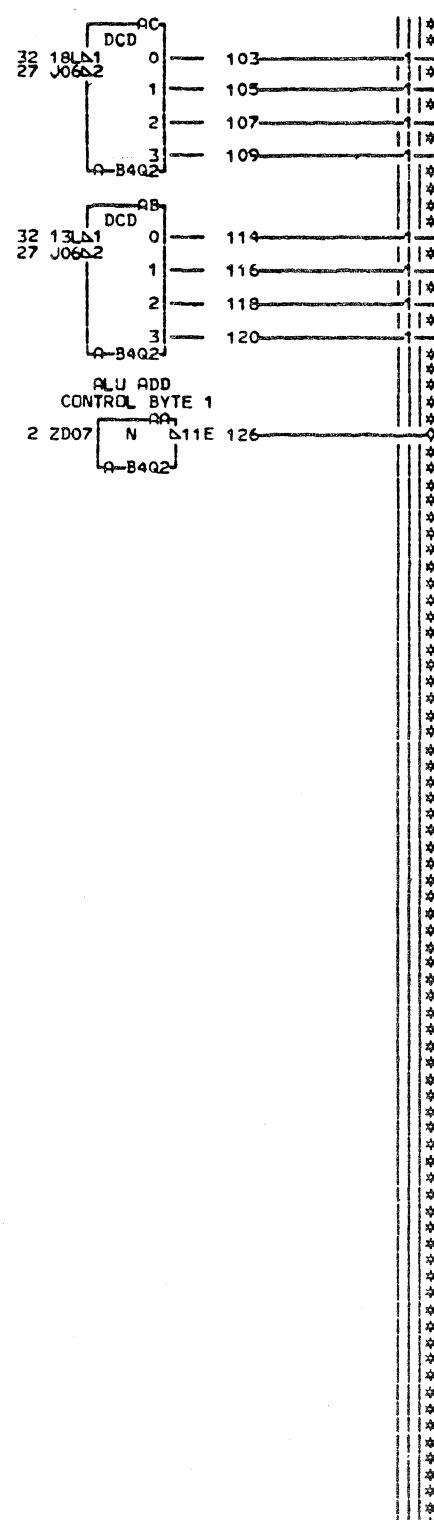
000 DM003  
 237 + TAR DR1 DR2 BIT 1.5 TO IND DB2  
 4AP015  
 224 + TAR DR1 DR2 BIT 1.6 TO IND DF2  
 4AP015  
 211 + TAR DR1 BIT 1.7 TO IND AP015-DL2  
 204 + DR2 BIT 1.7 TO IND AP015-DM2

LDC<sub>o</sub> TYPE  
A-B4Q2 6802

DM003  
000

CCU DISPLAY REGISTERS 1 AND 2
BITS 1.5 1.6 AND 1.7
E-C HISTORY E-MACH 3705
FRAME 01
IBM CORP-SCD DM003
DATE LAST EC 10-14-80 344270
P-N 1859619 000

+ ALU ADD CONTROL BYTE 1—CA004EK6— 2-1  
 + T2+T3 SET Z-REG BYTE 1—CC006FG3— 7-2  
 - ALU 0 SUM BIT 0.5—DJ008FF2— 12-1  
 - ALU 0 SUM BIT 0.6—DJ009FF2— 17-  
 - ALU 0 SUM BIT 0.7—DJ010FF2— 22-1  
 - Z BUS BITS 1.0-1.7 SELECT 2—DK976CB2— 27-2  
 - Z BUS BITS 1.5-1.7 SELECT A—DM006GL6— 32-2  
 + ALU 0 EXCLUSIVE OR BIT 1.5—DM008ED2— 37-1  
 - ALU 0 SUM BIT 1.5—DM008FF2— 42-1  
 - ALU 1 SUM BIT 1.5—DM009GM2— 47-  
 + ALU 0 EXCLUSIVE OR BIT 1.6—DM009ED2— 52-1  
 - ALU 0 SUM BIT 1.6—DM009FF2— 57-1  
 - ALU 1 SUM BIT 1.6—DM009GM2— 62-1  
 + ALU 0 EXCLUSIVE OR BIT 1.7—DR010ED2— 67-1  
 - ALU 0 SUM BIT 1.7—DM010FF2— 72-1  
 - ALU 1 SUM BIT 1.7—DM010GM2— 77-1



000 DM004  
126 - ALU ADD CONTROL BYTE 1—DM006-AC2

212 - Z REG BIT 1.5—  
LDM001 LDM002 LDM003 FB2

219 - Z REG BIT 1.6—  
LDM001 LDM002 LDM003 FB7

237 - Z REG BIT 1.7—  
LDM001 LDM002 LDM003 FK2

213 - Z BUS BIT 1.5—  
GCR002 LCU001 LCU005 LCU014  
LCV061 LCX006 LCX009 LDK976  
LDR991 GB6

215 + OUTBUS BIT 1.5—  
AA001-GC2

220 - Z BUS BIT 1.6—  
GCR001 LCU015 LCV061 LCX009  
LDK976 LDR991 GF6

222 + OUTBUS BIT 1.6—  
AA001-GG2

238 - Z BUS BIT 1.7—  
GCR001 LCU015 LCV061 LDK976  
LDR992 GK6

240 + OUTBUS BIT 1.7—  
AA001-GL2

304 - Z BUS BITS 1.5-1.7 ZERO DK974-GM6

DR004

000

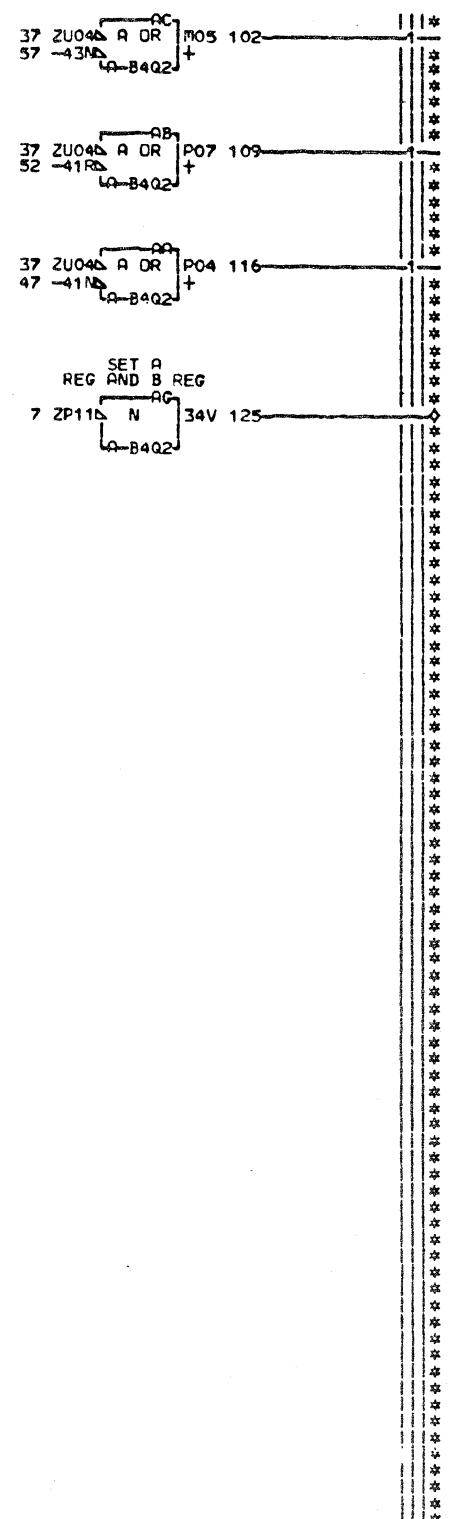
EDGE CONN.  
213 A-B4F1E13  
01A-B3F6E04  
215 A-B4U4D10  
220 A-B4G1A11  
01A-B3G6A02  
222 A-B4U4D11  
238 A-B4G1A13  
01A-B3G6A04  
240 A-B4U4D13

LOC. TYPE  
A-B4Q2 6802

ALU 0 ALU 1 AND Z REG BITS 1.5-1.7	E.C.—HISTORY—	E.MACH.3705
FRAME 01		
DATE 10-14-80	LAST EC 344270	IBM CORP. SCD DM004
P.o.N. 1859620 000		

- COMPLEMENT A BUS CAC04DD2- 2-3  
 - T0+T1 TIME SET A-B REGS CC007HK4- 7-1-3  
 + FORCE CONSTANT 00002 CF001AK6- 12-1  
 + FORCE CONSTANT 00001 CF001AL6- 17-1  
 + FORCE A BUS BIT 1.5 CF001BG2- 22-1  
 + FORCE A BUS BIT 1.6 CF001BH2- 27-1  
 + FORCE A BUS BIT 1.7 CF001BJ2- 32-1  
 - GATE SAR TO A BUS CS004BK6- 37-3  
 + COMPARE FORCE A BUS BIT 1.7 CS006BA2- 42-1  
 - SAR BIT 1.5 DM001EC6- 47-1  
 - SAR BIT 1.6 DM001EH6- 52-1  
 - SAR BIT 1.7 DM001EK6- 57-1  
 + SHIFT RIGHT BIT 1.5 TO A BUS DR994BB2- 62-1  
 + SDR BIT 1.5 TO A BUS DR994BC2- 67-1  
 + SHIFT RIGHT BIT 1.6 TO A BUS DR994BE2- 72-1  
 + SDR BIT 1.6 TO A BUS DR994BF2- 77-1  
 + SHIFT RIGHT BIT 1.7 TO A BUS DR994BH2- 82-1  
 + SDR BIT 1.7 TO A BUS DR994BJ2- 87-1

EDGE CONN.  
 202 A-B4V3D10  
 01A-B3V3D10



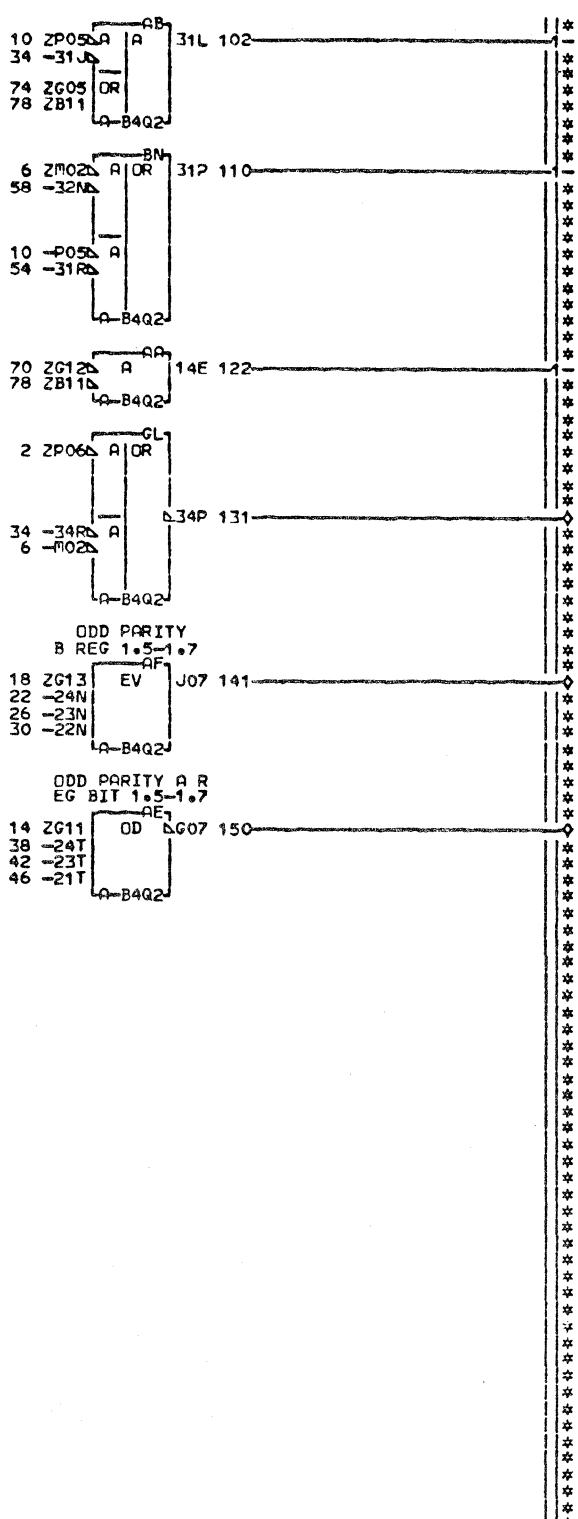
000 DM005  
 125 + SET A REG AND B REG DM002-EA2  
 321 - A REG BIT 1.5 DM007-FD2  
 322 + A REG BIT 1.5 FD6  
 312 - A REG BIT 1.6 DM007-FH2  
 313 + A REG BIT 1.6 FH6  
 303 - A REG BIT 1.7 DM007-FM2  
 304 + A REG BIT 1.7 FM6  
 4DM006 4DM008  
 4DM006 4DM009  
 4DM006 4DM010

A BUS ASSEMBLER	BITS 1.5-1.7	E.C. MISTCRY	MARCH 3705
FRAME	01		
DATE	LAST EC	IBM CORP/SCD	DM005
10-14-80	344270	P/N 1859621	000

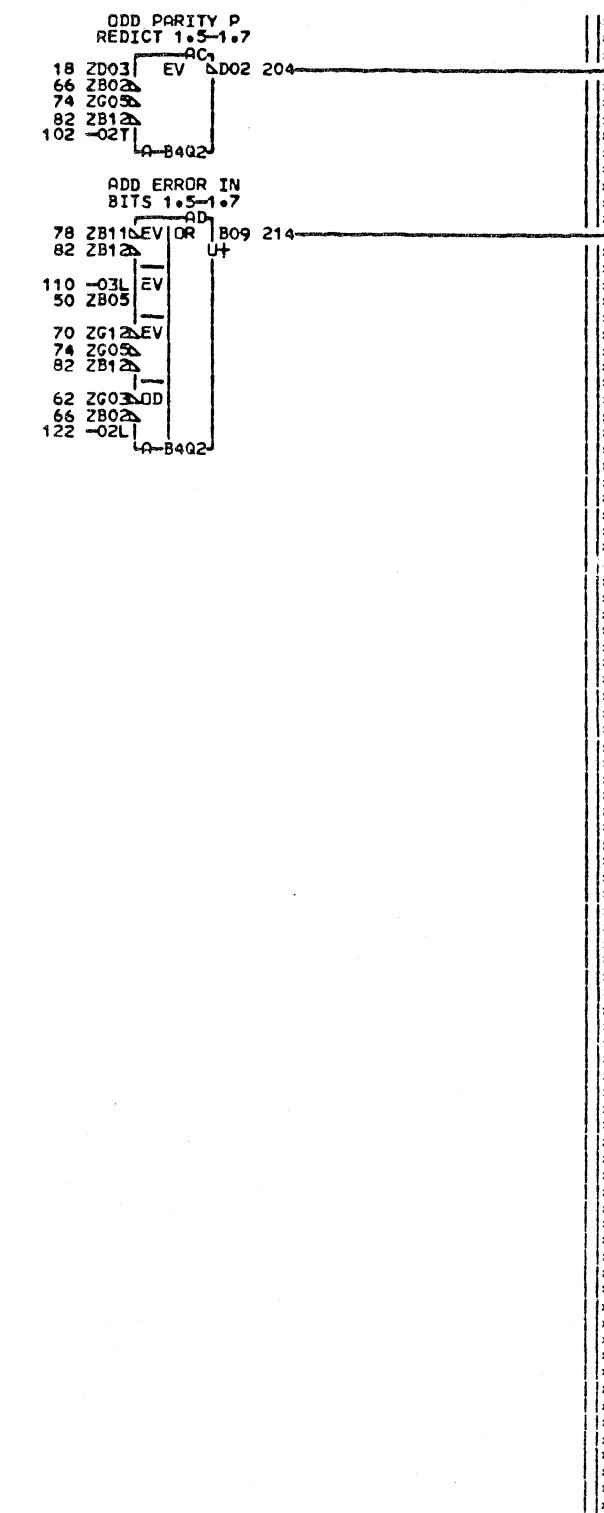
DM005  
000

LOC. TYPE  
A-B4Q2 6802

- CROSS HI TO LO CA003HH2- 2-1  
 - SUBTRACT A BUS CA004EM2- 6-2  
 + SUBTRACT A BUS CA004EM6- 10-2  
 + FORCE A REG PARITY ERROR CK001GB2- 14-1  
 + TIE UP DK002GF4- 18-1  
 + B REG BIT 1.5 TO ALU 0 DM002FJ2- 22-1  
 + B REG BIT 1.6 TO ALU 0 DM002FL2- 26-1  
 + B REG BIT 1.7 TO ALU 0 DM002FN2- 30-1  
 - ALU ADD CONTROL BYTE 1 DM004AC2- 34-2  
 + A REG BIT 1.5 DM005FD6- 38-1  
 + A REG BIT 1.6 DM005FH6- 42-1  
 + A REG BIT 1.7 DM005FM6- 46-1  
 + CARRY LA FROM BITS 1.5-1.7 DM007DD6- 50-1  
 + ALU 0 CARRY BIT 1.5 DM008DA6- 54-1  
 + ALU 1 CARRY BIT 1.5 DM008EG6- 58-1  
 - ALU 0 SUM BIT 1.5 DM008FF2- 62-1  
 - ALU 1 SUM BIT 1.5 DM008GM2- 66-1  
 - ALU 0 SUM BIT 1.6 DM009FF2- 70-1  
 - ALU 1 SUM BIT 1.6 DM009GM2- 74-12  
 - ALU 0 SUM BIT 1.7 DM010FF2- 78-21  
 - ALU 1 SUM BIT 1.7 DM010GM2- 82-3



LOC. TYPE  
A-B4Q2 6802



000 DM006  
204 - ODD PARITY PREDICT 1.5-1.7 DF8  
LDL006

214 + ADD ERROR IN BITS 1.5-1.7 DJ6  
LDK976

150 - ODD PARITY A REG BIT 1.5-1.7 FA2  
LDL006

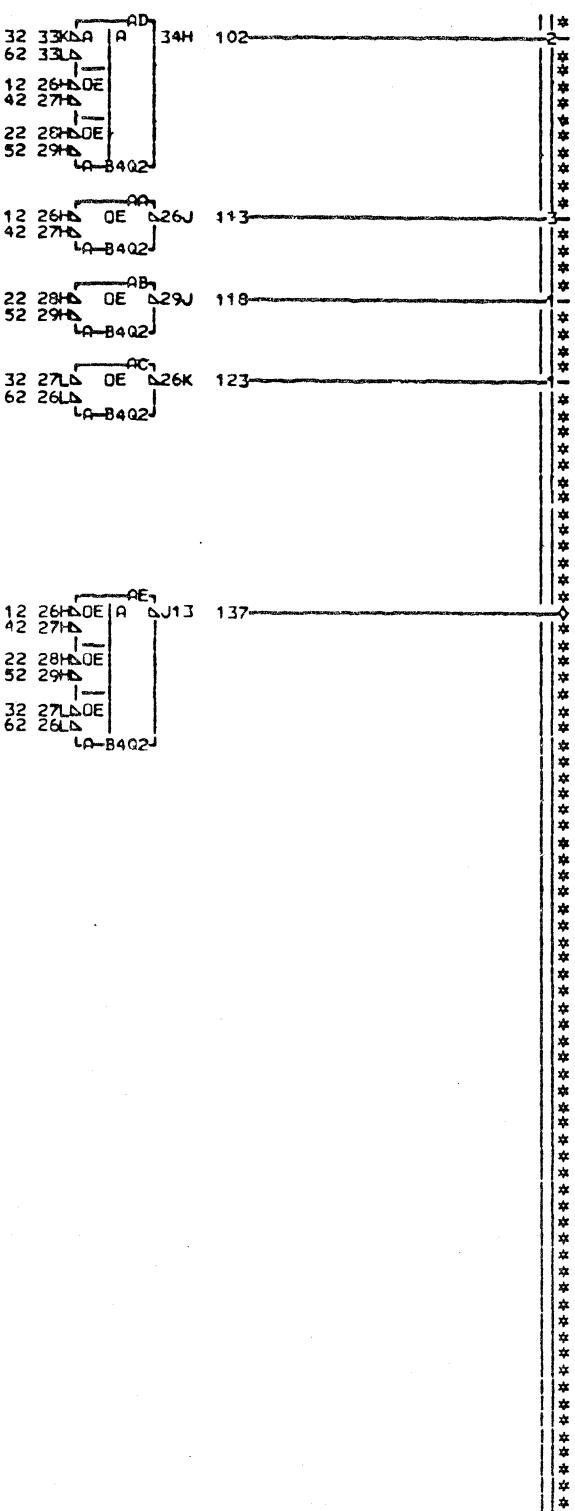
141 + ODD PARITY B REG 1.5-1.7 FD2  
LDL006

131 - Z BUS BITS 1.5-1.7 SELECT A GL6  
LDM004

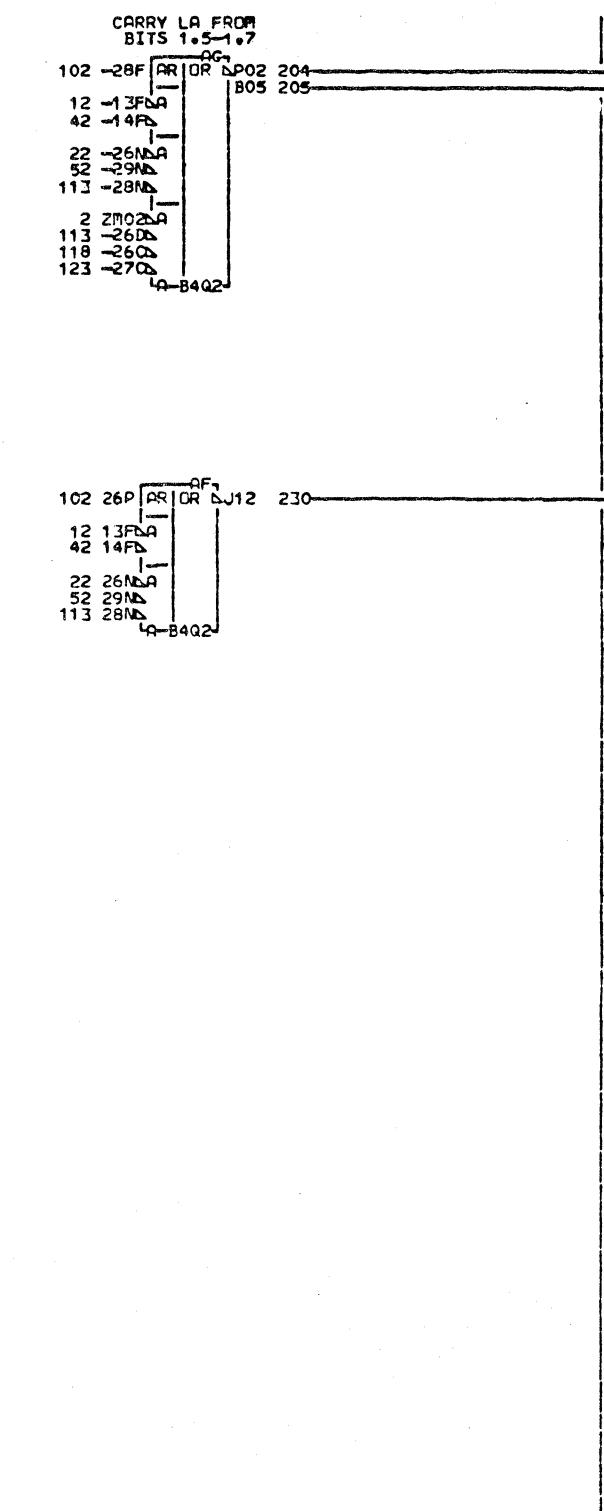
ALU CHECK	BITS 1.5-1.7	E-C-HISTORY	MACH-3705
DATE 10-14-80	LAST EC 344270	FRAME 01	
IBM CORP-SCD	DM006	P.N. 1859622	000

DM006  
000

- SUBTRACT A BUS CA004EM2 2  
 - B REG BIT 1.5 DM002DH2 12-32  
 - B REG BIT 1.6 DM002DH5 22-32  
 - B REG BIT 1.7 DM002DH8 32-3  
 - A REG BIT 1.5 DM005FD2 42-32  
 - A REG BIT 1.6 DM005FH2 52-32  
 - A REG BIT 1.7 DM005FM2 62-3



LCC TYPE  
A-B4Q2 6802

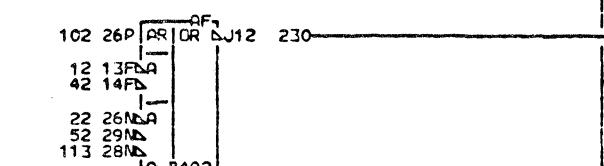


000 DM007  
137 TEST POINT CJ6

230 TEST POINT DB2

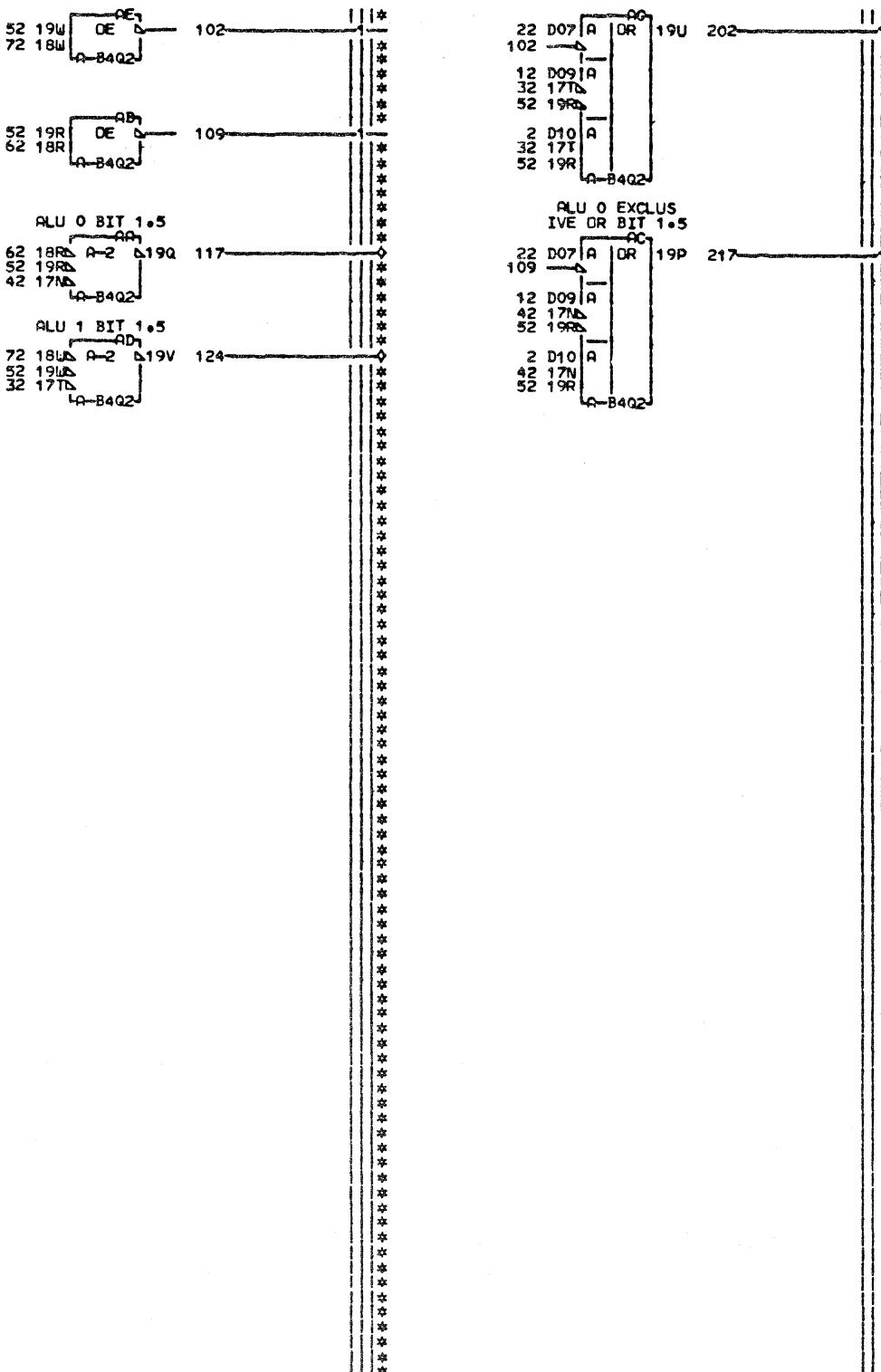
204 - CARRY LA FROM BITS 1.5-1.7 DD2  
4DL006 4DL007

205 + CARRY LA FROM BITS 1.5-1.7 DD6  
4DL006 4DM006

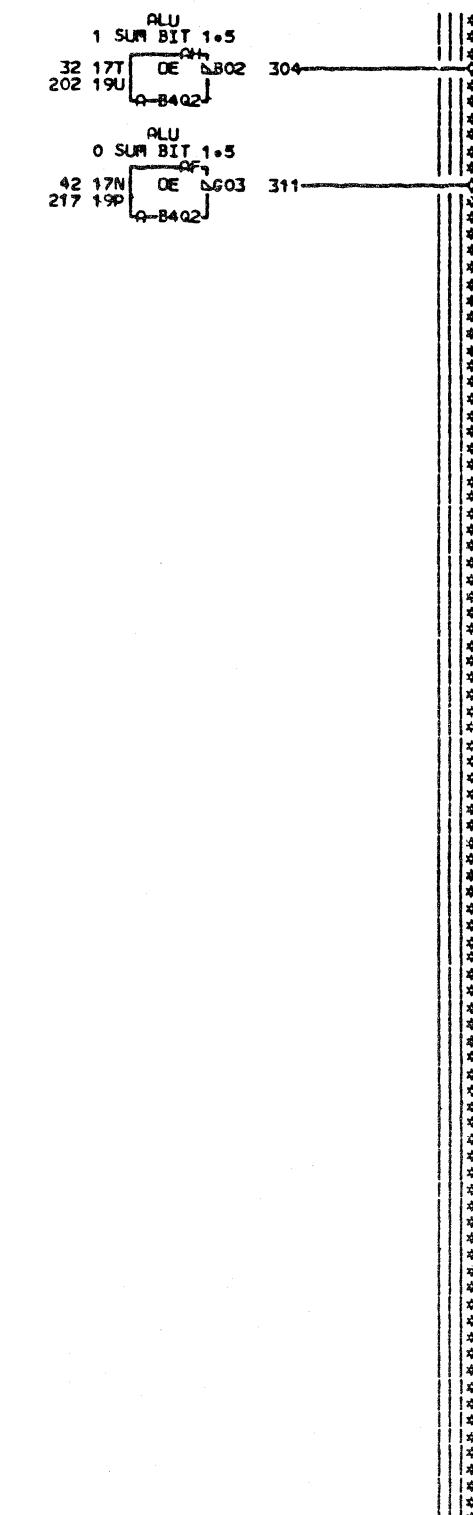


3-BIT CARRY LOOKAHEAD	
BITS 1.5-1.7	
E-C-HISTORY E-PACH=3705	
FRAME	01
DATE	LAST EC
10-14-80	344270
IBM CORP-SCD DM007	
P.N. 1859623 000	

+ ALU AND CONTROL BYTE 1 — CA004EC6 — 2 — 2  
 + ALU OR CONTROL BYTE 1 — CA004EG6 — 12 — 2  
 + ALU ADD CONTROL BYTE 1 — CA004EK6 — 22 — 2  
 + B REG BIT 1.5 TO ALU 1 — DM002EJ2 — 32 — 21  
 + B REG BIT 1.5 TO ALU 0 — DM002FJ2 — 42 — 21  
 + A REG BIT 1.5 — DM005FD6 — 52 — 44  
 + ALU 0 CARRY BIT 1.6 — DM009DA6 — 62 — 2  
 + ALU 1 CARRY BIT 1.6 — DM009EG6 — 72 — 2



LOC. TYPE  
A-B4Q2 6802



000 DM008  
 117 + ALU 0 CARRY BIT 1.5 — DM006-DA6  
 217 + ALU 0 EXCLUSIVE OR BIT 1.5 — ED2  
 124 + ALU 1 CARRY BIT 1.5 — DM006-EG6  
 311 - ALU 0 SUM BIT 1.5 — FF2  
 304 - ALU 1 SUM BIT 1.5 — GM2

ALU 0 AND ALU 1		MACH.3705
BIT 1.5	E-C. HISTORY	
DATE 10-14-80 LAST EC 344270		FRAME 01
IBM CORP. SCD P.N. 1859624		DM008
		000

DM008  
000

+ ALU AND CONTROL BYTE 1 — CA004EC6— 2-2

+ ALU OR CONTROL BYTE 1 — CA004EG6— 12-2

+ ALU ADD CONTROL BYTE 1 — CA004EK6— 22-2

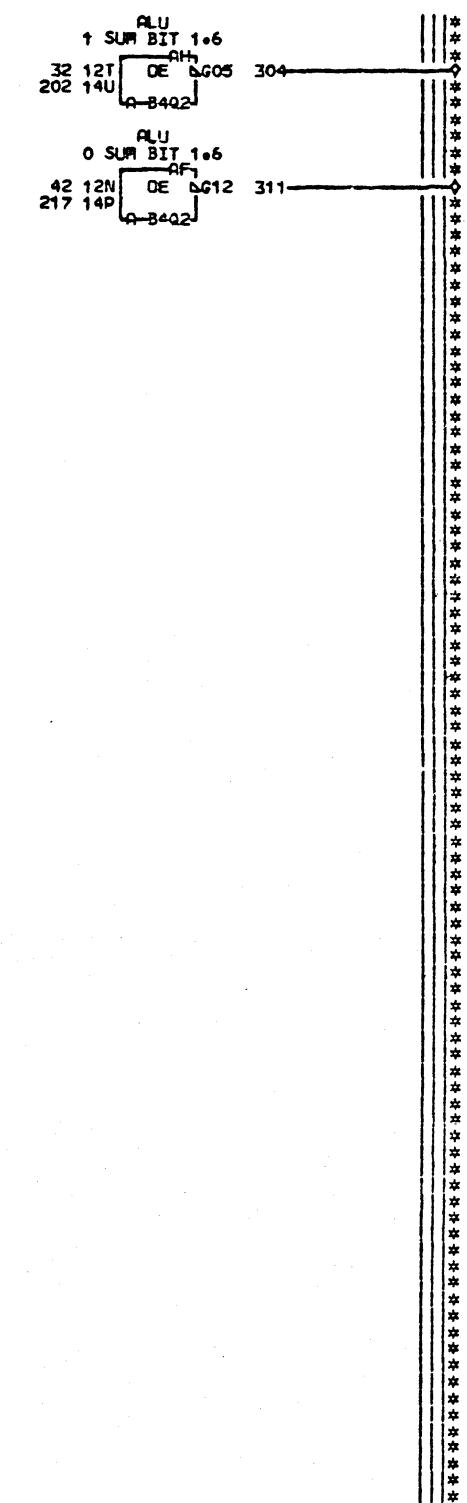
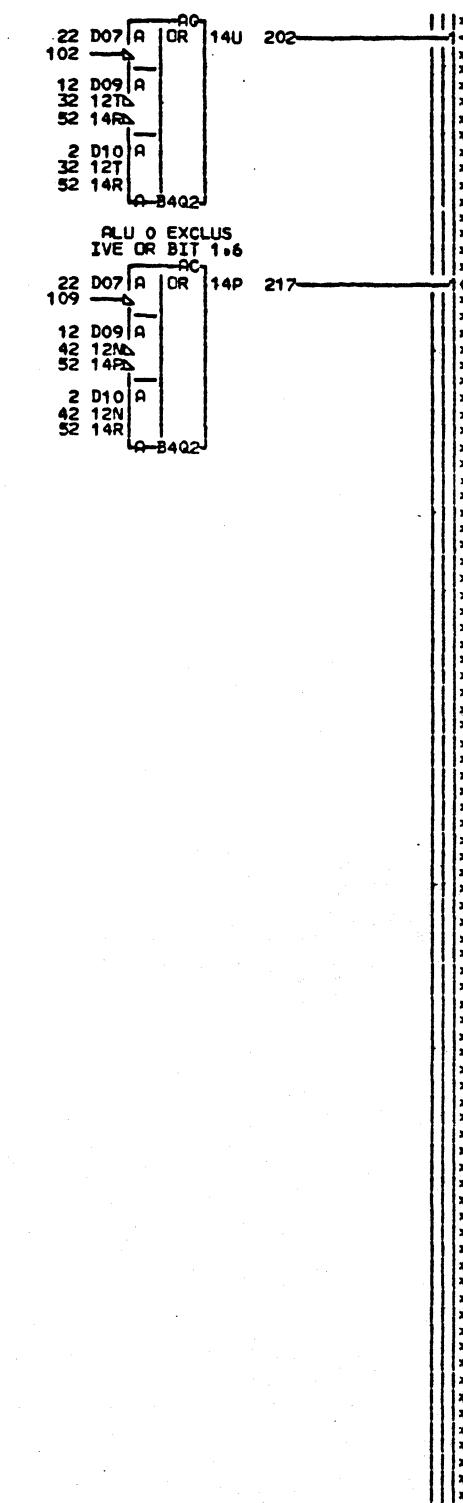
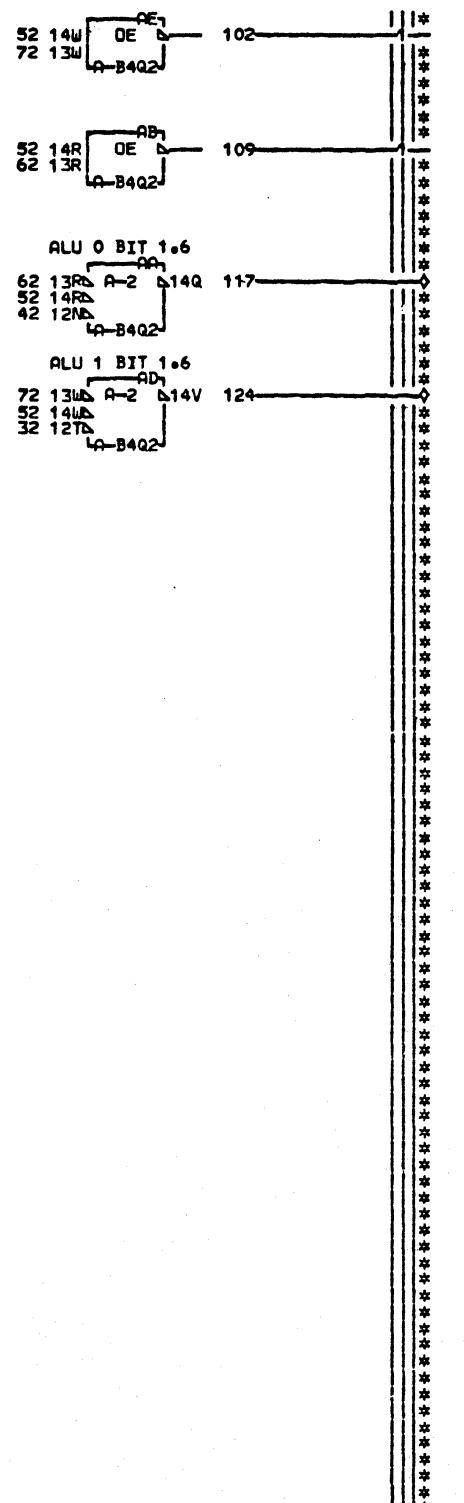
+ B REG BIT 1.6 TO ALU 1 — DM002EL2— 32-12

+ B REG BIT 1.6 TO ALU 0 — DM002FL2— 42-12

+ A REG BIT 1.6 — DM005FH6— 52-44

+ ALU 0 CARRY BIT 1.7 — DM010DA6— 62-2

+ ALU 1 CARRY BIT 1.7 — DM010EG6— 72-2



000 DM009  
117 + ALU 0 CARRY BIT 1.6 — DM008-DA6  
217 + ALU 0 EXCLUSIVE OR BIT 1.6 — ED2  
42 12N DE 6G05 304  
ALU  
+ SUM BIT 1.6  
32 12T DE AFH 202 14U 202  
0 SUM BIT 1.6  
42 12N DE AF1 217 14P 311  
ALU 0 EXCLUS  
IVE OR BIT 1.6  
22 D07 A OR 14P 217  
109  
12 D09 A  
32 12M  
52 14R  
2 D10 A  
32 12T  
52 14R  
42 12N A  
32 12M  
52 14R  
ALU 1 BIT 1.6  
72 13W A-2 D14V 117  
52 14W  
32 12D  
42 12N A  
32 12M  
52 14R  
ALU 1 BIT 1.6  
72 13W A-2 D14V 124  
52 14W  
32 12D  
42 12N A  
32 12M  
52 14R  
A-B4Q2

124 + ALU 1 CARRY BIT 1.6 — DM008-EG6  
311 - ALU 0 SUM BIT 1.6 — FF2  
42 12N DE 6G04 4DM006  
304 - ALU 1 SUM BIT 1.6 — GM2  
42 12N DE 6G04 4DM006

LOC. TYPE  
A-B4Q2 6802

ALU 0 AND ALU 1 BIT 1.6	E.C.-HISTORY	MACH.3705
		FRAME 01
		IBM CORP-SCD DM009
DATE 10-14-80	LAST EC 344270	P.N. 1859625 000

DM009  
000

+ ALU AND CONTROL BYTE 1 — CR004EC6— 2-2

+ ALU OR CONTROL BYTE 1 — CA004EG6— 12-2

+ ALU ADD CONTROL BYTE 1 — CR004EK6— 22-2

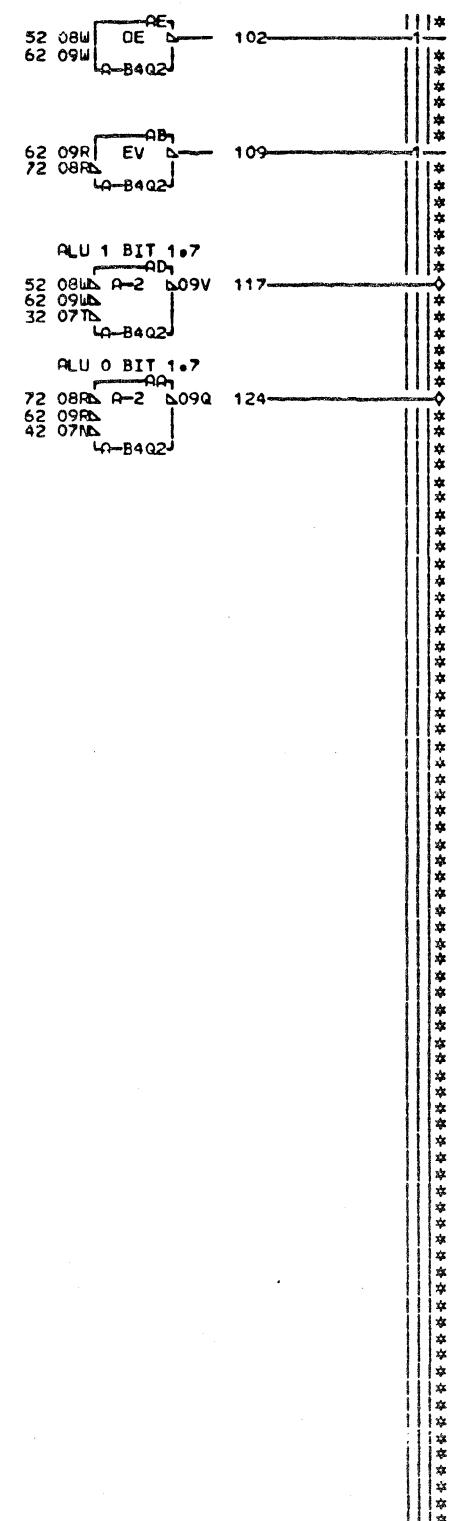
+ B REG BIT 1.7 TO ALU 1 — DM002EN2— 32-121

+ B REG BIT 1.7 TO ALU 0 — DM002FN2— 42-121

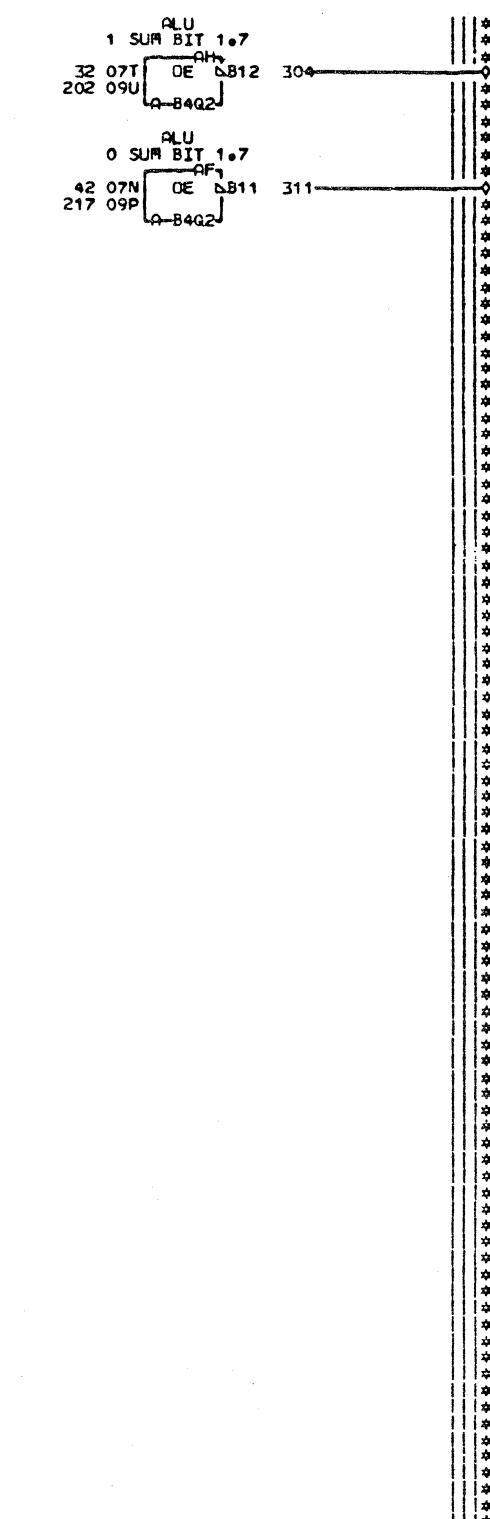
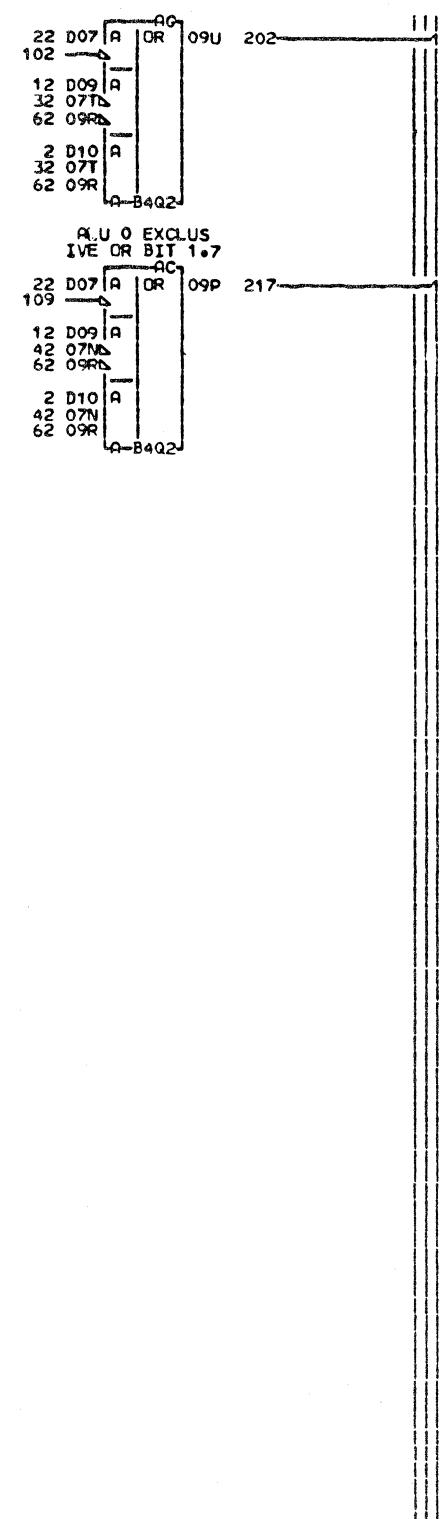
+ TIE UP — DM002GF4— 52-2

+ A REG BIT 1.7 — DM005FM6— 62-44

- FLOAT — DF010001— 72-2

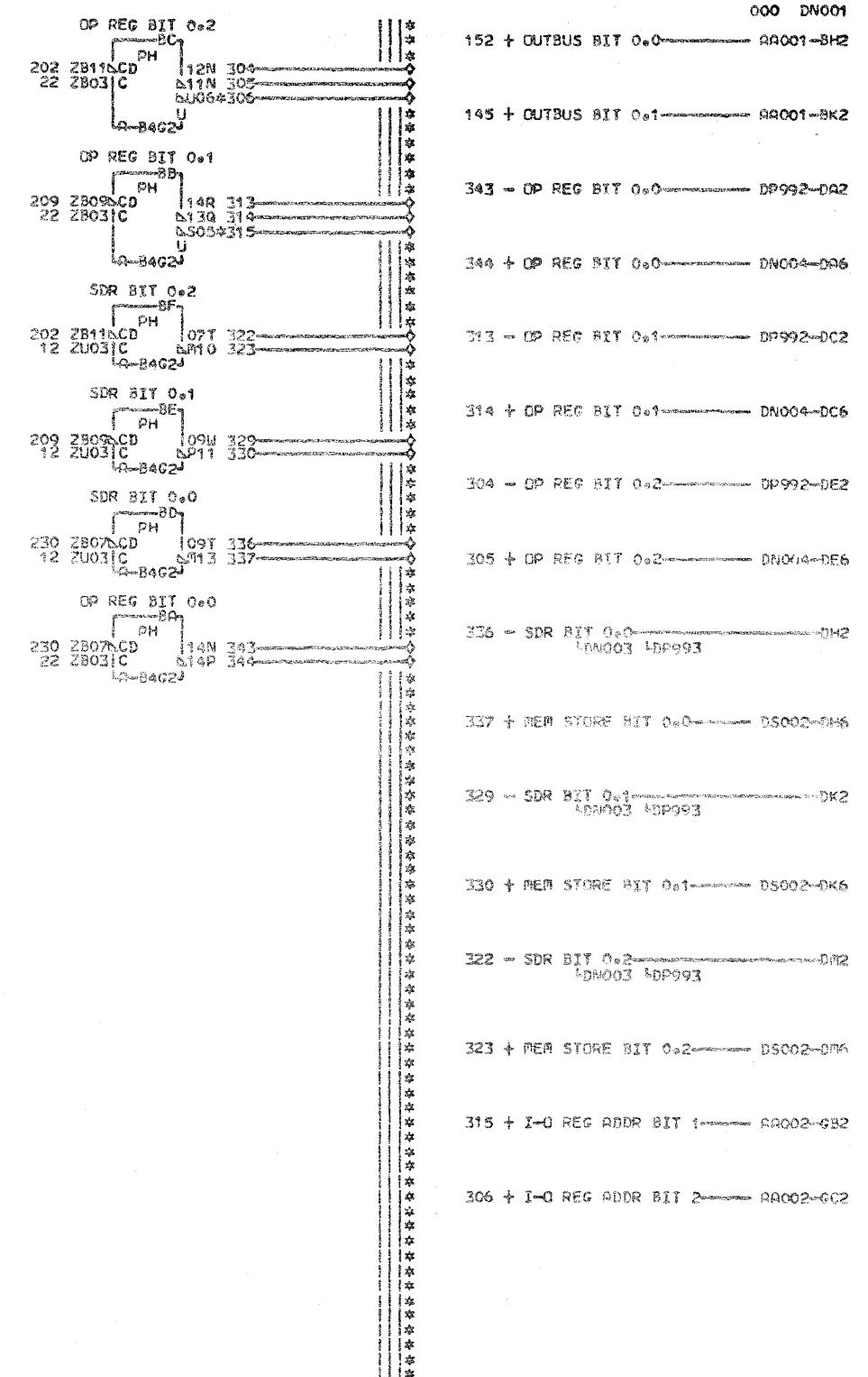
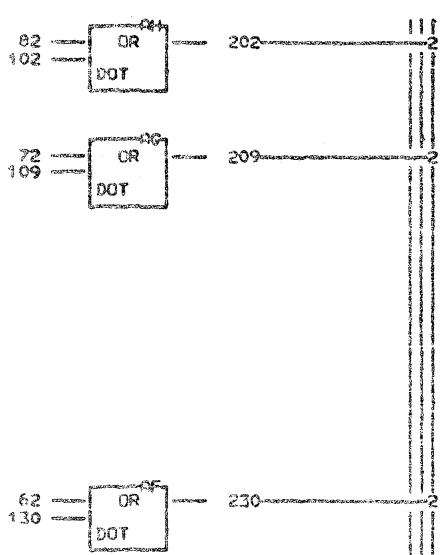
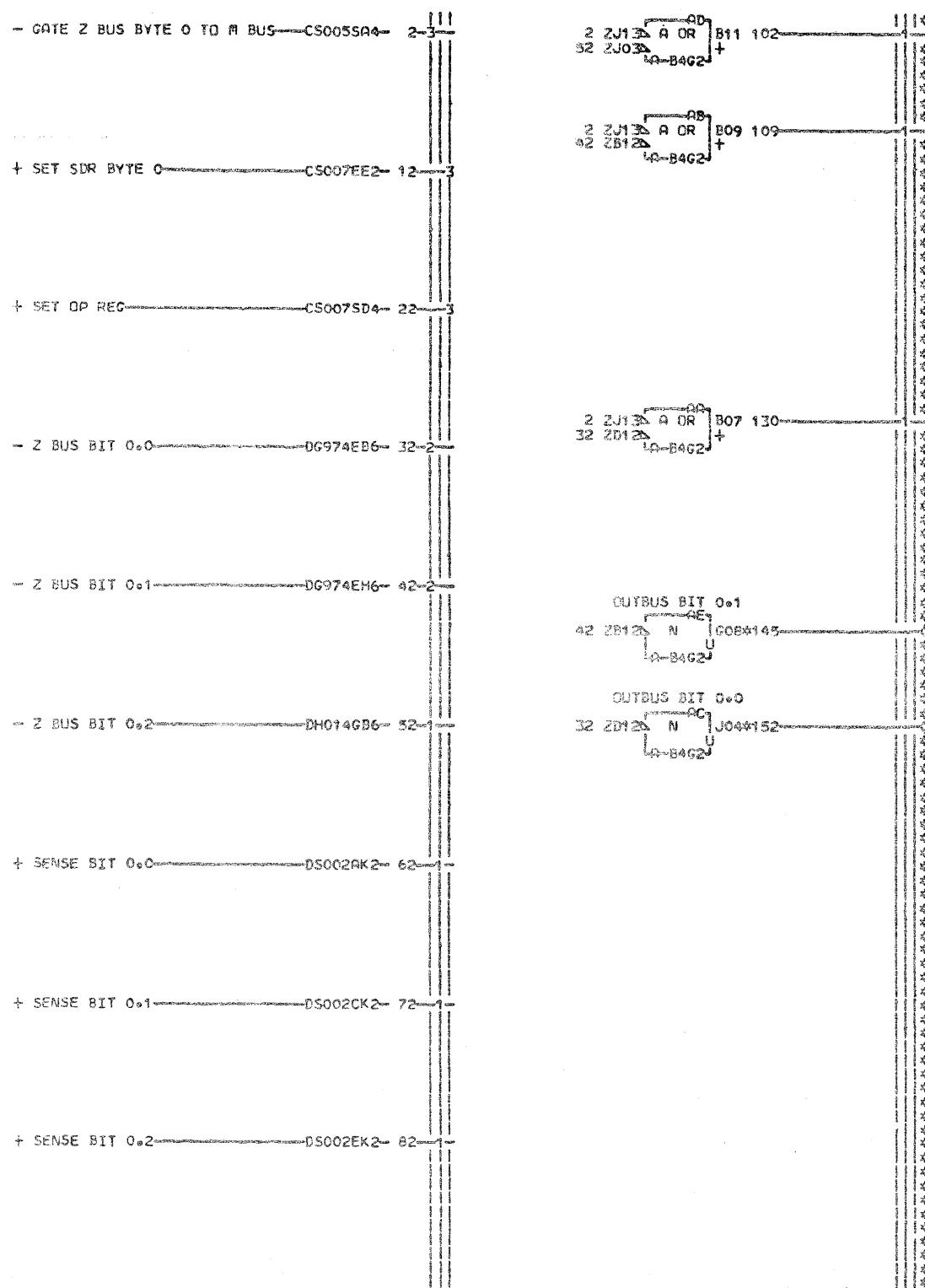


LOC. TYPE  
P-B4Q2 6802



ALU 0 AND ALU 1 BIT 1.7	E-C-HISTORY	MACH. 3705
		FRAME 01
		IBR CORP. SCD DM010
DATE LAST EC 10-14-80 344270	P.N. 1859626 000	

DM010  
000



EDGE CONNs  
 143 A-B4U4B05  
 152 A-B4U4B04  
 306 A-B4V5B05  
 315 A-B4V5B04

LOC. TYPE  
A-8462 AB90

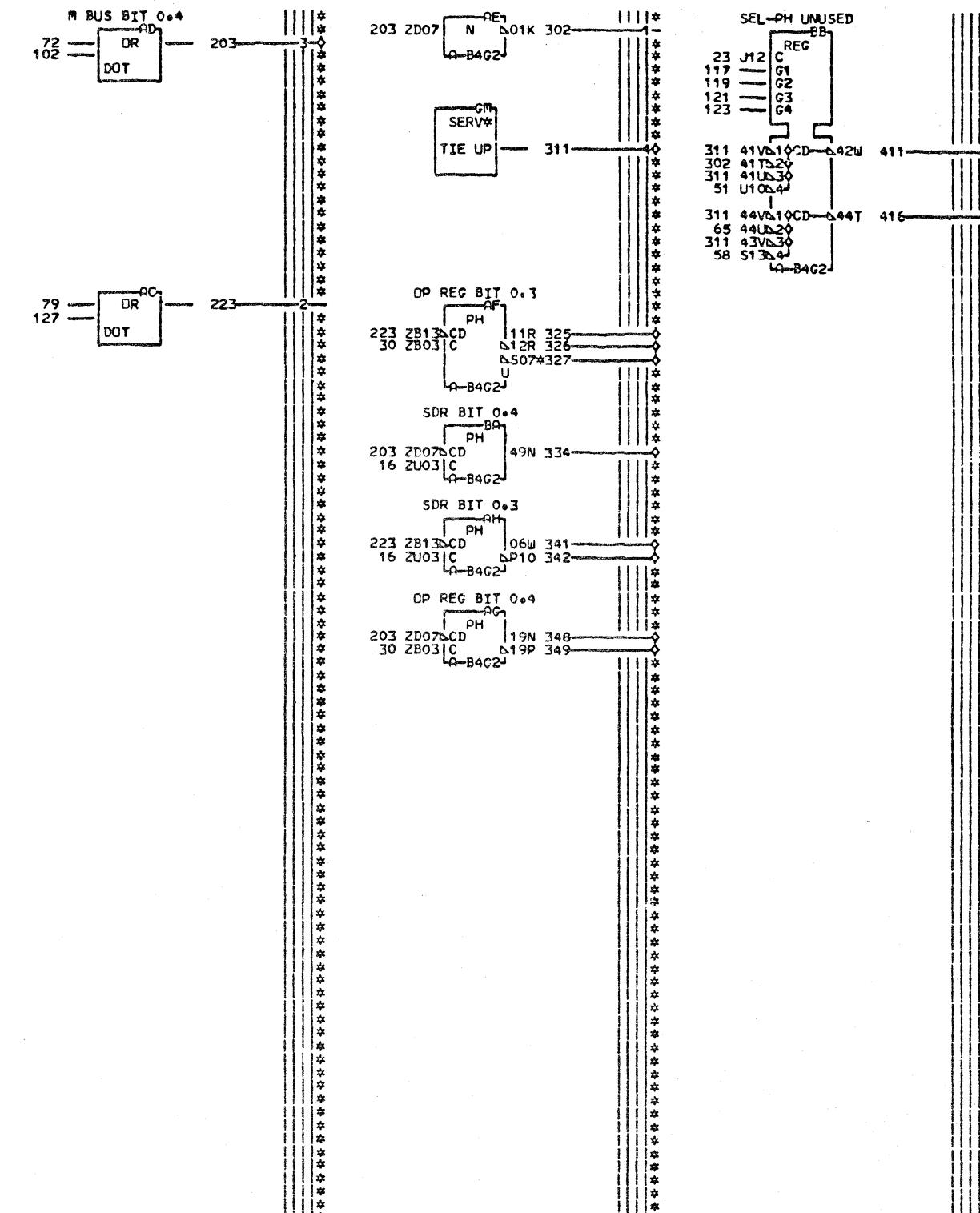
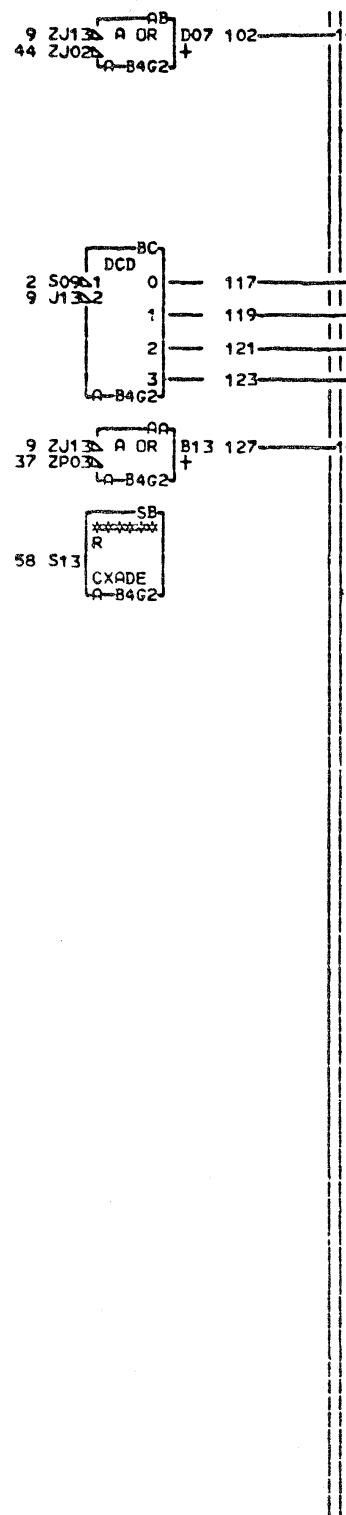
DN001  
000

CP REG AND SDR  
 BITS 0e-0e.4  
 -eC-HISTORY-FRACH3705  
 FRAME 01  
 IBM COPPeSCD DM001  
 DATE LAST EC  
 10-14-80 344270 PoN 1859627 C00

+ TIE UP AU001GF4- 2-1  
 - GATE Z BUS BYTE 0 TO M BUS CS005SA4- 9-3  
 + SET SDR BYTE 0 CS007EE2- 16-2  
 + SET SDR BYTE X CS007EG2- 23-  
 + SET OP REG CS007SD4- 30-2  
 - Z BUS BIT 0.3 DH014GF6- 37-  
 - Z BUS BIT 0.4 DH014GK6- 44-1  
 - FLDAT DN002003# 51-  
 ALWAYS MINUS DN002005- 58-  
 - M BUS BIT 0.5 DP991HH2- 65-  
 + SENSE BIT 0.4 DS002AM2- 72-1  
 + SENSE BIT 0.3 DS002GK2- 79-1

EDGE CONN.  
 51 RESISTOR  
 A-B4G2U10  
 327 A-B4V5B06

DN002  
000



LOC. TYPE  
A-B4G2 AB90

DP REG AND SDR BITS 0=0-0.4 E-C.+TISTORY	E-MACH.3705
FRAME 01	
DATE 10-14-80 LAST EC 344270	IBM CORP./SCD DA002
P.N. 1859628	000

- GATE SDR 0.0-0.4 TO A BUS — CF003DB6— 2-5  
 - SHIFT RIGHT BIT 0.0 — CF004CE6— 12  
 - SHIFT RIGHT — CF004CF6— 22-5  
 - SDR BIT 0.0 — DN001DH2— 32-2  
 - SDR BIT 0.1 — DN001DK2— 42-2  
 - SDR BIT 0.2 — DN001DM2— 52-2  
 - SDR BIT 0.3 — DN002DF2— 62-2  
 - SDR BIT 0.4 — DN002DH2— 72-

SHIFT RIGHT BI  
T 0.0 TO A BUS  
12 ZP045 A OR M12 104  
22 ZP020 U+  
LA-B4G2J  
  
 SDR BIT  
0.4 TO A BUS  
2 ZP130 A OR M07 111  
72 -42D U+  
LA-B4G2J  
  
 SDR BIT  
0.3 TO A BUS  
2 ZP130 A OR P09 118  
62 -36D U+  
LA-B4G2J  
  
 SDR BIT  
0.2 TO A BUS  
2 ZP130 A OR M09 125  
52 -37D U+  
LA-B4G2J  
  
 SDR BIT  
0.1 TO A BUS  
2 ZP130 A OR P12 132  
42 -39D U+  
LA-B4G2J  
  
 SDR BIT  
0.0 TO A BUS  
2 ZM110 A OR M12 139  
32 -37D U+  
LA-B4G2J  
  
 SHIFT RIGHT BI  
T 0.4 TO A BUS  
22 ZP020 A OR M07 146  
62 -32D U+  
LA-B4G2J  
  
 SHIFT RIGHT BI  
T 0.3 TO A BUS  
22 ZP020 A OR P09 153  
52 -46D U+  
LA-B4G2J  
  
 SHIFT RIGHT BI  
T 0.2 TO A BUS  
22 ZP020 A OR M09 160  
42 -47D U+  
LA-B4G2J  
  
 SHIFT RIGHT BI  
T 0.1 TO A BUS  
22 ZP020 A OR P12 167  
32 -49D U+  
LA-B4G2J

000 DN003  
 104 + SHIFT RIGHT BIT 0.0 TO A BUS — BB2  
LDG975

139 + SDR BIT 0.0 TO A BUS — DG975-BG2

167 + SHIFT RIGHT BIT 0.1 TO A BUS — CD2  
LDG975

132 + SDR BIT 0.1 TO A BUS — DG975-CE2

160 + SHIFT RIGHT BIT 0.2 TO A BUS — DF2  
LDH015

125 + SDR BIT 0.2 TO A BUS — DH015-DG2

153 + SHIFT RIGHT BIT 0.3 TO A BUS — EH2  
LDH015

118 + SDR BIT 0.3 TO A BUS — DH015-EJ2

146 + SHIFT RIGHT BIT 0.4 TO A BUS — FK2  
LDH015

111 + SDR BIT 0.4 TO A BUS — DH015-FL2

LOC. TYPE  
P-B4G2 AB90

OP REG AND SDR CARD	
BITS 0.0-0.4	
E-C-HISTORY	E-MACH-3705
FRAME	01
DATE LAST EC	DN003
10-14-80 344270	IBM CORP-SCD
P.N. 1859629	000

DN003  
000

+ OP REG BIT 0.0 — DN001DA6— 21  
 + OP REG BIT 0.1 — DN001DC6— 12  
 + OP REG BIT 0.2 — DN001DE6— 22  
 + OP REG BIT 0.3 — DN002DB6— 32  
 + OP REG BIT 0.4 — DN002DD6— 42

DP REG BIT 0.4  
 42 01E N D04\* 103  
 A-B4G2

DP REG BIT 0.3  
 32 06C N D03\* 110  
 A-B4G2

DP REG BIT 0.2  
 22 09F N D10\* 117  
 A-B4G2

DP REG BIT 0.1  
 12 06F N D06\* 124  
 A-B4G2

DP REG BIT 0.0  
 2 09C N D10\* 131  
 A-B4G2

000 DN004  
 131 — OP REG BIT 0.0 — GB2  
 LCD001 LCD002 LDN005

124 — OP REG BIT 0.1 — GD2  
 XC002 LCD001 LCD002 LC003  
 LC2001 LDN005

117 — OP REG BIT 0.2 — GF2  
 XC003 LCD003 LCD001 LCD002  
 XC002 LC003 LC2001 LCZ002  
 LDN005

110 — OP REG BIT 0.3 — GH2  
 XC003 LCD001 LCD002 LCFO02  
 XC003 LC004 LCZ001 LCZ002  
 LDN005

103 — OP REG BIT 0.4 — GK2  
 LCD001 LCD002 LC003 LDN005

NOTE. SEE PAGE DN005  
FOR GATE OP REG  
TO INDICATOR GATES

DN004  
000

EDGE CONN. 01A-B3A6D02  
 103 A-B4B1B11  
 01A-B3B6B02  
 110 A-B4B1A13  
 01A-B3B6A04  
 117 A-B4A1E11  
 01A-B3A6E02  
 124 A-B4A1D13  
 01A-B3A6D04  
 131 A-B4A1D11

LOC. TYPE  
A-B4G2 AB90

OP REG AND SDR BITS 0.0-0.4	E-C HISTORY	E-MACH 3705
FRAME 01		
DATE 10-14-80	LAST EC 344270	IBM CORP-SCD DN004
		P.N. 1859630

- GATE OP REG TO IND CU001FM6 2-D3  
 - OP REG BIT 0.0 DN004GB2 7  
 - OP REG BIT 0.1 DN004GD2 12  
 - OP REG BIT 0.2 DN004GF2 17  
 - OP REG BIT 0.3 DN004GH2 22-1  
 - OP REG BIT 0.4 DN004GK2 27  
 - OP REG BIT 0.5 DP991GS6 32-1  
 - OP REG BIT 0.6 DP991GE6 37-1  
 - OP REG BIT 0.7 DP992GA6 42-1  
 - OP REG BIT 1.0 DQ004GB2 47-1  
 - OP REG BIT 1.1 DQ004GD2 52-1  
 - OP REG BIT 1.2 DQ004GF2 57-1  
 - OP REG BIT 1.3 DQ004GH2 62-1  
 - OP REG BIT 1.4 DQ004GK2 67-1  
 - OP REG BIT 1.5 DR991GB6 72-1  
 - OP REG BIT 1.6 DR991GE6 77-1  
 - OP REG BIT 1.7 DR992GA6 82-1

OP REG  
 BIT 1.7 TO IND  
 2 ZP04 A OR S13 104  
 82 ZU13 U+ LA-B4E2  
 OP REG  
 BIT 1.6 TO IND  
 2 ZP04 A OR S12 111  
 77 ZU12 U+ LA-B4E2  
 OP REG  
 BIT 1.5 TO IND  
 2 ZP04 A OR S11 118  
 72 ZU11 U+ LA-B4E2  
 OP REG  
 BIT 1.4 TO IND  
 2 ZP04 A OR S10 125  
 67 ZU10 U+ LA-B4E2  
 OP REG  
 BIT 1.3 TO IND  
 2 ZP04 A OR S09 132  
 62 ZU09 U+ LA-B4E2  
 OP REG  
 BIT 1.2 TO IND  
 2 ZP04 A OR S08 139  
 57 ZU07 U+ LA-B4E2  
 OP REG  
 BIT 1.1 TO IND  
 2 ZP04 A OR S07 146  
 52 ZU06 U+ LA-B4E2  
 OP REG  
 BIT 1.0 TO IND  
 2 ZP04 A OR S05 153  
 47 ZU05 U+ LA-B4E2  
 OP REG  
 BIT 0.7 TO IND  
 2 ZP04 A OR S04 160  
 42 ZU04 U+ LA-B4E2  
 OP REG  
 BIT 0.6 TO IND  
 2 ZP04 A OR M12 167  
 37 ZU03 U+ LA-B4E2  
 OP REG  
 BIT 0.5 TO IND  
 2 ZP04 A OR M11 174  
 32 ZP13 U+ LA-B4E2  
 OP REG  
 BIT 0.4 TO IND  
 2 ZP04 A OR M10 181  
 27 ZP12 U+ LA-B4E2  
 OP REG  
 BIT 0.3 TO IND  
 2 ZP04 A OR M09 188  
 22 ZP11 U+ LA-B4E2

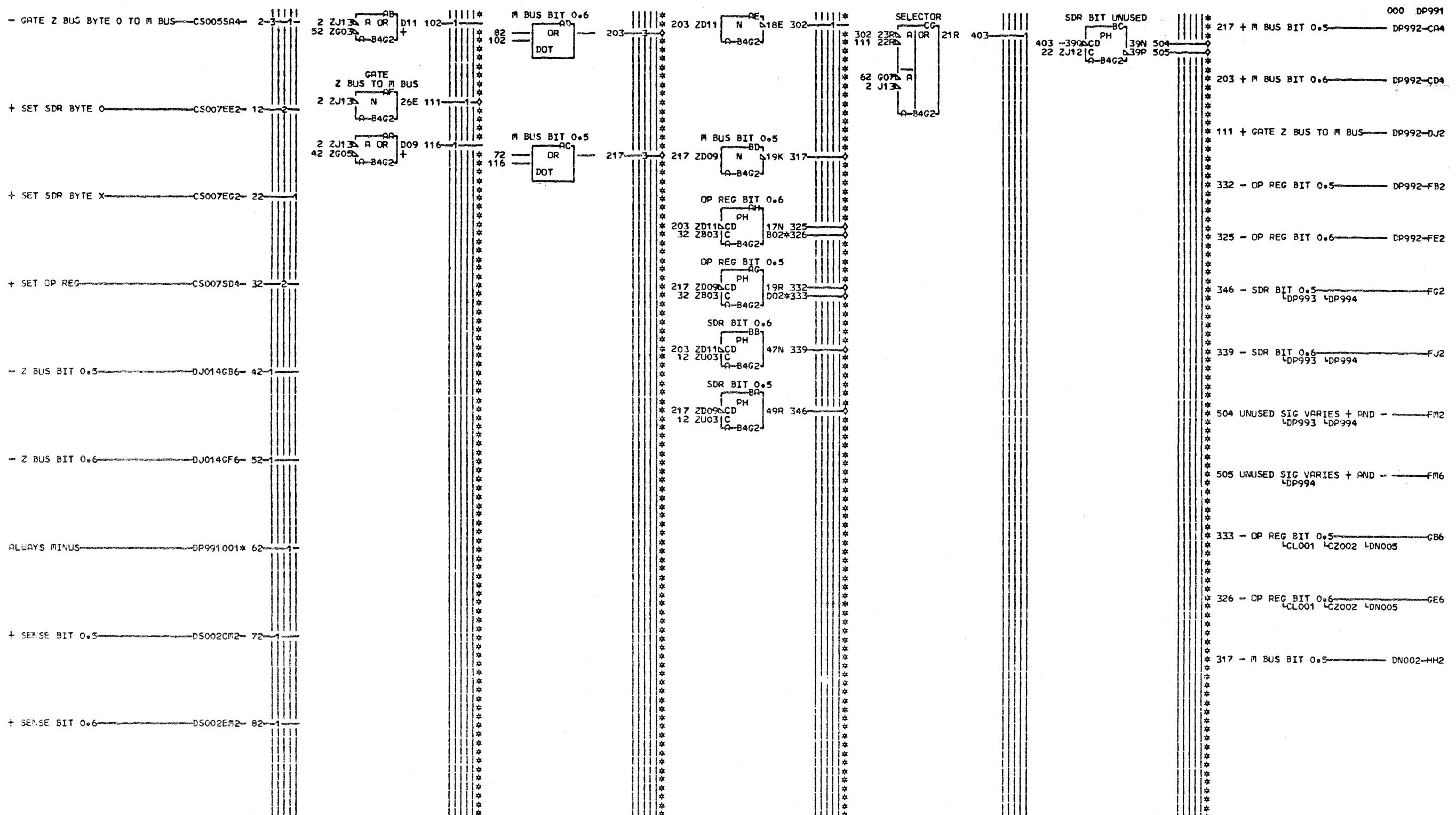
LOC. TYPE  
A-B4E2 AB89

OP REG  
 BIT 0.2 TO IND  
 2 ZP04 A OR M08 204  
 17 ZP10 U+ LA-B4E2  
 OP REG  
 BIT 0.1 TO IND  
 2 ZP04 A OR M07 211  
 12 ZP09 U+ LA-B4E2  
 OP REG  
 BIT 0.0 TO IND  
 2 ZP04 A OR M03 218  
 7 ZP07 U+ LA-B4E2

000 DN005  
 218 + OP REG BIT 0.0 TO IND AP012-CC2  
 211 + OP REG BIT 0.1 TO IND AP012-CD2  
 204 + OP REG BIT 0.2 TO IND AP012-CE2  
 188 + OP REG BIT 0.3 TO IND AP012-CF2  
 181 + OP REG BIT 0.4 TO IND AP013-CG2  
 174 + OP REG BIT 0.5 TO IND AP013-CH2  
 167 + OP REG BIT 0.6 TO IND AP013-CJ2  
 160 + OP REG BIT 0.7 TO IND AP013-CK2  
 153 + OP REG BIT 1.0 TO IND AP014-FC2  
 146 + OP REG BIT 1.1 TO IND AP014-FD2  
 139 + OP REG BIT 1.2 TO IND AP014-FE2  
 132 + OP REG BIT 1.3 TO IND AP014-FF2  
 125 + OP REG BIT 1.4 TO IND AP015-FG2  
 118 + OP REG BIT 1.5 TO IND AP015-FH2  
 111 + OP REG BIT 1.6 TO IND AP015-FJ2  
 104 + OP REG BIT 1.7 TO IND AP015-FK2

DN005  
000

FROM SDR CARDS	GATE OP REG TO INDICATORS
E.C.-HISTORY	E.MACH-3705
FRAME	01
DATE LAST EC	DN005
10-14-80 344270	P.No. 1859631 000



NOTE. SEE PAGE DN005  
FOR GATE OP REG  
TO INDICATOR. GATES

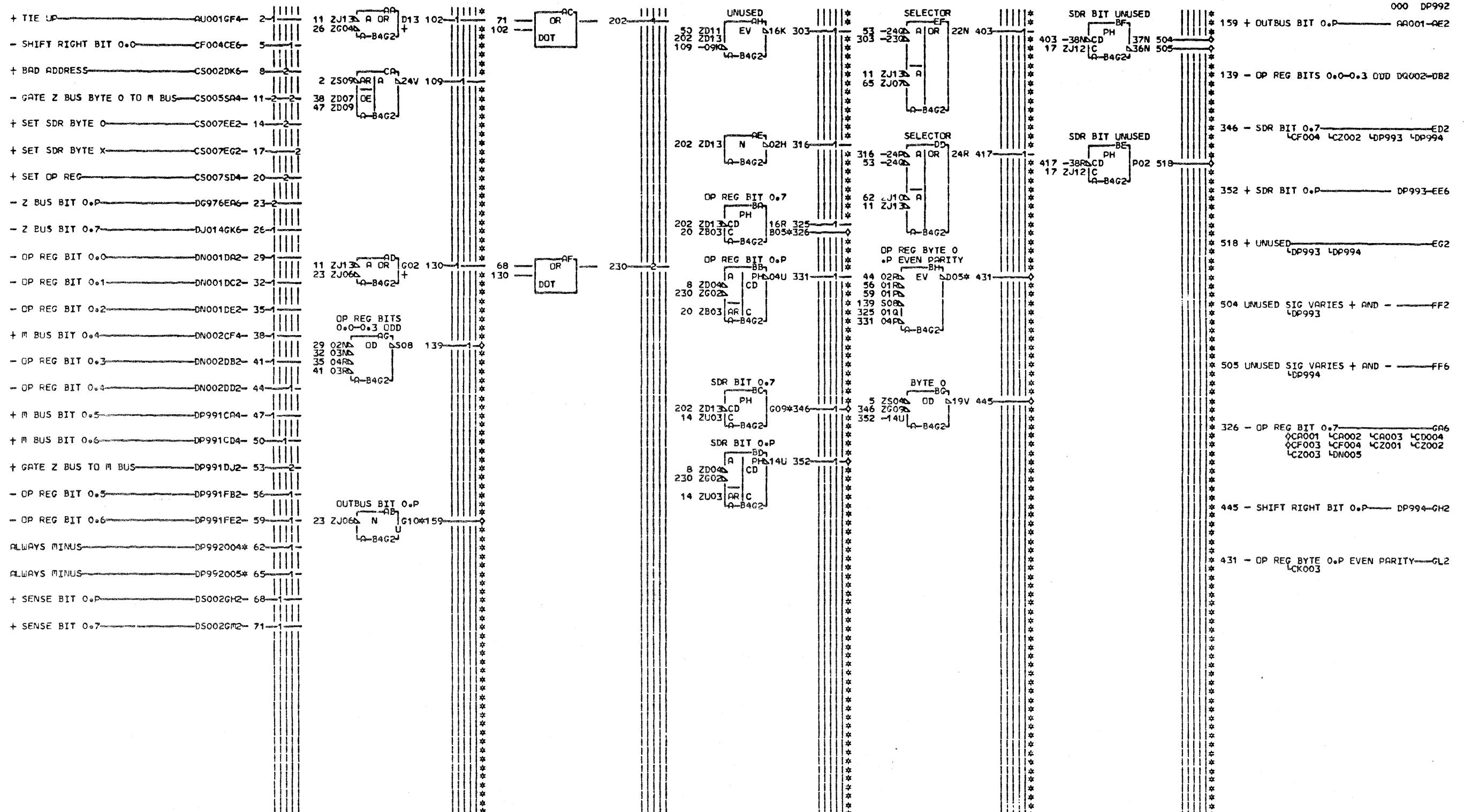
EDGE CONN.  
62 RESISTOR  
A-B4G2G0  
326 P-B481C1  
01A-B3B6C0  
333 P-B481B1  
01A-B3B6B0

LOC. TY  
A-B4G2 AB

DP991

00-0

OP REG AND SDR  
BITS 0.5-0.7  
E-C-E-HISTORY E-MACH-3705  
DATE LAST EC FRAME 01  
10-14-80 344270 IBM CORP+SCD DP991  
P.N. 1859632 000



NOTE: SEE PAGE DNO05  
FOR GATE OP REG  
TO INDICATOR GATES

EDGE CONN. 431 A-B4N6B04  
 62 RESISTOR 01A-B3N1B13  
 65 RESISTOR A-B4G2J10  
 159 RESISTOR A-B4G2J07  
 326 A-B4U4B02  
 326 A-B4B1C13  
 01A-B3B6C04  
 346 A-B4Q1E11  
 01A-B3Q6E02

LOC. TYPE  
A-B4G2 AB90

OP REG AND SDR BITS 0.5-0.7	
-Ec-HISTORY--E-MACH-3705	
FRAME	01
DATE	LAST EC
IBM CORP. SCD	DP992
P/N. 1859633 000	

10-14-80 344270

+ TIE UP	AU001GF4-	2
+ FORCE STORAGE PARITY ERROR	CK001GD2-	6
+ I1.B TIME INHIBIT SDR 0	CS005SJ4-	10
- SDR BIT 0.0	DN001DH2-	14
- SDR BIT 0.1	DN001DK2-	18
- SDR BIT 0.2	DN001DM2-	22
- SDR BIT 0.3	DN002DF2-	26
- SDR BIT 0.4	DN002DH2-	30
ALWAYS PLUS	DN002GE2-	34
ALWAYS PLUS	DN002GE7-	38
+ SDR CARD BYTE 0 TIE UP	DN002GM4-	42
- SDR BIT 0.5	DP991FG2-	46
- SDR BIT 0.6	DP991FJ2-	50
UNUSED SIG VARIES + AND -	DP991FM2-	54
- SDR BIT 0.7	DP992ED2-	58
+ SDR BIT 0.P	DP992EE6-	62
+ UNUSED	DP992EG2-	66
UNUSED SIG VARIES + AND -	DP992FF2-	70
- FLDAT	DP993002-	74

2 ZS09 OR 28V 102  
74 ZU07 29V 103  
LA-B4G2

74 ZU07 N AD 27T 116  
LA-B4G2

N.O.-N.7 ODD

14 -32RD AE  
18 -31RD OD DJ05 131  
22 -31RD  
26 -31RD  
30 -32RD  
46 -33RD  
50 -34RD  
58 ZG09  
LA-B4G2

+ SDR BIT 0.4

```

    graph TD
      30 ---|---> OR1[OR]
      31 ---|---> OR1
      32 ---|---> OR1
      33 ---|---> OR1
      102 ---|---> OR1
      103 ---|---> OR1
      34 ---|---> OR1
      OR1 ---|---> INV1[INV]
      INV1 ---|---> M05[M05]
  
```

+ SDR BIT 0.5

```

    graph TD
      46 ---|---> OR2[OR]
      33 ---|---> OR2
      34 ---|---> OR2
      102 ---|---> OR2
      103 ---|---> OR2
      38 ---|---> OR2
      39 ---|---> OR2
      OR2 ---|---> INV2[INV]
      INV2 ---|---> M08[M08]
  
```

+ SDR BIT 0.6

```

    graph TD
      50 ---|---> OR3[OR]
      39 ---|---> OR3
      38 ---|---> OR3
      74 ---|---> OR3
      116 ---|---> OR3
      33 ---|---> OR3
      32 ---|---> OR3
      OR3 ---|---> INV3[INV]
      INV3 ---|---> J09[J09]
  
```

+ SDR BIT 0.7

```

    graph TD
      74 ---|---> OR4[OR]
      32 ---|---> OR4
      58 ---|---> OR4
      26 ---|---> OR4
      116 ---|---> OR4
      33 ---|---> OR4
      66 ---|---> OR4
      OR4 ---|---> INV4[INV]
      INV4 ---|---> U09[U09]
  
```

ALWAYS - OUT  
BYTE 0 PC

```

    graph TD
      62 ---|---> OR5[OR]
      19 ---|---> OR5
      131 ---|---> OR5
      ZJ05 ---|---> OR5
      34 ---|---> OR5
      07L ---|---> OR5
      38 ---|---> OR5
      06L ---|---> OR5
      42 ---|---> OR5
      08H ---|---> OR5
      54 ---|---> OR5
      06J ---|---> OR5
      66 ---|---> OR5
      ZP02 ---|---> OR5
      70 ---|---> OR5
      07H ---|---> OR5
      OR5 ---|---> INV5[INV]
      INV5 ---|---> AC1[AC1]
      AC1 ---|---> DE[DE]
      AC1 ---|---> OR[OR]
      AC1 ---|---> DC12[DC12]
  
```

**MEMORY PG**

```

    +---+-----+
    | ZM05 | AB1 |
    +---+-----+
    | ZM08 | EV  D11 303
    +---+-----+
    | ZJ09 |
    +---+
    | ZU09 |
    +---+
    | ZC11 |
    +---+
    | -22L6 |
    +---+
    | -21L6 |
    +---+
    | -21L6 |
    +---+
    | -21K6 |
    +---+
    | 4A-84G2 |
    +---+
  
```

**SDR BYTE**

0 EVEN PARITY

```

    +---+-----+
    | 10 | A   C   * 332
    +---+-----+
    | 256 | DOT
    +---+
  
```

The diagram illustrates the connection between memory components and a control logic block. The top section, labeled "MEMORY PG", shows a vertical stack of memory modules: ZM05, ZM08, ZJ09, ZU09, ZC11, -22L6, -21L6, -21L6, and -21K6. The output of ZM08 is connected to an "EV" pin, which is then connected to a "D11" pin and a "303" component. The bottom section, labeled "SDR BYTE", shows a logic block with inputs from pins 10 and 256, and an output to pin 332. The output of this block is labeled "DOT". A label "AF" is positioned above the connection between the two sections.

+ MEM STORE BIT 0.6 — DS002-BD2

+ MEM STORE BIT 0.7 — DS002-BF2

- MEM STORE BIT 0.P — DS002-CM2

+ MEM STORE BIT 0.4 — DS002-CN2

+ MEM STORE BIT 0.5 — DS002-CP2

+ GENERATED SDR BIT ODD 0.P — CV4  
LCP003

- SDR BYTE 0 EVEN PARITY — CK003-SA4

+ TIE UP AU001GF4 2-5

+ BLOCK COMPLETE SDR CF003AC6 8-1

- GATE SDR 0-5-0-7 TO A BUS CF003DE6 14-3

- SHIFT RIGHT CF004CF6 20-62

- SDR BIT 0-4 DN002DH2 26-1

ALWAYS PLUS DN002GE2 32-3

ALWAYS PLUS DN002GE7 38-3

- SDR BIT 0-5 DP991FG2 44-2

- SDR BIT 0-6 DP991FJ2 50-2

UNUSED SIG VARIES + AND - DP991FM2 56-2

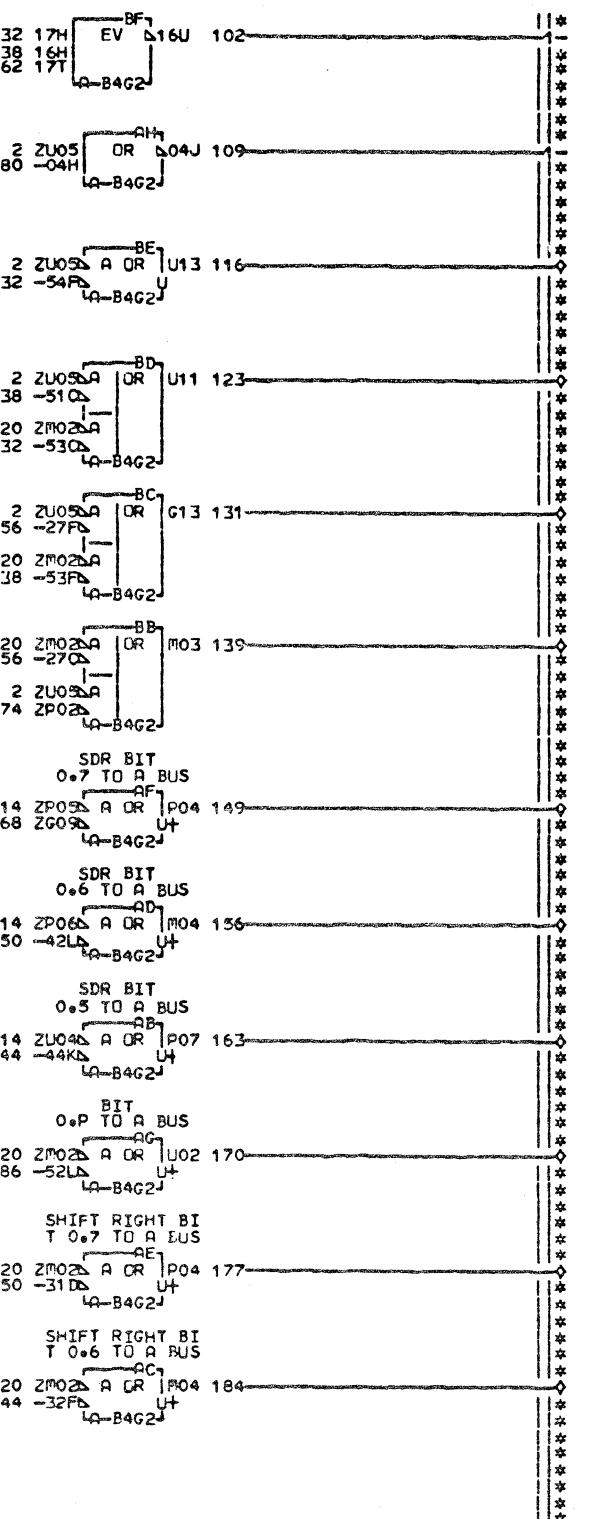
UNUSED SIG VARIES + AND - DP991FM6 62-1

- SDR BIT 0-7 DP992ED2 68-1

+ UNUSED DP992EG2 74-1

UNUSED SIG VARIES + AND - DP992FF6 80-1

- SHIFT RIGHT BIT C,P DP992GH2 86-1



20 ZM02A OR S03 202  
102 -5214 AA  
8 ZS02A  
109 -54KA U  
4-B4G2

SHIFT RIGHT BI  
T 0.5 TO A BUS

20 ZM02A OR P07 212  
26 -34E AA  
4-B4G2

000 DP994  
212 + SHIFT RIGHT BIT 0.5 TO A BUS—BB2  
LDJ015

163 + SDR BIT 0.5 TO A BUS—DJ015-BG2

184 + SHIFT RIGHT BIT 0.6 TO A BUS—BE2  
LDJ015

156 + SDR BIT 0.6 TO A BUS—DJ015-BF2

177 + SHIFT RIGHT BIT 0.7 TO A BUS—BH2  
LDJ015

149 + SDR BIT 0.7 TO A BUS—DJ015-BJ2

170 + BIT 0.P TO A BUS—DG975-CL2

202 GROUND LEVEL—FC4

139 GROUND LEVEL—FF4

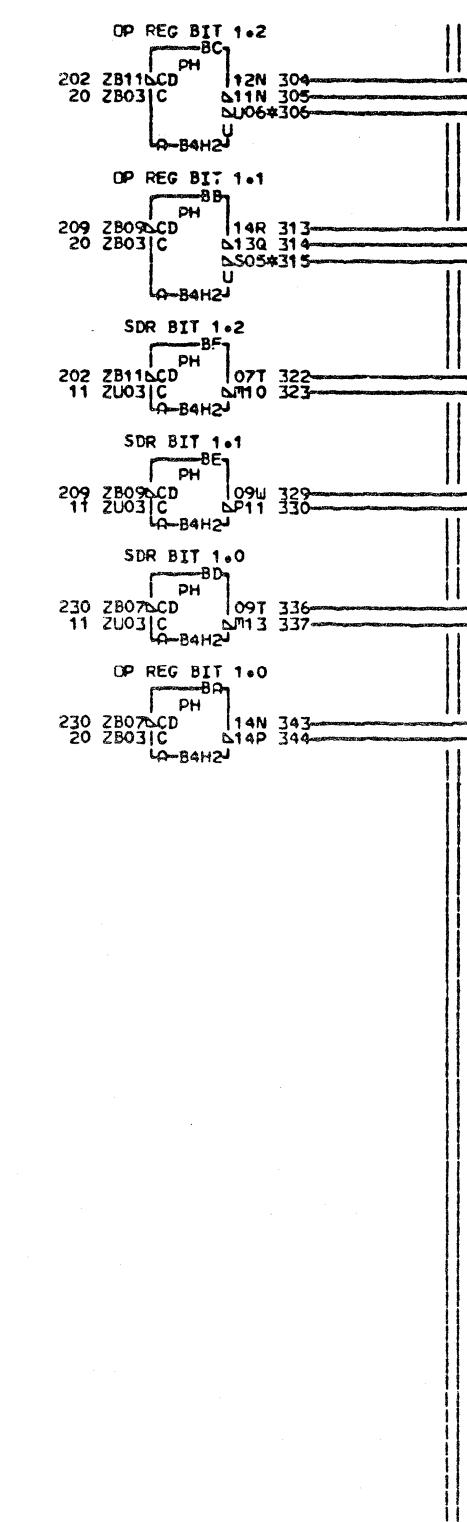
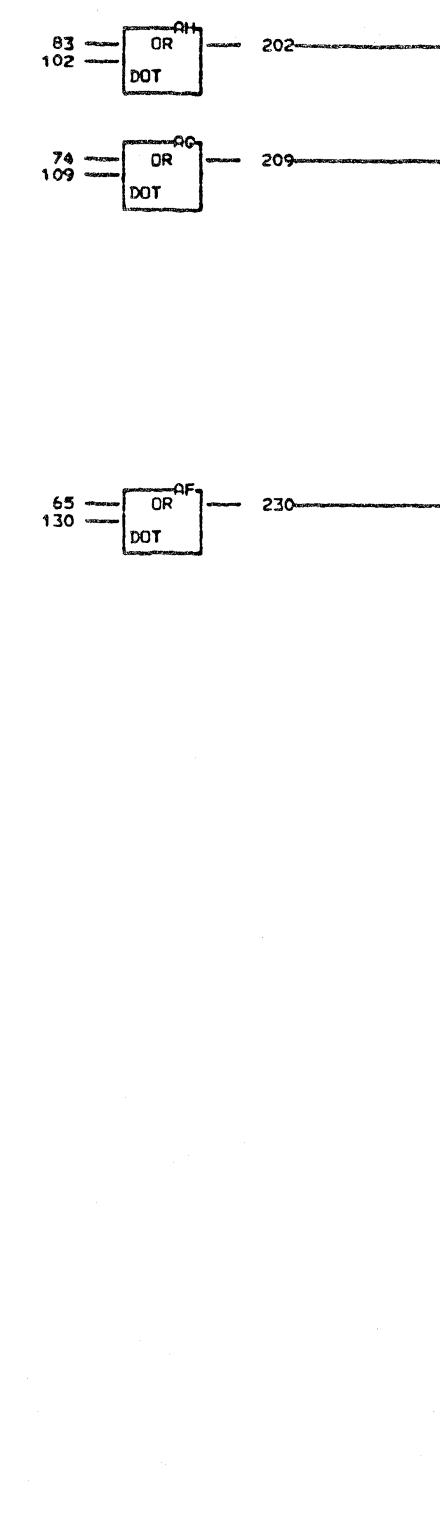
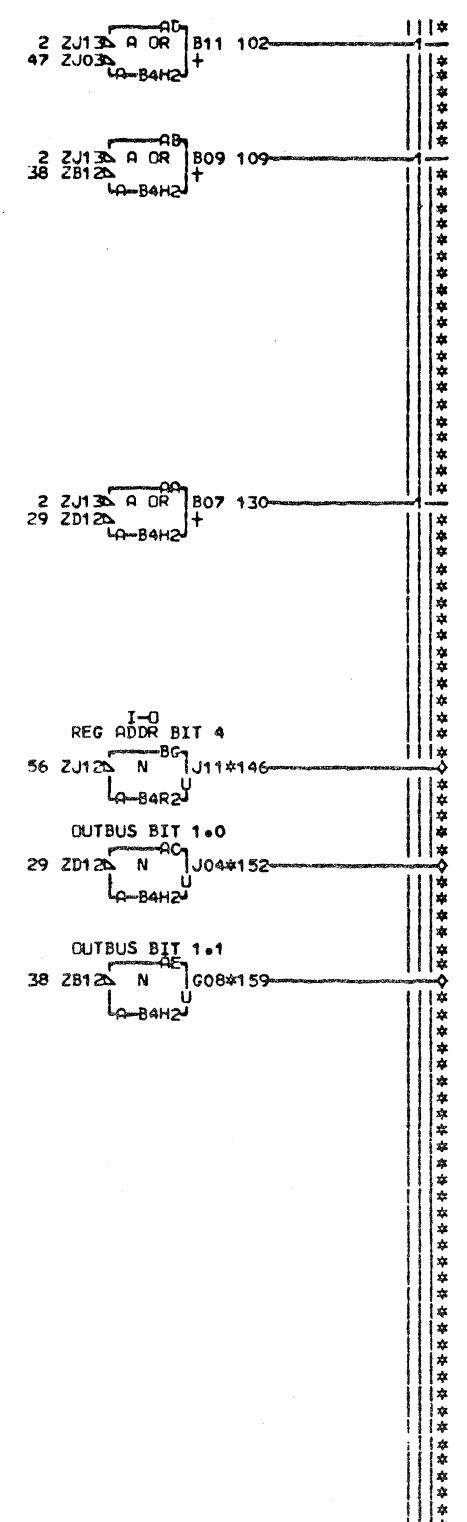
131 GROUND LEVEL—FJ4

123 GROUND LEVEL—FL4

116 GROUND LEVEL—GR2

DP994  
000

- GATE Z BUS BYTE 1 TO M BUS CS0055C4- 2-3  
 + SET SDR BYTE 1 CS007EF2- 11-3  
 + SET OP REG CS007SD4- 20-3  
 - Z BUS BIT 1.0 DK974EB6- 29-2  
 - Z BUS BIT 1.1 DK974EH6- 38-2  
 - Z BUS BIT 1.2 DL004GB6- 47-  
 - OP REG BIT 1.0 DQ004GB2- 56-  
 + SENSE BIT 1.0 DS004AK2- 65-  
 + SENSE BIT 1.1 DS004CK2- 74-  
 + SENSE BIT 1.2 DS004EK2- 83-

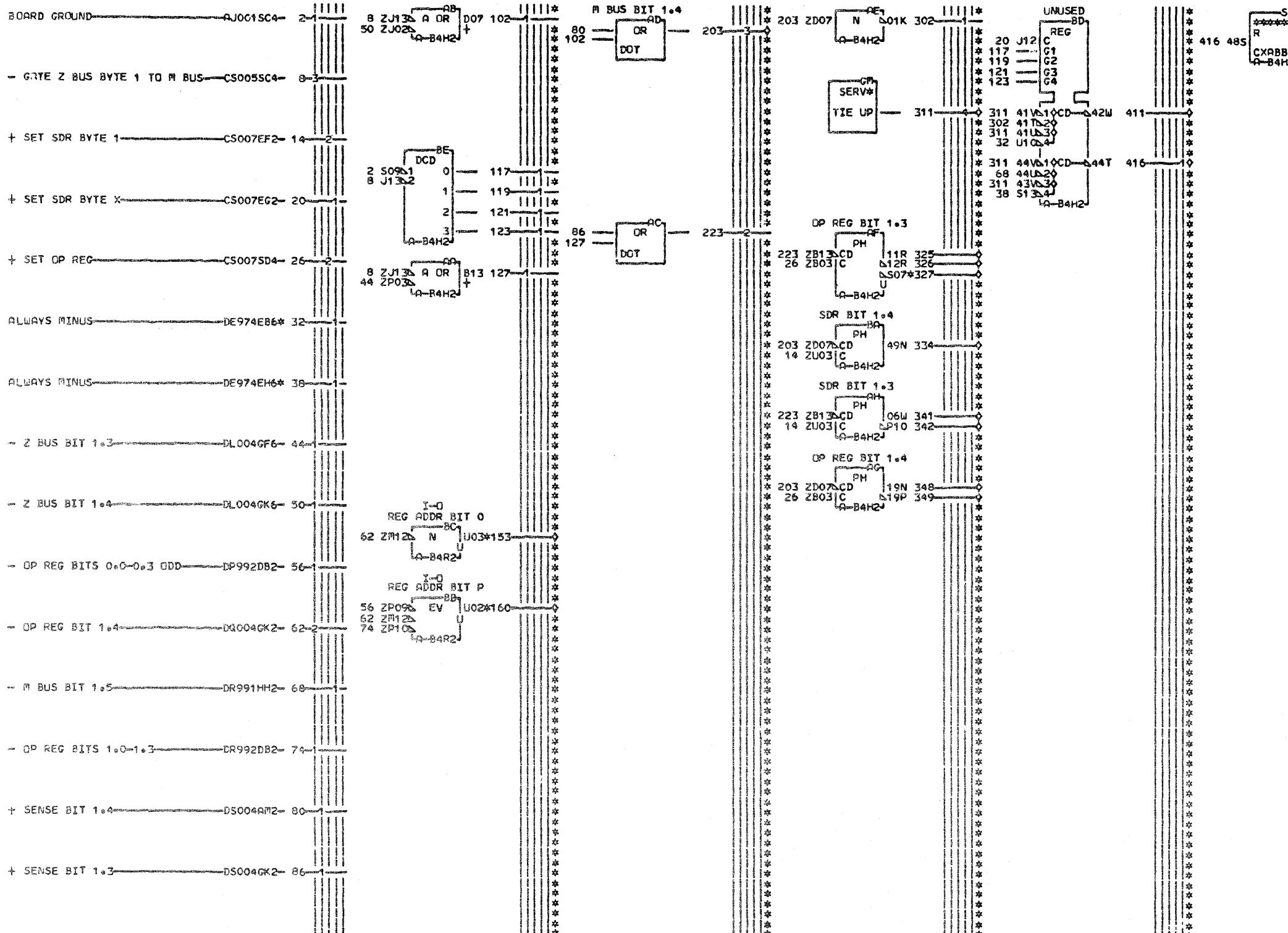


000 DQ001  
 152 + OUTBUS BIT 1.0 AA001-BK2  
 159 + OUTBUS BIT 1.1 AA001-BK2  
 343 - OP REG BIT 1.0 DR992-DA2  
 344 + OP REG BIT 1.0 DQ004-DA6  
 313 - OP REG BIT 1.1 DR992-DC2  
 314 + OP REG BIT 1.1 DQ004-DC6  
 304 - OP REG BIT 1.2 DR992-DE2  
 305 + OP REG BIT 1.2 DQ004-DE6  
 336 - SDR BIT 1.0 LDQ003 DR993 DH2  
 337 + MEM STORE BIT 1.0 DS004-DH6  
 329 - SDR BIT 1.1 LDQ003 DR993 DK2  
 330 + MEM STORE BIT 1.1 DS004-DK6  
 322 - SDR BIT 1.2 LDQ003 DR993 DM2  
 323 + MEM STORE BIT 1.2 DS004-DM6  
 146 + I-O REG ADDR BIT 4 AA002-GB2  
 315 + I-O REG ADDR BIT 5 AA002-GB2  
 306 + I-O REG ADDR BIT 6 AA002-GC2

EDGE CONN.  
 146 A-B4V5B08  
 152 A-B4U4D03  
 159 A-B4U4D05  
 306 A-B4V5B10  
 315 A-B4V5B09

LOC. TYPE  
 A-B4H2 AB90  
 A-B4R2 6807

OP REG AND SDR BITS 1.0-1.4	E-C-HISTORY	MACH.3705
		FRAME 01
		IBM CORP-SCD DQ001
DATE 10-14-80	LST EC 344270	P.N. 1859636 000



000 DQ002  
203 + M BUS BIT 1:4 - DR992-CF4

325 - OP REG BIT 1:3 - DR992-DB2

326 + OP REG BIT 1:3 - DQ004-DB6

348 - OP REG BIT 1:4 - DR992-DD2

349 + OP REG BIT 1:4 - DQ004-DD6

341 - SDR BIT 1:3 - DQ003 LDR993 DF2

342 + MEM STORE BIT 1:3 - DS004-DF6

334 - SDR BIT 1:4 - DQ003 LDR993 LDR994 DH2

160 + I-O REG ADDR BIT P - RA002-FK6

327 + I-O REG ADDR BIT 7 - RA002-GS2

153 + I-O REG ADDR BIT 0 - RA002-GD2

411 ALWAYS PLUS - LDR993 LDR994 GE2

416 ALWAYS PLUS - LDR993 LDR994 GE7

311 + SDR CARD BYTE 1 TIE UP - DR993-GM4

DQ002  
000

EDGE CONN.  
32 RESISTOR  
A-B4H2U10  
38 RESISTOR  
A-B4H2S13  
153 R-B4V5802  
160 R-B4V5813  
327 R-B4V5812

LOC. TYPE  
A-B4H2 AB90  
A-B4R2 6807

CP REG AND SDR BITS 1:0-1:4 E-C-E-HISTCRY	E-FACH-3705
FRAME 01	
DATE 10-14-60	LAST EC 344270
IBM CORP-SCD	DQ002
P.N. 1859637	000

- GATE SDR 1.0 TO A BUS — CF003DF6 — 2-1  
 - GATE SDR 1.1-1.5 TO A BUS — CF003EH6 — 12-4  
 - SHIFT RIGHT BIT 1.0 — CF004CC6 — 22-1  
 - SHIFT RIGHT — CF004CF6 — 32-5  
 - SDR BIT 1.0 — DQ001DH2 — 42-2  
 - SDR BIT 1.1 — DQ001DK2 — 52-2  
 - SDR BIT 1.2 — DQ001DM2 — 62-2  
 - SDR BIT 1.3 — DQ002DF2 — 72-2  
 - SDR BIT 1.4 — DQ002DH2 — 82-1

SDR BIT 1.0 TO A BUS  
 2 ZP11D A OR M12 104  
 42 -37CD U4  
 4A-B4H2J  
 SHIFT RIGHT BI T 1.4 TO A BUS  
 32 ZM02D A OR M07 111  
 72 -32CD U4  
 4A-B4H2J  
 SHIFT RIGHT BI T 1.3 TO A BUS  
 32 ZM02D A OR P09 118  
 62 -46D U4  
 4A-B4H2J  
 SHIFT RIGHT BI T 1.2 TO A BUS  
 32 ZM02D A OR M09 125  
 52 -47D U4  
 4A-B4H2J  
 SHIFT RIGHT BI T 1.1 TO A BUS  
 32 ZM02D A OR P12 132  
 42 -49D U4  
 4A-B4H2J  
 SHIFT RIGHT BI T 1.0 TO A BUS  
 22 ZS04D A OR M12 139  
 32 ZM02D U4  
 4A-B4H2J  
 SDR BIT 1.4 TO A BUS  
 12 ZP13D A OR M07 146  
 82 -32H U4  
 4A-B4H2J  
 SDR BIT 1.3 TO A BUS  
 12 ZP13D A OR P09 153  
 72 -36D U4  
 4A-B4H2J  
 SDR BIT 1.2 TO A BUS  
 12 ZP13D A OR M09 160  
 62 -37D U4  
 4A-B4H2J  
 SDR BIT 1.1 TO A BUS  
 12 ZP13D A OR P12 167  
 52 -39D U4  
 4A-B4H2J

000 DQ003  
 139 + SHIFT RIGHT BIT 1.0 TO A BUS — B82  
 4D975  
 104 + SDR BIT 1.0 TO A BUS — DK975-BC2  
 132 + SHIFT RIGHT BIT 1.1 TO A BUS — CD2  
 4D975  
 167 + SDR BIT 1.1 TO A BUS — DK975-CE2  
 125 + SHIFT RIGHT BIT 1.2 TO A BUS — DF2  
 4DL005  
 160 + SDR BIT 1.2 TO A BUS — DL005-DG2  
 118 + SHIFT RIGHT BIT 1.3 TO A BUS — EH2  
 4DL005  
 153 + SDR BIT 1.3 TO A BUS — DL005-EJ2  
 111 + SHIFT RIGHT BIT 1.4 TO A BUS — FK2  
 4DL005  
 146 + SDR BIT 1.4 TO A BUS — DL005-FL2

LDC TYPE  
R-B4H2 AB90

D0003  
000

CCU DATA FLOW	
SDR DP REG CARD	
E.C.—HISTORY	E.MACH.3705
FRAME 01	
DATE LAST EC	IBM CDRP.SCD DQ003
10-14-80 344270	PoN. 1859638 000

+ OP REG BIT 1.0 — DQ001DAG— 2  
 + OP REG BIT 1.1 — DQ001DC6— 12  
 + OP REG BIT 1.2 — DQ001DE6— 22  
 + OP REG BIT 1.3 — DQ002DB6— 32  
 + OP REG BIT 1.6 — DQ002DD6— 42

DP REG BIT 1.4  
 42 04E N D04\* 103  
 A-B4H2

DP REG BIT 1.3  
 32 06C N D03\* 110  
 A-B4H2

DP REG BIT 1.2  
 22 09F N D10\* 117  
 A-B4H2

DP REG BIT 1.1  
 12 06F N D06\* 124  
 A-B4H2

DP REG BIT 1.0  
 2 09C N D10\* 131  
 A-B4H2

000 DQ004  
 131 — OP REG BIT 1.0 — GK2  
 LCD001 LCF002 LCF003 LCL004  
 LCQ005 LCX001 LDN005 LDQ001

124 — OP REG BIT 1.1 — GD2  
 CA003 LCD001 LCD003 LCL004  
 C2001 C2003 LDN005

117 — OP REG BIT 1.2 — GF2  
 CA003 LCD001 LCD003 LCL004  
 C2003 LDN005

110 — OP REG BIT 1.3 — GH2  
 CA003 LCD001 LCD003 LCL004  
 C2003 LDN005

103 — OP REG BIT 1.4 — GK2  
 LCD002 CD003 LDN005 LDQ002

NOTE: SEE PAGE DN005  
 FOR GATE OP REG  
 TO INDICATOR GATES

EDGE CONN.  
 103 A-B4C1B11  
 01A-B3C6B02  
 110 A-B4C1A13  
 01A-B3C6A04  
 117 A-B4C1A11  
 01A-B3C6A02  
 124 A-B4B1E13  
 01A-B3B6E04  
 131 A-B4B1D11

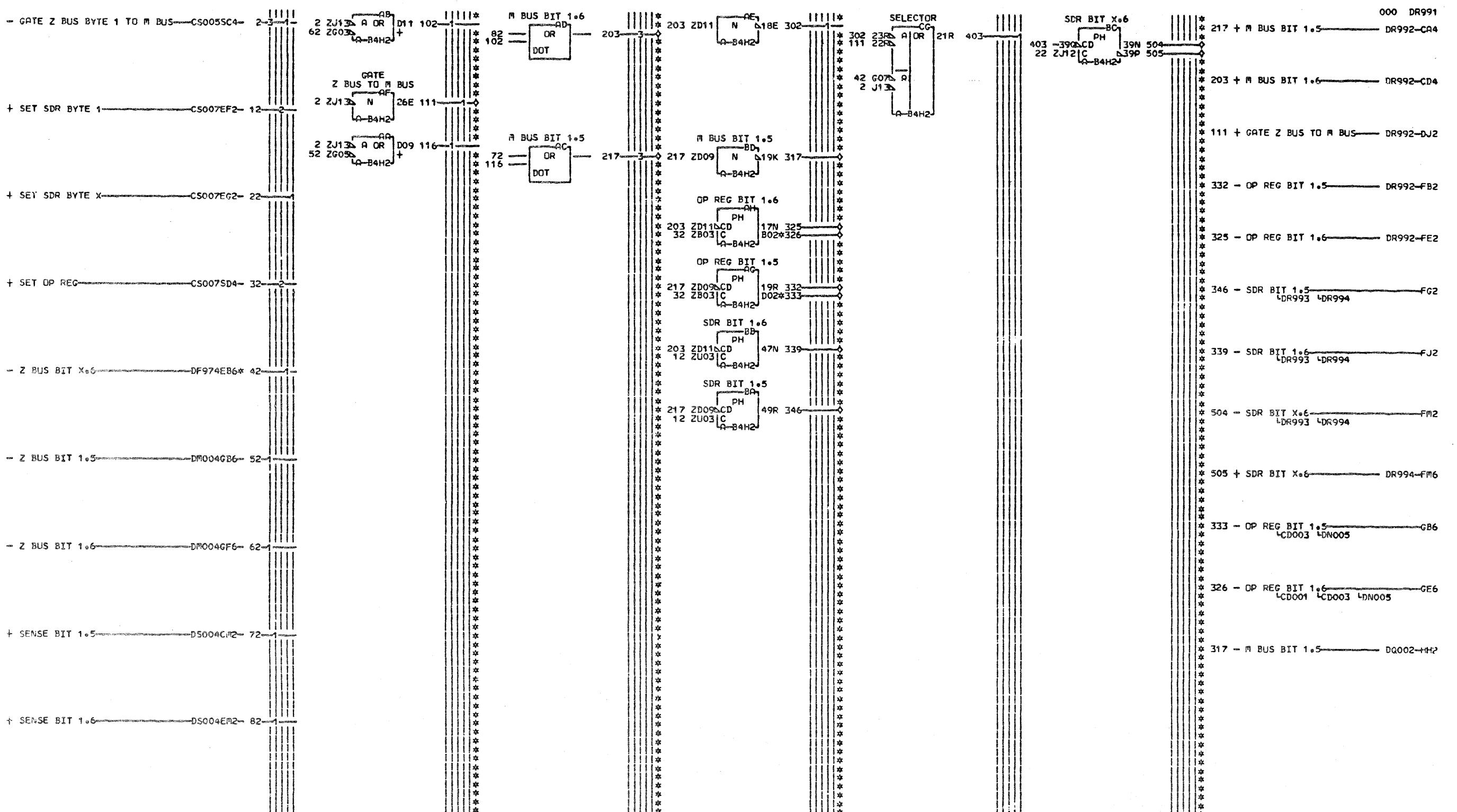
01A-B3B6D02

LOC. TYPE  
A-B4H2 AB90

00004

000

CCU DATA FLOW	
SDR OP REG CARD	
E-Ca-HISTORY	E-FACH-3705
FRAME 01	
DATE 10-14-80	LAST EC 304270
IBM CORP-SCD DQ004	
P.N. 1859639 000	

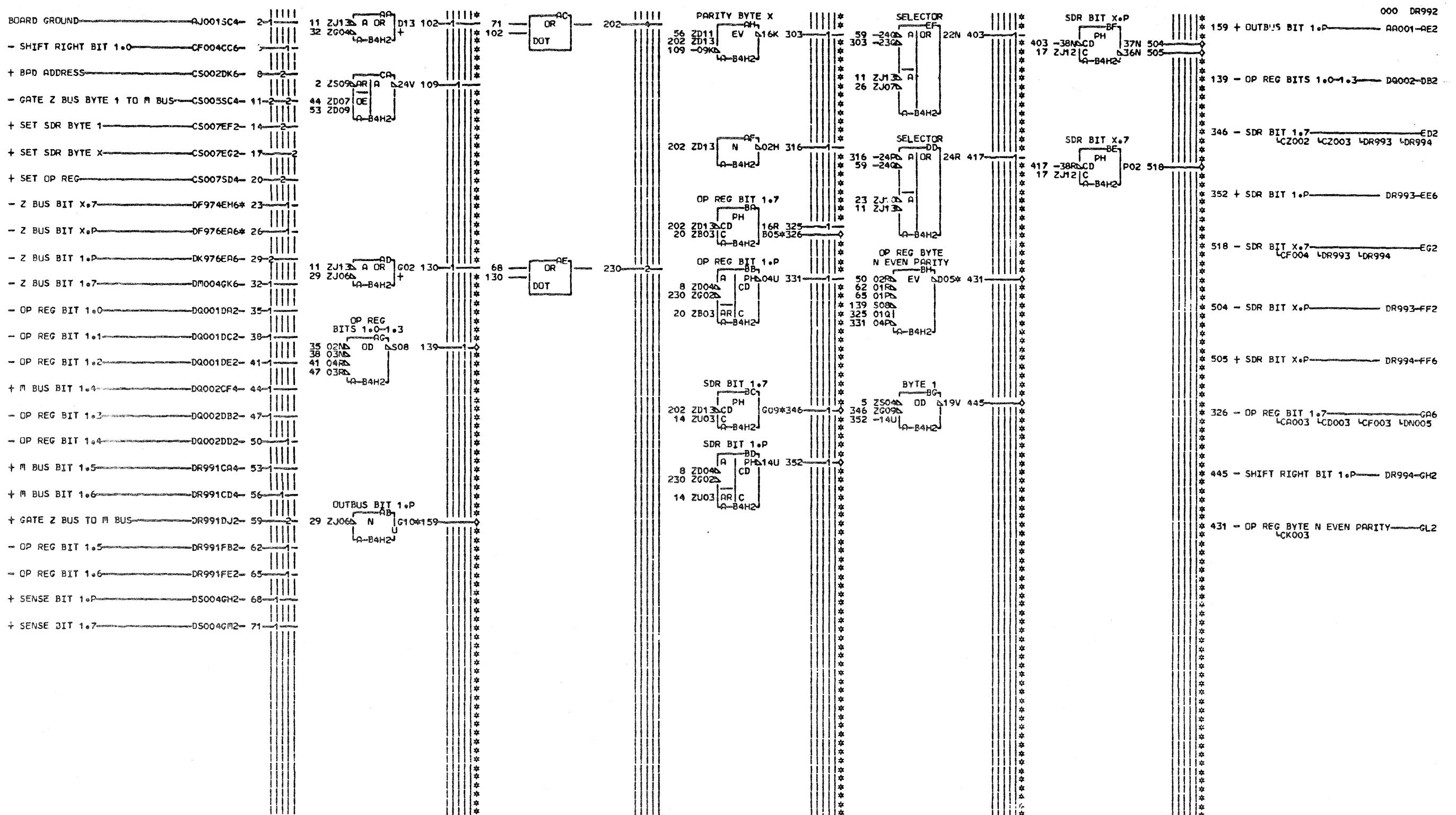


NOTE: SEE PAGE DR995  
FOR GATE OP REG  
TO INDICATOR GATES

EDGE CONN.  
42 RESISTOR  
A-B4H2G07  
326 A-B4C1C11  
01A-B3C6C02  
333 A-BAC1B13  
01A-B3C6B04

LOC<sub>a</sub> TYPE  
A-B4H2 AB90

CCU DATA FLOW	
SDR OP REG CARD	
E.C. HISTORY	E.PACH-3705
FRAME	01
DATE LAST EC	DR991
10-14-80 344270	PoNo 1859640 C00



NOTE. SEE PAGE DN005  
FOR GATE OP REG  
TO INDICATOR GATES

EDGE CONN.	431	A-B4N6C02
23 RESISTOR	01A-B3N1C11	
	A-B4H2J10	
26 RESISTOR	A-B4H2J07	
59 A-B4U4D02		
26 A-B4C1D13		
01A-B3C6D04		
146 A-B4N6E02		
01A-B3N1E11		

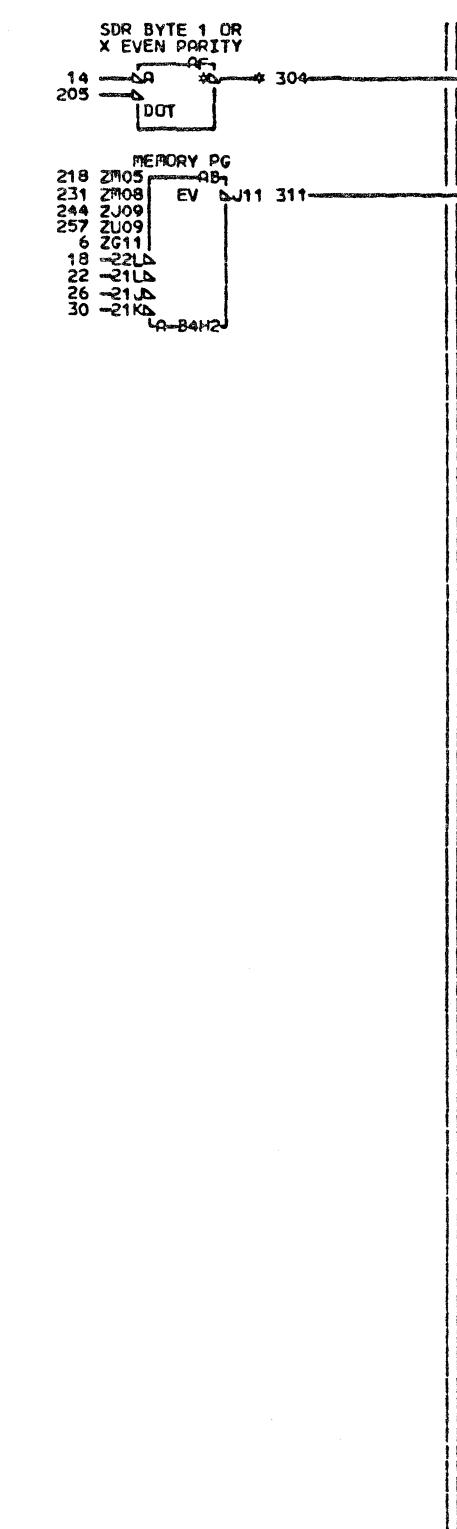
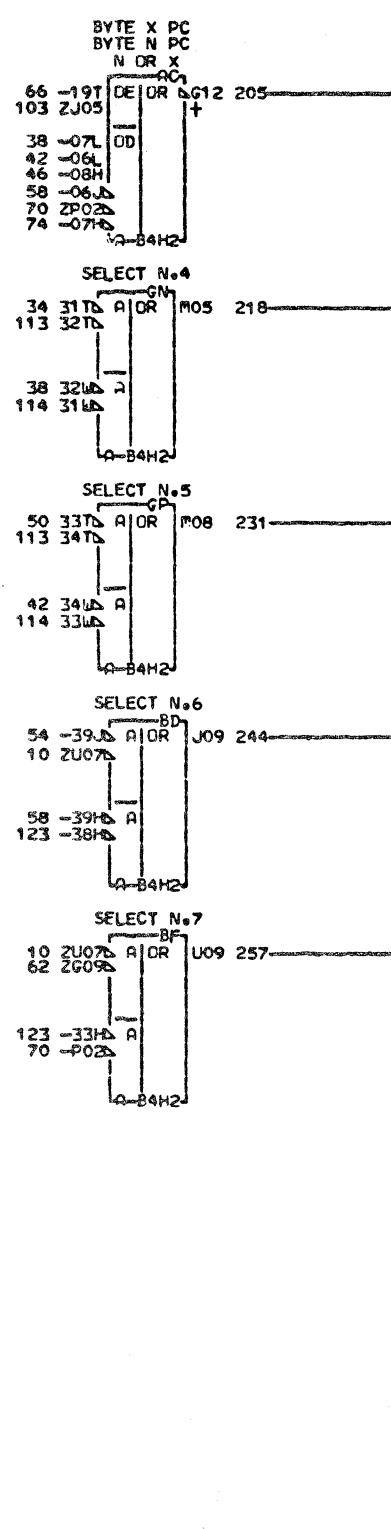
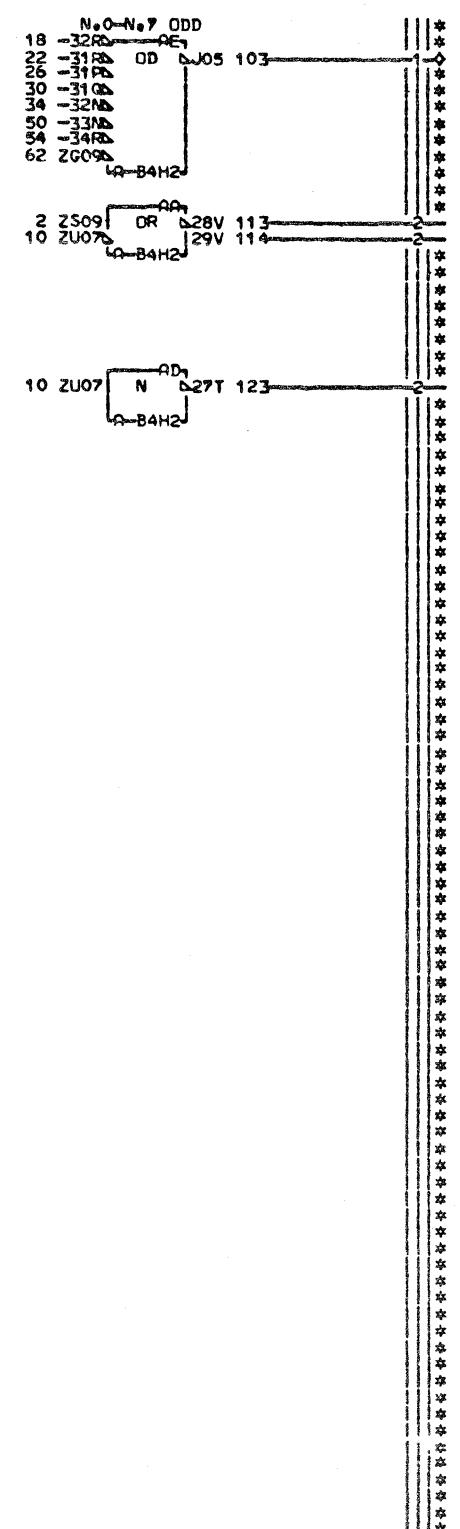
LCCo TYP  
A-84K2 QB9

BR992

300

CCU DATA FLOW	SDR DP REG CARD
E.C. HISTORY	EACH. 3705
	FRAME 01
DATE LAST EC	IBM CORP. SCD
10-14-80 344270	P.o.N. 1859641
	000

BOARD GROUND — AJ001SC4— 2  
 + FORCE STORAGE PARITY ERROR— CK001GD2— 6  
 + GATE SDR BYTE X TO MEM— CS004BM2— 10-22  
 + I1.B TIME INHIBIT SDR 1+X— CS005SJ5— 14  
 - SDR BIT 1.0— DQ001DH2— 18  
 - SDR BIT 1.1— DQ001DK2— 22  
 - SDR BIT 1.2— DQ001DM2— 26  
 - SDR BIT 1.3— DQ002DF2— 30  
 - SDR BIT 1.4— DQ002DH2— 34  
 ALWAYS PLUS— DQ002GE2— 38  
 ALWAYS PLUS— DQ002GE7— 42  
 + SDR CARD BYTE 1 TIE UP— DQ002GM4— 46  
 - SDR BIT 1.5— DR991FG2— 50  
 - SDR BIT 1.6— DR991FJ2— 54  
 - SDR BIT X.6— DR991FM2— 58  
 - SDR BIT 1.7— DR992ED2— 62  
 + SDR BIT 1.eP— DR992EE6— 66  
 - SDR BIT X.7— DR992EG2— 70  
 - SDR BIT X.eP— DR992FF2— 74



000 DR993  
 244 + MEM STORE BIT 1.6— DS004-BD2

257 + MEM STORE BIT 1.7— DS004-BF2

311 - MEM STORE BIT 1.eP— DS004-CM2

205 - SDR BYTE 1 OR X EVEN PARITY— FD2

218 + MEM STORE BIT 1.4— DS004-GN2

231 + MEM STORE BIT 1.5— DS004-GP2

103 + GENERATED SDR BIT ODD 1.eP— GV4  
 LCK003

304 - SDR BYTE 1 OR X EVEN PARITY— SA4  
 LCK003

EDGE CONN.  
 304 A-B4Q1D11  
 01A-B3Q6D02

LOC. TYPE  
 A-B4H2 AB90

CCU DATA FLOW	
SDR OP REG CARD	
E.C.—HISTORY	E.MACH-3705
FRAME	01
DATE LAST EC	DR993
10-14-80 344270	P.N. 1986952 000

+ BLOCK COMPLETE SDR—CF003AG6—2  
 - GATE SDR 1.6 TO A BUS—CF003DG6—7  
 - GATE SDR 1.7 TO A BUS—CF003DJ6—12  
 - GATE SDR X.6-X.7 TO A BUS—CF003EA6—17  
 - GATE SDR 1.1-1.5 TO A BUS—CF003EH6—22  
 - SHIFT RIGHT—CF004CF6—27-44  
 - SDR BIT 1.4—DQ002DH2—32  
 ALWAYS PLUS—DQ002GE2—37-3  
 ALWAYS PLUS—DQ002GE7—42-3  
 - SDR BIT 1.5—DR991FG2—47  
 - SDR BIT 1.6—DR991FJ2—52-1  
 - SDR BIT X.6—DR991FM2—57-2  
 + SDR BIT X.6—DR991FM6—62-1  
 - SDR BIT 1.7—DR992ED2—67-1  
 - SDR BIT X.7—DR992EG2—72-1  
 + SDR BIT X.8—DR992FF6—77-1  
 - SHIFT RIGHT BIT 1.8—DR992GH2—82

SDR BIT 1.5 TO A BUS  
 22 ZU04D A OR P07 104  
 47 -44D U4  
 4A-B4H2J  
 FLOAT  
 17 ZU05 [OR OR DU13 110  
 37 -54F U4  
 4A-B4H2J  
 FLOAT  
 17 ZU05 [OR OR DU11 117  
 42 -51C U4  
 4A-B4H2J  
 SDR BIT X.6 TO A BUS  
 17 ZU05S A OR G13 125  
 57 -27F U4  
 4A-B4H2J  
 SDR BIT X.7 TO A BUS  
 17 ZU05S A OR M03 132  
 72 ZP02D U4  
 4A-B4H2J  
 SDR BIT 1.7 TO A BUS  
 12 ZP02D A OR P04 139  
 67 ZG09S U4  
 4A-B4H2J  
 SDR BIT 1.6 TO A BUS  
 7 ZP06D A OR M04 146  
 52 -42L U4  
 4A-B4H2J  
 SDR BIT X.8 TO A BUS  
 2 ZS02D A OR S03 153  
 17 ZU05 [OR U4  
 77 -04H 4A-B4H2J  
 FLOAT  
 27 ZM02 [OR OR DU11 161  
 37 -53C U4  
 4A-B4H2J  
 SHIFT RIGHT BI T X.6 TO A BUS  
 27 ZM02D A OR G13 168  
 42 -53D U4  
 4A-B4H2J  
 SHIFT RIGHT BI T X.7 TO A BUS  
 27 ZM02D A OR M03 175  
 57 -27D U4  
 4A-B4H2J  
 SHIFTED X.8  
 27 ZM02D A R/A S03 181  
 37 -17H EV U4  
 42 -16H 4A-B4H2J  
 62 -17T

LDC TYPE  
 A-B4H2 AB90

N.O.P TO A BUS  
 27 ZM02D A OR U02 204  
 82 -52L U4  
 4A-B4H2J  
 SHIFT RIGHT BI T 1.7 TO A BUS  
 27 ZM02D A OR P04 211  
 52 -31D U4  
 4A-B4H2J  
 SHIFT RIGHT BI T 1.6 TO A BUS  
 27 ZM02D A OR M04 218  
 47 -32D U4  
 4A-B4H2J  
 SHIFT RIGHT BI T 1.5 TO A BUS  
 27 ZM02D A OR P07 225  
 32 -34D U4  
 4A-B4H2J

000 DR994  
 225 + SHIFT RIGHT BIT 1.5 TO A BUS—BB2  
 4D9005  
 104 + SDR BIT 1.5 TO A BUS—DM005-B2  
 218 + SHIFT RIGHT BIT 1.6 TO A BUS—BE2  
 4D9005  
 146 + SDR BIT 1.6 TO A BUS—DM005-BF2  
 211 + SHIFT RIGHT BIT 1.7 TO A BUS—BH2  
 4D9005  
 139 + SDR BIT 1.7 TO A BUS—DM005-BJ2  
 204 + BIT N.O.P TO A BUS—DK975-CL2  
 181 + SHIFT RIGHT BIT X.8 TO A BUS—EC2  
 4DF975  
 153 + SDR BIT X.8 TO A BUS—DF975-ED2  
 175 + SHIFT RIGHT BIT X.7 TO A BUS—EF2  
 4DF975  
 132 + SDR BIT X.7 TO A BUS—DF975-EG2  
 125 + SDR BIT X.6 TO A BUS—DF975-EK2  
 168 + SHIFT RIGHT BIT X.6 TO A BUS—GN2  
 4DF975  
 117 - FLOAT—DE975-GP2  
 161 - FLOAT—DE975-GQ2  
 110 - FLOAT—DE975-GR2

CCU DATA FLOW	SDR DP REG CARD	
E.C.—HISTORY	E.MACH.3705	
	FRAME 01	
DATE 10-14-80	LAST EC 344270	DR994
F.n. 1966953		000

GROUND LEVEL CM003DE2- 2  
 + SAR BIT X.6 DF971EG2- 6-1  
 + SAR BIT X.7 DF971EL2- 10-  
 + SAR BIT 0.0 DG971EG2- 14-  
 + SAR BIT 0.1 DG971EL2- 18-1  
 + SAR BIT 0.2 DH011EC2- 22-1  
 + SAR BIT 0.3 DH011EH2- 26-1  
 + SAR BIT 0.4 DH011EK2- 30-1  
 + SAR BIT 0.5 DJ011EC2- 34-1  
 + SAR BIT 0.6 DJ011EH2- 38-1  
 + SAR BIT 0.7 DJ011EK2- 42-1  
 + SAR BIT 1.0 DK971EC2- 46-1  
 + SAR BIT 1.0 DK971EG2- 50-11-  
 + SAR BIT 1.1 DK971EL2- 54-11-  
 + SAR BIT 1.2 DL001EC2- 58-1-  
 + SAR BIT 1.3 DL001EH2- 62-11-  
 + SAR BIT 1.4 DL001EK2- 66-11-  
 + SAR BIT 1.5 DM001EC2- 70-2-  
 + SAR BIT 1.6 DM001EH2- 74-2-  
 + SAR BIT 1.7 DM001EK2- 78-1-

18 ZD06 BG 03J 102  
 A-B4B2  
 22 ZB10 AB 06K 109  
 A-B4B2  
 26 ZD10 AH 09J 116  
 A-B4B2  
 30 ZD12 BH 09K 123  
 A-B4B2  
 34 ZB12 CA 11J 130  
 A-B4B2  
 38 ZB13 AD 12C 137  
 A-B4B2  
 6 ZB02 AG 01J 144  
 A-B4B2  
 42 ZD03 AA 04C 151  
 A-B4B2  
 50 ZB05 BF 03E 158  
 A-B4B2  
 54 ZD07 BC 04G 165  
 A-B4B2  
 58 ZD09 CF 05G 172  
 A-B4B2  
 62 ZB09 AC 06G 179  
 A-B4B2  
 66 ZB11 BD 09E 186  
 A-B4B2

EDGE CONN.  
 279 A-BAC1E11  
 01A-B3C6E02

102 05N FN W26 201  
 109 07N W28 203  
 116 08N W29 204  
 123 09N W30 205  
 130 11N W32 207  
 137 12N W33 208  
 SPEC CONN  
 A-B4B2

144 01M FD W02 243  
 151 02M W03 244  
 158 04M W05 246  
 165 05M W06 247  
 172 06M W07 248  
 179 08M W09 250  
 SPEC CONN  
 A-B4B2

70 ZD11 BE 09F 257  
 A-B4B2  
 74 ZD13 BA 11E 262  
 A-B4B2  
 10 ZB03 AE 01F 267  
 A-B4B2  
 1^ ZD04 BB 01K 272  
 A-B4B2

SAR BYTE  
 1 PARITY ERROR  
 46 ZD11 CB  
 50 ZB03 EV D07#279  
 54 ZB09  
 58 ZB11  
 62 ZD03  
 66 ZB10  
 70 ZD09  
 74 ZD06  
 78 ZD05  
 A-B4E2

LOC. TYPE  
 A-B4B2 6797  
 A-B4E2 AB89

186 09M FN W10 301  
 257 10M W11 302  
 262 12M W13 304  
 267 03N W24 307  
 272 04N W23 308  
 SPEC CONN  
 A-B4B2

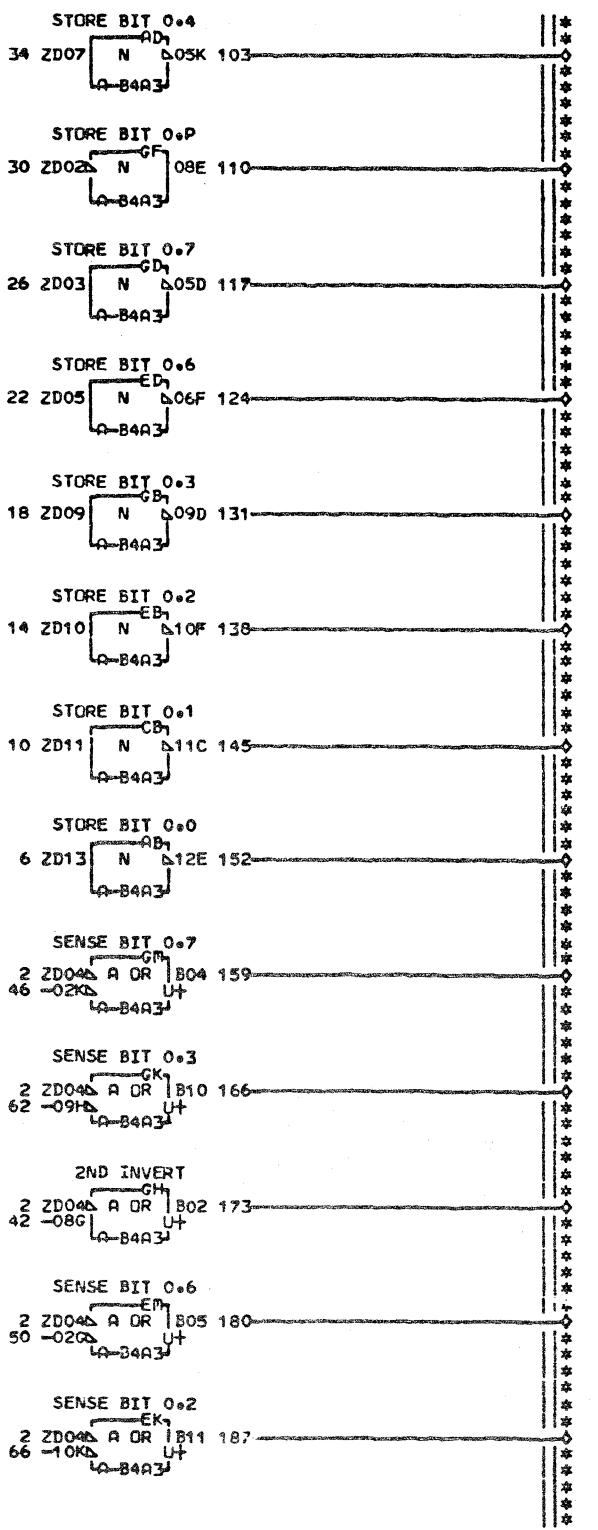
000 DS001  
 279 - SAR BYTE 1 PARITY ERROR CK003-EL2  
 243 + SAR BIT X.6 TO MEM AM001-FD1  
 244 + SAR BIT 0.7 TO MEM AM001-FD2  
 246 + SAR BIT 1.0 TO MEM AM001-FD4  
 247 + SAR BIT 1.1 TO MEM AM001-FD5  
 248 + SAR BIT 1.2 TO MEM AM001-FD6  
 250 + SAR BIT 1.3 TO MEM AM001-FD8  
 301 + SAR BIT 1.4 TO MEM AM001-FH1  
 302 + SAR BIT 1.5 TO MEM AM001-FH2  
 304 + SAR BIT 1.6 TO MEM AM001-FH4  
 305 GROUND LEVEL AM001-FH5  
 307 + SAR BIT X.7 TO MEM AM001-FH7  
 308 + SAR BIT 0.0 TO MEM AM001-FH8  
 201 + SAR BIT 0.1 TO MEM AM001-FH1  
 203 + SAR BIT 0.2 TO MEM AM001-FR3  
 204 + SAR BIT 0.3 TO MEM AM001-FR4  
 205 + SAR BIT 0.4 TO MEM AM001-FRS  
 207 + SAR BIT 0.5 TO MEM AM001-FR7  
 208 + SAR BIT 0.6 TO MEM AM001-FRS

DS001

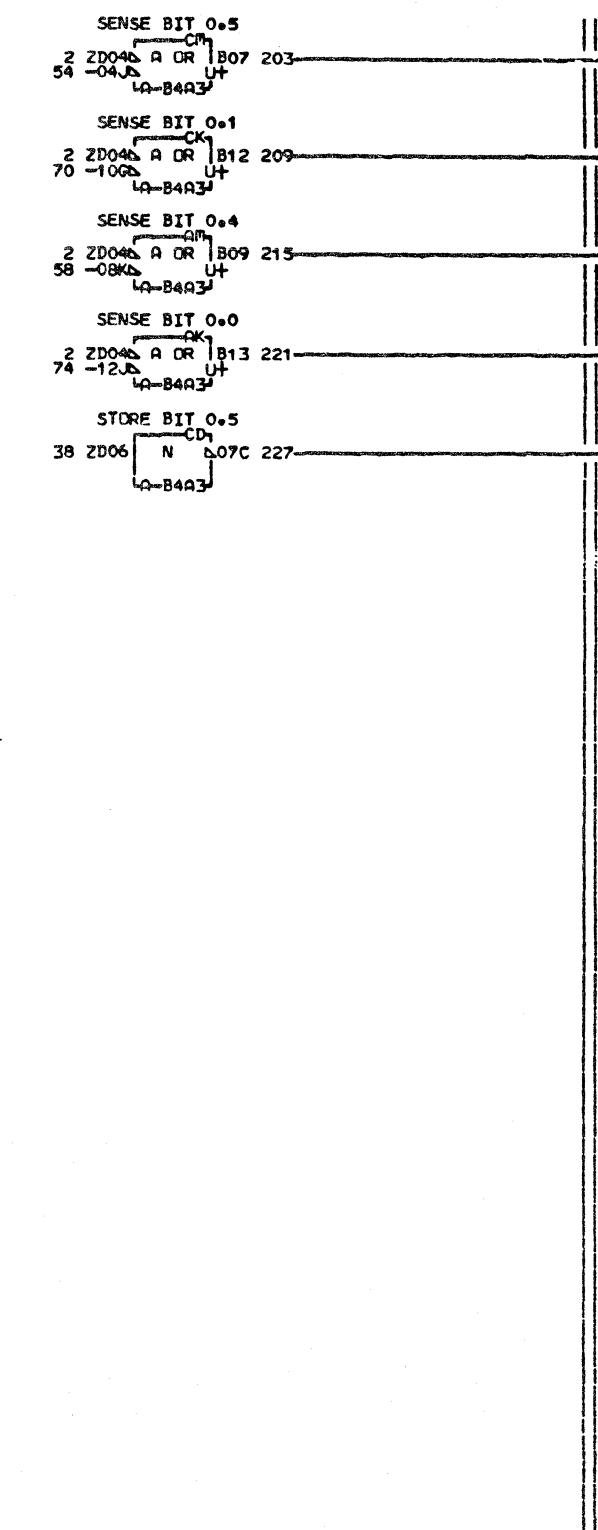
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SAR DRIVER TO MEM	
E.C.-HISTORY	E-MACH-3705
FRAME	01
DATE	LAST EC
10-14-80	340270
IBM CORP. SCD	DS001
PoNo.	1986954
000	

- GATE MEM BYTE 0 TO R BUS CS005SB6- 2-54  
 + MEM STORE BIT 0.0 DN001DK6- 6-  
 + MEM STORE BIT 0.1 DN001DK6- 10-  
 + MEM STORE BIT 0.2 DN001DK6- 14-  
 + MEM STORE BIT 0.3 DN002DF6- 18-  
 + MEM STORE BIT 0.6 DP993Bn2- 22-  
 + MEM STORE BIT 0.7 DP993BF2- 26-  
 - MEM STORE BIT 0.P DP993CM2- 30-  
 + MEM STORE BIT 0.4 DP993GN2- 34-  
 + MEM STORE BIT 0.5 DP993GP2- 38-  
 + SENSE BIT 0.P DS003BG5- 42-  
 - SENSE BIT 0.7 DS003BG7- 46-  
 - SENSE BIT 0.6 DS003BG8- 50-  
 - SENSE BIT 0.5 DS003BL1- 54-  
 - SENSE BIT 0.4 DS003BL3- 58-  
 - SENSE BIT 0.3 DS003BL4- 62-  
 - SENSE BIT 0.2 DS003BL5- 66-  
 - SENSE BIT 0.1 DS003BL7- 70-  
 - SENSE BIT 0.0 DS003BL8- 74-



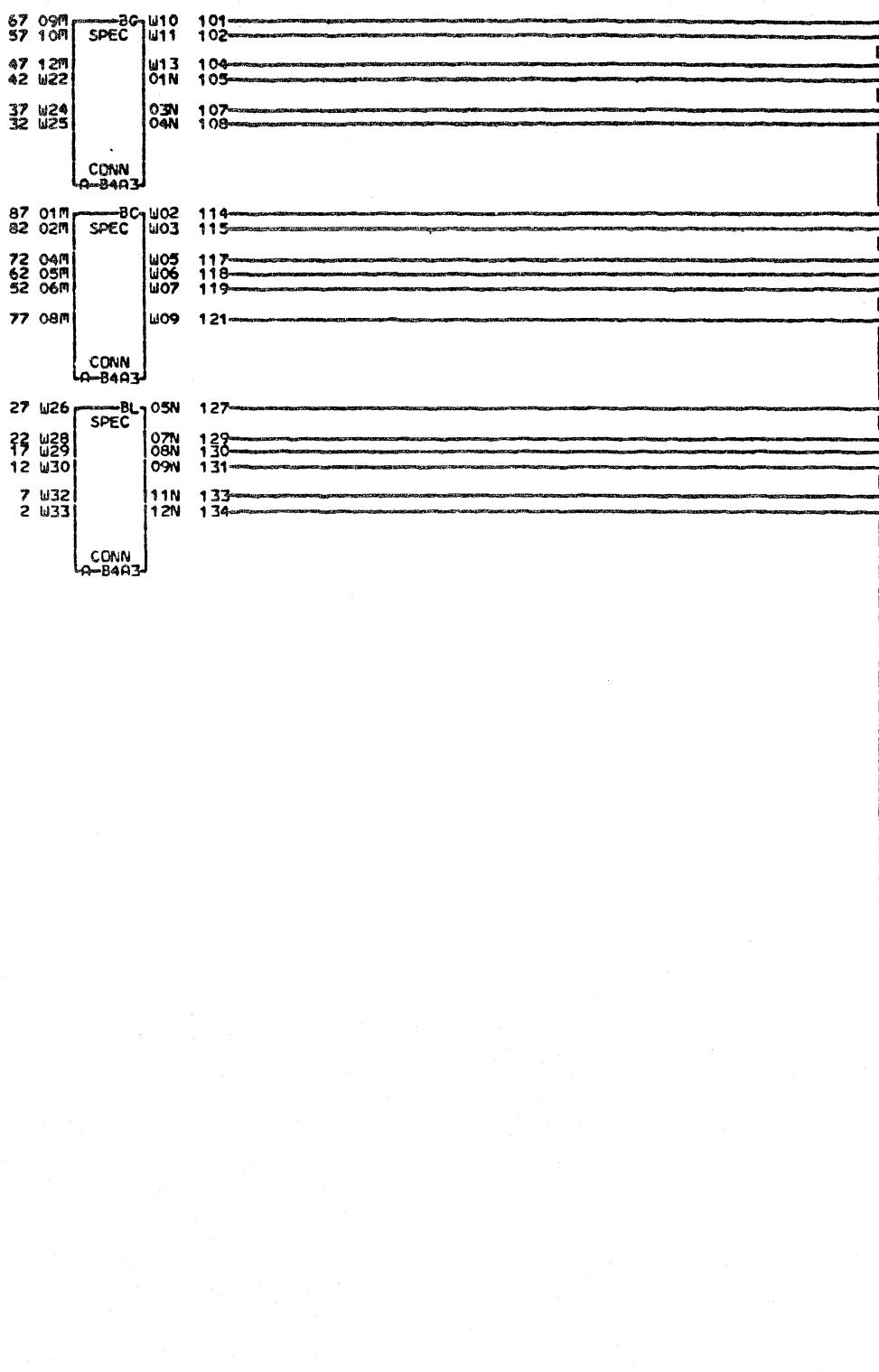
LOC. TYPE  
A-B4A3 6799



000 DS002  
 152 - STORE BIT 0.0 DS003-AB2  
 103 - STORE BIT 0.4 DS003-AD2  
 221 + SENSE BIT 0.0 DN001-AK2  
 215 + SENSE BIT 0.4 DN002-AM2  
 145 - STORE BIT 0.1 DS003-CB2  
 227 - STORE BIT 0.5 DS003-CD2  
 209 + SENSE BIT 0.1 DN001-CK2  
 203 + SENSE BIT 0.5 DP991-CM2  
 138 - STORE BIT 0.2 DS003-EB2  
 124 - STORE BIT 0.6 DS003-ED2  
 187 + SENSE BIT 0.2 DN001-EK2  
 180 + SENSE BIT 0.6 DP991-EM2  
 131 - STORE BIT 0.3 DS003-GB2  
 117 - STORE BIT 0.7 DS003-GD2  
 110 + STORE BIT 0.P DS003-GF2  
 173 + SENSE BIT 0.P DP992-GH2  
 166 + SENSE BIT 0.3 DN002-GK2  
 159 + SENSE BIT 0.7 DP992-GM2

STORAGE DATA	DRIVERS AND RECEIVERS BYTE 0
E.C.-HISTORY	E.MACH.3705
FRAME	01
DATE LAST EC	DS002
10-14-60 344270	IBM CORP-SCD
	PoNo. 1986955
	000

- SENSE BIT 0.0 AM002DD1= 2  
 - SENSE BIT 0.1 AM002DD3= 7  
 - SENSE BIT 0.2 AM002DD5= 12  
 - SENSE BIT 0.3 AM002DD7= 17  
 - SENSE BIT 0.4 AM002DE2= 22  
 - SENSE BIT 0.5 AM002DE4= 27  
 - SENSE BIT 0.6 AM002DE6= 32  
 - SENSE BIT 0.7 AM002DF1= 37  
 + SENSE BIT 0.P AM002DF3= 42  
 - STORE BIT 0.0 DS002AB2= 47  
 - STORE BIT 0.4 DS002AD2= 52  
 - STORE BIT 0.6 DS002CB2= 57  
 - STORE BIT 0.5 DS002CD2= 62  
 - STORE BIT 0.2 DS002EB2= 67  
 - STORE BIT 0.6 DS002ED2= 72  
 - STORE BIT 0.3 DS002GB2= 77  
 - STORE BIT 0.7 DS002GD2= 82  
 + STORE BIT 0.P DS002GF2= 87



000 DS003  
 114 + STORE BIT 0.P AM002-BG1  
 115 - STORE BIT 0.7 AM002-BG2  
 117 - STORE BIT 0.6 AM002-BG4  
 118 - STORE BIT 0.5 AM002-BG5  
 119 - STORE BIT 0.4 AM002-BG6  
 121 - STORE BIT 0.3 AM002-BG8  
 101 - STORE BIT 0.2 AM002-BG1  
 102 - STORE BIT 0.1 AM002-BG2  
 104 - STORE BIT 0.0 AM002-BG4  
 105 + SENSE BIT 0.P DS002-BG5  
 107 - SENSE BIT 0.7 DS002-BG7  
 108 - SENSE BIT 0.6 DS002-BG8  
 127 - SENSE BIT 0.5 DS002-BL1  
 129 - SENSE BIT 0.4 DS002-BL3  
 130 - SENSE BIT 0.3 DS002-BL4  
 131 - SENSE BIT 0.2 DS002-BL5  
 133 - SENSE BIT 0.1 DS002-BL7  
 134 - SENSE BIT 0.0 DS002-BL8

LOC. TYPE  
A-B4A3 5799

DS003  
000

TOP CARD CONN STORAGE	
DATA TO FROM MEM BYTE 0	
E.C.-HISTORY E.MACH-3705	
FRAME	01
DATE	LAST EC
10-14-80	344270
IBM CORP.SCD	DS003
P.N. 1986956	000

- GATE MEM BYTE 1 TO M BUS CS005SD6- 2-54  
 + MEM STORE BIT 1+0 DQ001DH6- 6-  
 + MEM STORE BIT 1+1 DQ001DK6- 10-  
 + MEM STORE BIT 1+2 DQ001DM6- 14-  
 + MEM STORE BIT 1+3 DQ002DF6- 18-  
 + MEM STORE BIT 1+6 DR993BD2- 22-  
 + MEM STORE BIT 1+7 DR993BF2- 26-  
 - MEM STORE BIT 1+P DR993CM2- 30-  
 + MEM STORE BIT 1+8 DR993CN2- 34-  
 + MEM STORE BIT 1+5 DR993GP2- 38-  
 + SENSE BIT 1+P DS005BG5- 42-  
 - SENSE BIT 1+7 DS005BG7- 46-  
 - SENSE BIT 1+6 DS005BG8- 50-  
 - SENSE BIT 1+5 DS005BL1- 54-  
 - SENSE BIT 1+4 DS005BL3- 58-  
 - SENSE BIT 1+3 DS005BL4- 62-  
 - SENSE BIT 1+2 DS005BLS- 66-  
 - SENSE BIT 1+1 DS005BL7- 70-  
 - SENSE BIT 1+0 DS005BL8- 74-

STORE BIT 1+4 AD1  
 34 ZD07 N 005K 103  
 A-B4A2  
 STORE BIT 1+P GF  
 30 ZD02 N 08E 110  
 A-B4A2  
 STORE BIT 1+7 GD1  
 26 ZD03 N 005D 117  
 A-B4A2  
 STORE BIT 1+6 ED1  
 22 ZD05 N 006F 124  
 A-B4A2  
 STORE BIT 1+3 GB1  
 18 ZD09 N 009D 131  
 A-B4A2  
 STORE BIT 1+2 EB1  
 14 ZD10 N 010F 138  
 A-B4A2  
 STORE BIT 1+1 CB1  
 10 ZD11 N 011C 145  
 A-B4A2  
 STORE BIT 1+0 AB1  
 6 ZD13 N 012E 152  
 A-B4A2  
 SENSE BIT 1+7 GM1  
 2 ZD04 A DR 004 159  
 46 -02KA U+  
 A-B4A2  
 SENSE BIT 1+3 CK1  
 2 ZD04 A DR 010 166  
 62 -09KA U+  
 A-B4A2  
 2ND INVERT GH1  
 2 ZD04 A DR 002 173  
 42 -08G U+  
 A-B4A2  
 SENSE BIT 1+6 EM1  
 2 ZD04 A DR 005 180  
 50 -02G U+  
 A-B4A2  
 SENSE BIT 1+2 EK1  
 2 ZD04 A DR 011 187  
 66 -10G U+  
 A-B4A2

LOC. TYPE  
R-B4A2 6799

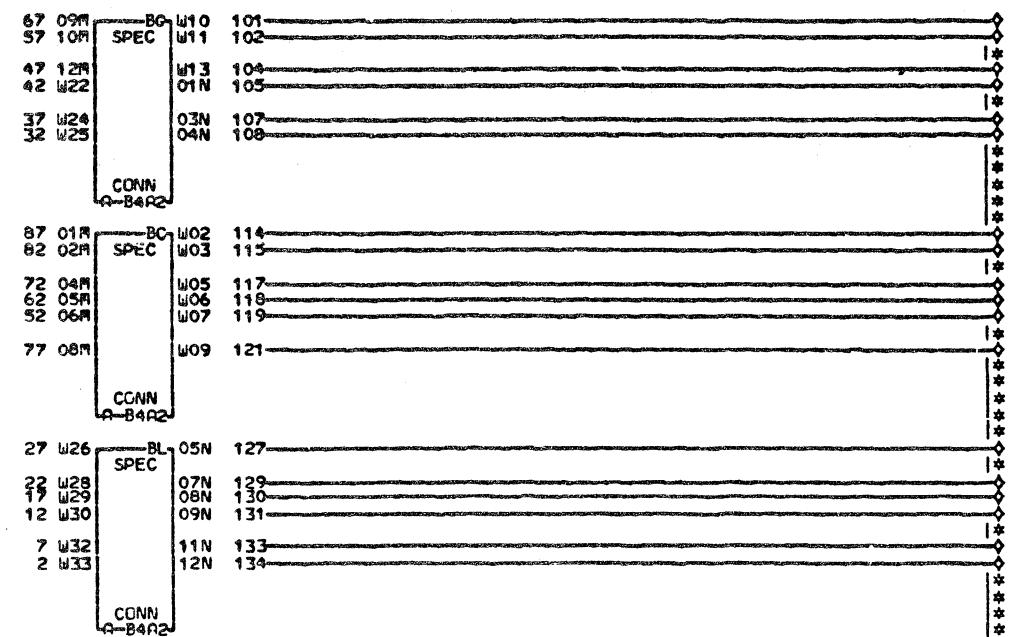
SENSE BIT 1+5 CP1  
 2 ZD04 A DR 007 203  
 54 -04J U+  
 A-B4A2  
 SENSE BIT 1+1 CK1  
 2 ZD04 A DR 012 209  
 70 -10G U+  
 A-B4A2  
 SENSE BIT 1+4 AM1  
 2 ZD04 A DR 009 215  
 58 -08KA U+  
 A-B4A2  
 SENSE BIT 1+0 AK1  
 2 ZD04 A DR 013 221  
 71 -12G U+  
 A-B4A2  
 STORE BIT 1+5 CD1  
 38 ZD06 N 007C 227  
 A-B4A2

000 DS004  
 152 - STORE BIT 1+0 DS005-AB2  
 103 - STORE BIT 1+4 DS005-AD2  
 221 + SENSE BIT 1+0 DQ001-AK2  
 215 + SENSE BIT 1+4 DQ002-AM2  
 145 - STORE BIT 1+1 DS005-CB2  
 227 - STORE BIT 1+5 DS005-CD2  
 209 + SENSE BIT 1+1 DQ001-CK2  
 203 + SENSE BIT 1+5 DR991-CM2  
 138 - STORE BIT 1+2 DS005-EB2  
 124 - STORE BIT 1+6 DS005-ED2  
 187 + SENSE BIT 1+2 DQ001-EK2  
 180 + SENSE BIT 1+6 DR991-EM2  
 131 - STORE BIT 1+3 DS005-GB2  
 117 - STORE BIT 1+7 DS005-GD2  
 110 + STORE BIT 1+P DS005-GF2  
 173 + SENSE BIT 1+P DR992-GH2  
 166 + SENSE BIT 1+3 DQ002-GK2  
 159 + SENSE BIT 1+7 DR992-GR2

STORAGE DATA	DRIVERS AND RECEIVERS BYTE 1
E.C.-HISTORY	E.MACH.3705
FRAME	01
DATE LAST EC	IBR CORP. SCD DS004
10-14-80 344270	P.N. 1986957 000

DS004  
000

- SENSE BIT 1.0 AM002DL1- 2-  
 - SENSE BIT 1.1 AM002DL3- 7-  
 - SENSE BIT 1.2 AM002DL5- 12-  
 - SENSE BIT 1.3 AM002DL7- 17-  
 - SENSE BIT 1.4 AM002DM2- 22-  
 - SENSE BIT 1.5 AM002DM4- 27-  
 - SENSE BIT 1.6 AM002DM6- 32-  
 - SENSE BIT 1.7 AM002DN1- 37-  
 + SENSE BIT 1.P AM002DN3- 42-  
 - STORE BIT 1.0 DS004AB2- 47-  
 - STORE BIT 1.4 DS004AD2- 52-  
 - STORE BIT 1.1 DS004CB2- 57-  
 - STORE BIT 1.5 DS004CD2- 62-  
 - STORE BIT 1.2 DS004EB2- 67-  
 - STORE BIT 1.6 DS004ED2- 72-  
 - STORE BIT 1.3 DS004GB2- 77-  
 - STORE BIT 1.7 DS004GD2- 82-  
 + STORE BIT 1.P DS004GF2- 87-



000 DS005  
 114 + STORE BIT 1.P AM002-BG1  
 115 - STORE BIT 1.7 AM002-BG2  
 117 - STORE BIT 1.6 AM002-BG4  
 118 - STORE BIT 1.5 AM002-BG5  
 119 - STORE BIT 1.4 AM002-BG6  
 121 - STORE BIT 1.3 AM002-BG8  
 101 - STORE BIT 1.2 AM002-BG1  
 102 - STORE BIT 1.1 AM002-BG2  
 104 - STORE BIT 1.0 AM002-BG4  
 105 + SENSE BIT 1.P DS004-BG5  
 107 - SENSE BIT 1.7 DS004-BG7  
 108 - SENSE BIT 1.6 DS004-BG8  
 127 - SENSE BIT 1.5 DS004-BL1  
 129 - SENSE BIT 1.4 DS004-BL3  
 130 - SENSE BIT 1.3 DS004-BL4  
 131 - SENSE BIT 1.2 DS004-BL5  
 133 - SENSE BIT 1.1 DS004-BL7  
 134 - SENSE BIT 1.0 DS004-BL8

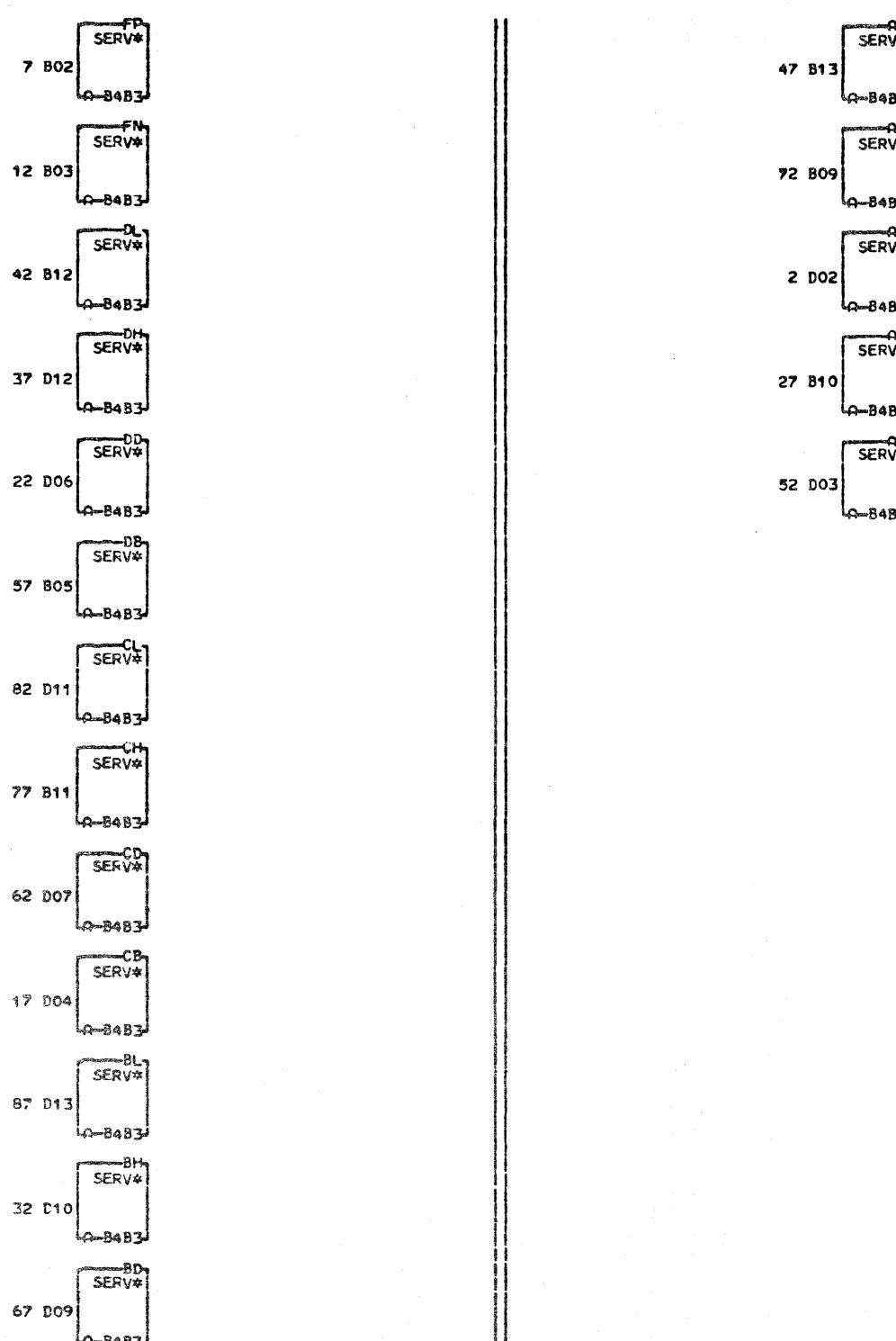
LOC. TYPE  
A-B4A2 6799

DS005  
000

TOP CARD CONN STORAGE		FRAME 01
DATA TO/FROM REM BYTE 1		
E-C HISTORY		E-RACH 3705
DATE 10-14-80	LAST EC 344270	IBR CORP/SCD DS005
		P.N. 1986958 000

000 DT001

GROUND LEVEL CM003DD2- 2  
+ SAR BIT X.6 DF971EG2- 7  
+ SAR BIT X.7 DF971EL2- 12  
+ SAR BIT 0.0 DG971EG2- 17-1  
+ SAR BIT 0.1 DG971EL2- 22-1  
+ SAR BIT 0.2 DH011EC2- 27-1  
+ SAR BIT 0.3 DH011EH2- 32-1  
+ SAR BIT 0.4 DH011EK2- 37-1  
+ SAR BIT 0.5 DJ011EC2- 42-1  
+ SAR BIT 0.6 DJ011EH2- 47-1  
+ SAR BIT 0.7 DJ011EK2- 52-1  
+ SAR BIT 1.0 DK971EG2- 57-1  
+ SAR BIT 1.1 DK971EL2- 62-1  
+ SAR BIT 1.2 DL001EC2- 67-1  
+ SAR BIT 1.3 DL001EH2- 72-1  
+ SAR BIT 1.4 DL001EK2- 77-1  
+ SAR BIT 1.5 DM001EC2- 82-1  
+ SAR BIT 1.6 DM001EH2- 87-1



LCC TYPE

DT001  
000

SERV WIRING	E-C-HISTORY	E MACH 3705
FRAME	01	IBM CORP SCD DT001
DATE	LAST EC	PoNo 1986959 000
10-14-80 344270		

GROUND LEVEL— CM003DC2— 2  
 + SAR BIT X.6— DF971EG2— 7  
 + SAR BIT X.7— DF971EL2— 12  
 + SAR BIT 0.0— DG971EG2— 17  
 + SAR BIT C.1— DG971EL2— 22  
 + SAR BIT 0.2— DH011EC2— 27  
 + SAR BIT 0.3— DH011EH2— 32  
 + SAR BIT 0.4— DH011EK2— 37  
 + SAR BIT 0.5— DJ011EC2— 42  
 + SAR BIT 0.6— DJ011EH2— 47  
 + SAR BIT 0.7— DJ011EK2— 52  
 + SAR BIT 1.0— DK971EG2— 57  
 + SAR BIT 1.1— DK971EL2— 62  
 + SAR BIT 1.2— DL001EC2— 67  
 + SAR BIT 1.3— DL001EH2— 72  
 + SAR BIT 1.4— DL001EK2— 77  
 + SAR BIT 1.5— DM001EC2— 82  
 + SAR BIT 1.6— DM001EH2— 87

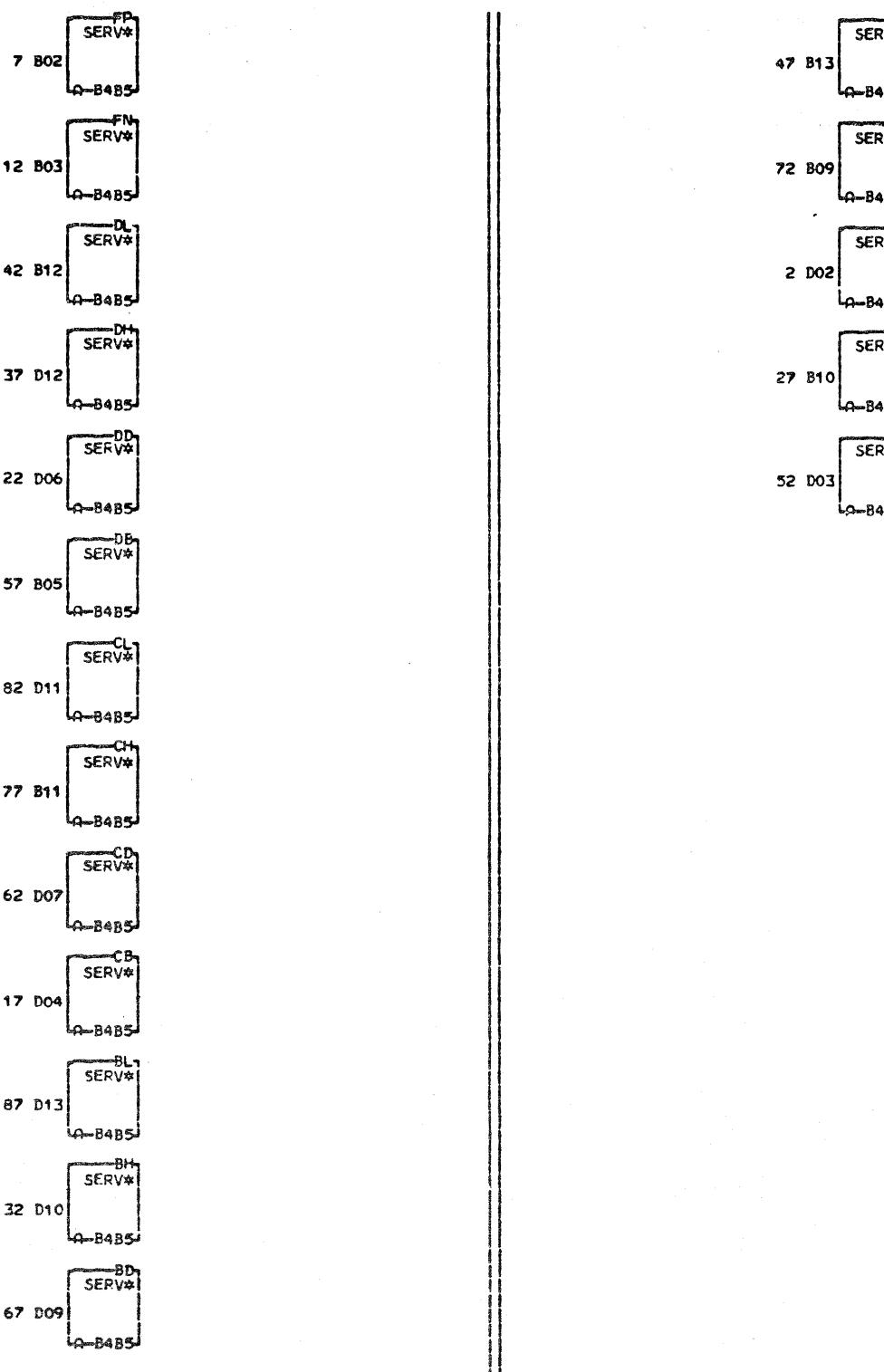
LOC.	TYPE
7 B02	FP SERV*
12 B03	FN SERV*
42 B12	FL SERV*
37 D12	DH SERV*
22 D06	DD SERV*
57 B05	DB SERV*
82 D11	CL SERV*
77 B11	CH SERV*
62 D07	CD SERV*
17 D04	CB SERV*
87 D13	BL SERV*
32 D10	BH SERV*
67 D09	BD SERV*
47 B13	AL SERV*
72 B09	AH SERV*
2 D02	AC SERV*
27 B10	AD SERV*
52 D03	AB SERV*

DU001  
000

000 DU001

SERV WIRING	EACH 3705
E.C.—HISTORY	FRAME 01
	IBM CORP. SCD DU001
DATE 10-14-80	LST EC 344270
P.N. 1986960	000

GROUND LEVEL CM003DB2- 2-  
 + SAR BIT X.6 DF971EG2- 7-  
 + SAR BIT X.7 DF971EL2- 12-  
 + SAR BIT 0.0 DG971EG2- 17-  
 + SAR BIT 0.1 DG971EL2- 22-  
 + SAR BIT 0.2 DH011EC2- 27-  
 + SAR BIT 0.3 DH011EH2- 32-  
 + SAR BIT 0.4 DH011EK2- 37-  
 + SAR BIT 0.5 DJ011EC2- 42-  
 + SAR BIT 0.6 DJ011EH2- 47-  
 + SAR BIT 0.7 DJ011EK2- 52-  
 + SAR BIT 1.0 DK971EG2- 57-  
 + SAR BIT 1.1 DK971EL2- 62-  
 + SAR BIT 1.2 DL001EC2- 67-  
 + SAR BIT 1.3 DL001EH2- 72-  
 + SAR BIT 1.4 DL001EK2- 77-  
 + SAR BIT 1.5 DM001EC2- 82-  
 + SAR BIT 1.6 DF001EH2- 87-



000 DV001

SERV WIRING	E-ECo-HISTORY	E-FACH-3705
FRAME	01	
DATE	LAST EC	DV001
10-14-80	344270	IBM CORP-SCD
		P.N. 1986961
		000

DVC01  
000

+ LAMP TEST DISP A-B BYTES 0-1—AP008AF6— 2

- SELECT LS REG GROUP 1+2—CC006AU4— 9

- SELECT LS REG GROUP 1+3—CC006AV4— 16

- SELECT LS REG BIT 0+1+2+3—CC006AW4— 23

- WRITE LS—CC006BJ4— 30

- SELECT LS REG 0+1+4+5—CC006BK4— 37

- SELECT LS REG BIT 0+2+4+6—CC006BL4— 44

- T0+T1 TIME SET A-B REGS—CC007HK4— 51

+ FORCE Z BUS PARITY ERROR—CK001GF2— 58

- GATE INPUT 74—CQ004FJ6— 65

+ SET LAR—CS001DM2— 72

- GATE CCU INDATA TO Y BUS—CS004DB2— 79

109 B03 OF SERV\* 109  
110 D04 GND 110  
111 B07 GND 111  
112 B11 A-B4A5 C07 112  
113 D12 A-B4A5 C12 113

FF SERV\* C03 109  
GND C04 110  
C07 111  
C11 112  
C12 113

44 D10 FB R A-B4F5  
65 B05 ED R A-B4F5  
37 D09 EB R A-B4F5  
169 B04 DP SERV\* GND A-B4B5  
173 D05 A-B4B5  
176 B04 DL SERV\* GND A-B4B4  
180 D05 A-B4B4  
183 B04 DK SERV\* GND A-B4B3  
187 D05 A-B4B3  
202 B04 DJ SERV\* GND A-B4B2  
206 D05 A-B4B2  
79 B04 DD R A-B4F5  
30 D07 DB R A-B4F5

OF SERV\* C03 169  
GND C04 170  
C07 173

CL SERV\* C03 176  
GND C04 177  
A-B4B4 D08 180

CK SERV\* C03 183  
GND C04 187

—BLANK COLUMN—

58 B02 CD R A-B4F5  
23 D06 CB R A-B4F5  
437 B04 BL SERV\* GND A-B4A4  
250 B03 BK SERV\* GND A-B4A3  
254 D12 A-B4A3  
257 B03 BJ SERV\* GND A-B4A2  
261 D12 A-B4A2  
51 D13 BD R A-B4F5  
16 D05 BB R A-B4F5  
OK SERV\* C03 250  
GND C04 251  
A-B4A3 C12 254

AJ SERV\* C03 257  
GND C04 258  
A-B4A2 C12 261

2 B09 OF R A-B4F5  
72 D11 AD R A-B4F5  
9 D03 AB R A-B4F5

GL SERV\* D08 402  
GND A-B4U3  
GJ SERV\* D08 409  
GND A-B4U2  
FE R B12 416  
A-B4F5

R FD B08 423  
A-B4F5

TIE DOWN ROS ESCAPE  
BF R B10 432  
A-B4F5

OL SERV\* C03 437  
GND A-B4A4 D08 441

000 DW001

441 GND ————— DB101-AL6

432 - TIE DOWN ROS ESCAPE— AJ001-BF4

423 ALWAYS MINUS———— AJ001-FD4

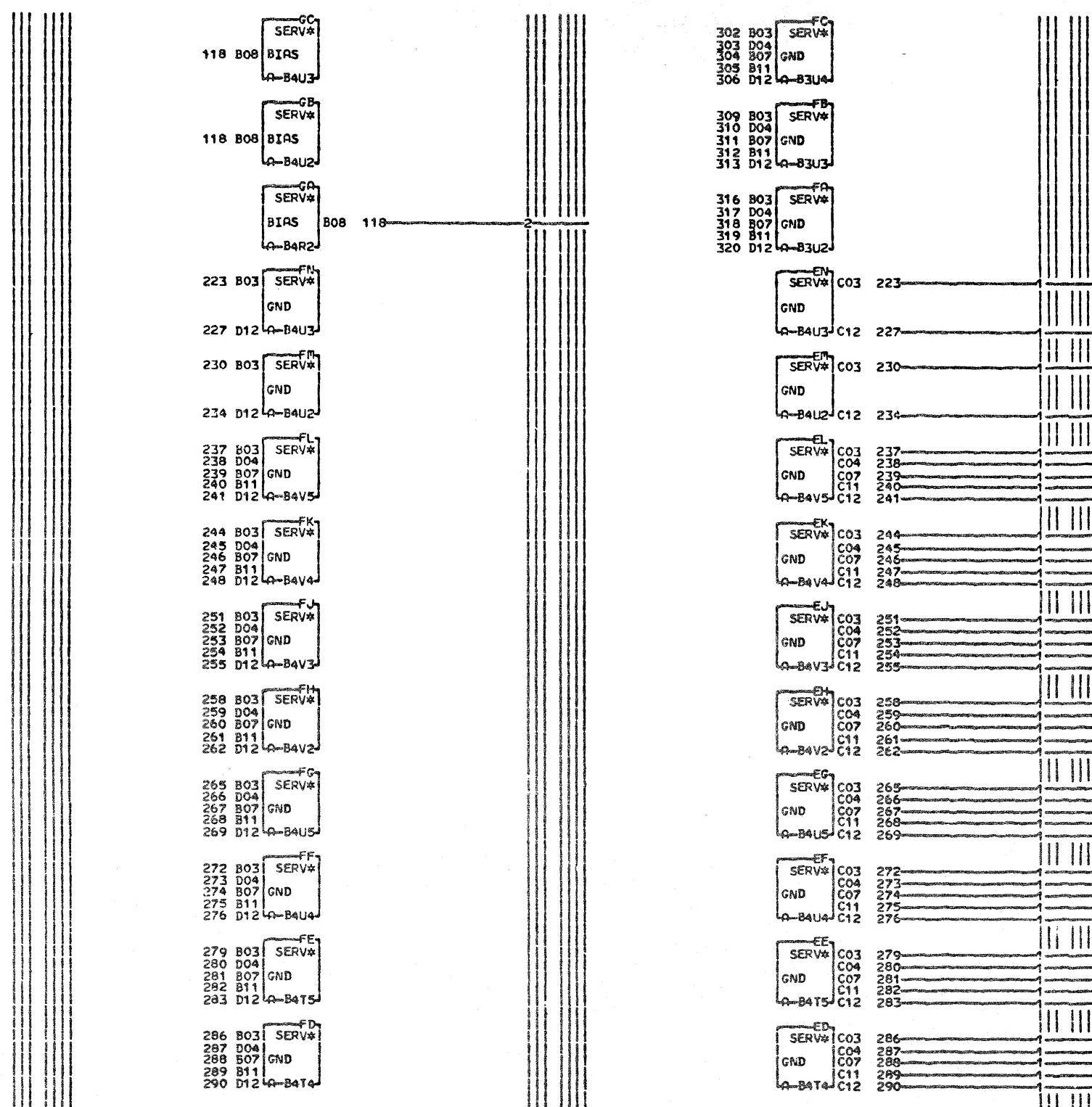
416 - TIE DOWN———— FE4

409 INDICATOR GND 1— LAP012 4AP013 4AP014 4AP015 GJ2

402 INDICATOR GND 2— LAP009 4AP010 4AP011 GL2

LDC TYPE  
A-B4F5 N885

TERMINATOR RESISTORS FOR CONTROL SIGNALS	E.C.—HISTORY	E.MACH.3705
344270		
FRAME 01		
DATE 06-02-81	LAST EC 344828	IBM CORP-JCD DW001
P.N. 1986962		000



LOC. TYPE

	000	DY001
EC SERV*	C03 302	
	C04 303	
GND	C07 304	
	C11 305	
A-B3U4	C12 306	
EB SERV*	C03 309	
	C04 310	
GND	C07 311	
	C11 312	
A-B3U3	C12 313	
EA SERV*	C03 316	
	C04 317	
GND	C07 318	
	C11 319	
A-B3U2	C12 320	
DN SERV*	425 B07 -4V	
	A-B4F5	
DM SERV*	432 B03 GND	
	A-B3T3	
DL SERV*	439 B03 GND	
	A-B3T2	
DK SERV*	446 B06 -4V	
	A-B4U3	
DJ SERV*	453 B06 -4V	
	A-B4U2	
DH SERV*	460 B06 -4V	
	A-B4C5	
DG SERV*	467 B06 -4V	
	A-B4C4	
DF SERV*	474 B06 -4V	
	A-B4C3	
DE SERV*	481 B06 -4V	
	A-B4C2	
DD SERV*	488 B06 -4V	
	A-B4B5	

PAGE  
01  
OF  
C2DY001  
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BIAS AND SERVICE VOLTAGES FOR A-B3 AND A-B4 BOARDS —E-Co-HISTORY— E-PACB-3705	PAGE 01 OF 02
FRAME 01	
DATE LAST EC 10-14-80 344270	IBM CORP-SCD DY001
P.N. 1985963 000	

PAGE 2 OF 2

504 B06	-4V	DC SERV*	A-B4B4
511 B06	-4V	DB SERV*	A-B4B3
518 B06	-4V	DA SERV*	A-B4B2
		CN SERV*	
		GND	B06 425
		CL SERV*	
		GND	C03 432
		CK SERV*	
		C03 439	A-B3T2
		CJ SERV*	
		C06 446	A-B4U3
		CH SERV*	
		C06 453	A-B4U2
		CH SERV*	
		C06 460	A-B4C5
		CC SERV*	
		C06 467	A-B4C4
		CF SERV*	
		C06 474	A-B4C3
		CE SERV*	
		C06 481	A-B4C2
		CD SERV*	
		C06 488	A-B4B5

504 B06	-4V	CC SERV*	A-B4B4
511 B06	-4V	CB SERV*	A-B4B3
518 B06	-4V	CC SERV*	A-B4B2
		BN SERV*	
		GND	B06 425
		BN SERV*	
		C03 432	A-B3C4
		BL SERV*	
		C03 439	A-B4A4
		BK SERV*	
		C06 446	A-B4A3
		BJ SERV*	
		C06 453	A-B4A2
		BN SERV*	
		C06 460	A-B3U5
		BC SERV*	
		C06 467	A-B3U4
		BF SERV*	
		C06 474	A-B3U3
		BE SERV*	
		C06 481	A-B3U2
		BD SERV*	
		C06 488	A-B3T5

704 B06	-4V	BC SERV*	A-B3T4
711 B06	-4V	BB SERV*	A-B3T3
718 B06	-4V	BA SERV*	A-B3T2
		CN SERV*	
		GND	B06 625
		CN SERV*	
		C06 632	A-B3D5
		CM SERV*	
		C06 632	A-B3D4
		AL SERV*	
		C06 639	A-B4A4
		AK SERV*	
		C06 646	A-B4A3
		AL SERV*	
		C06 653	A-B4A2
		AH SERV*	
		C06 660	A-B3U5
		AC SERV*	
		C06 667	A-B3U4
		AF SERV*	
		C06 674	A-B3U3
		AE SERV*	
		C06 681	A-B3U2
		AD SERV*	
		C06 688	A-B3T5

704 B06	-4V	AC SERV*	A-B3T4
711 B06	-4V	AB SERV*	A-B3T3
718 B06	-4V	AA SERV*	A-B3T2
		CN SERV*	
		GND	B06 711
		CN SERV*	
		C06 718	A-B3T2

PAGE  
02  
OF  
02

DY001  
000

000 DY001

LOC. TYPE

BIAS AND SERVICE Voltages FOR A-B3 AND A-B4 BOARDS	
E.C.-HISTORY - E MACH-3705	
FRAME	01
DATE LAST EC	DY001
IBA CORP. SCD	DY001
P.N. 1986963	000

10-14-80 344270

## SOLID LOGIC DESIGN AUTOMATION—FSOCKET LISTING

PAGE 01

A1	CONNECTOR D11 AP005PA6 D13 CQ001CD2 E11 CQ005CJ6	B6	R04 DN004CH2 B02 DN004GK2 B04 DP991GB6 C02 DP991GE6 C04 DP992GA6 D02 DQ004GB2 E04 DQ004GD2	E1	CONNECTOR A11 AJ001AB4 B13 AJ001FC4 C11 AJ001FD4 C13 AJ001EC4 D11 CG001BF2 D13 CQ005DK6 E11 CS004GH2	C5007 2Q 2S 2T C5008 2R 2S 2T C5009 2V C5007 2V C5004 2U C5002 2X C5007 2Y 2Z 30 31 32 33 C5003 37 C5006 39 39 C5007 3A C5006 3C C5004 3D 3E 3F C5014 3H C5003 3J C5004 3K 3L 3M 3N 3P C5007 40 41 C5007 42	G6	Q04 DRJ04GK6 H1	CONNECTOR A11 DK976EJ6 A13 CQ004FJ6 B11 CQ004BK6 C13 CSC04BM2 D11 CQ004CA6 D13 CS004DB2 E11 CS004ED2 E13 CS004FG2	CA001 2J CA002 2X CA004 2L 2M 2N 2P 2Q 2R CA001 2U 2V CA004 2U 2X 2Y 2Z 30 31 CA001 34 CA002 35 CA001 36 37 CA002 38 CA001 39 CA002 3A CA003 3B CA004 3C CA003 40 45 CA004 47 48 49 4A 4B 4C CA004 4F CA001 4G CA004 4H CA001 4K 4L CA004 4P 4Q	CP007 OL OM GN OP OQ OR O3 OT OU OV CU005 O4 CP007 OX CU005 OY 02 10 11 CU006 12 13 14 15 16 17 18 19 18 1C 1D 1E 1F 1G CU009 1H 1J CU010 1K 1L CU014 1M CU010 1N 1P 1Q 1R 1S 1T CU014 1U 1V 1W CU006 1X 1Y 1Z 20 CU014 21 CU006 22 CU009 23 CU014 24 25 26 CU009 27 CU014 28 29 2A 2B 2C 2D CK007 2E CU014 2F 2G 2H CU010 2P 2Q 2R 2S 2T 2U 2V 2U 2X 2Y 2Z 30 31 32 33 34 35 36 37 38 39 3A 3B 3C 30 3E 3J 3K 3V 3W 3X 40 41 42											
A2	CONNECTOR B02 CX010FR6 B04 CX010GB6 B05 CX010DB4 B06 CX010ED4 B08 CX010DE4 B09 CX010EF4 B10 CX010BH4 B12 CX010FG4 B13 CX010FK4 B02 CX010GL4 D03 CX010FN4 D05 CX011GN6 D06 CX001GJ6 D07 CCC007HK2 D09 CC007HJ0 D10 CX002GF6 D11 CX002GH6 D13 CX002GK6	C1	CONNECTOR A11 CP005AB4 A13 AA002DN2 B11 AA002DN4 E13 D002CF4 D13 CQ004DM2 E11 CQ004FH2 E13 CQ005FL6	E2	QUAD CARD SCAN 8218939 6809	CX001 00 01 02 03 04 05 06 07 08 CX001 09 0B 0C 0D 0E OF 06 09 0J 0K 0L 0M 0N 0P 0Q 0S 0T 0U 0V 0W 0X CX003 0Y 0Z 10 11 12 13 14 15 16 17 18 19 1A 1B 1C 1D 1E 1F 1G 1H 1J 1K 1L 1M 1N 1P CX006 1W 1X 1Y 1Z 20 21 CX001 22 CX006 23 24 25 26 27 28 29 2B CX009 2B 2J 2K 2L 2M 2N 2P 2Q 2R 2S 2T 2U 2V 2W 2X 2Y 2Z 30 31 32 33 34 35 36 37 38 39 3A 3B 3C 38 3C 3F 3G 3H 3J 3K 3E 3M 3P 3Q 3R 3Y 3Z 3U 3V 3W 40 4E 4F CX010 30 51 53 CX006 4G 4H 4J CX001 50 51 53 CX005 54 CX006 5A CX001 5C 5D 5E 5F 5G CX009 5H	F6	CONNECTOR A04 DK976EJ6 B02 DK974EB6 B04 DK974EH6 C02 DL004GB6 C04 DL004GF6 D02 DL004GK6 E04 DR004GB6	H2	QUAD CARD 8250103 AB91	CD001 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 0G 0H 0J 0K 0L	CD002 0M CD001 0N CD002 0P 0Q 0R 0S 0T 0U 0V CD001 0W CD002 0X 0Y 0Z 10 CD001 11 CD002 12 13 14 CD003 15 16 17 18 19 1A 1B 1C 1D 1E 1F 1G 1H 1J 1K 1L 1M 1N 1P 1Q 1R 1S 1T 1U 1V 1W 1X 1Y 1Z 20 21 22 CD004 23 24 25 26 27 28 29 2A 2B 2C 2D 2E 2F 2G 2H 2J	K6	CONNECTOR A02 CA004EG6 B04 CA004EH6 C02 CA004EJ6 C04 CA004EK6 D02 CA004EM2 D04 CA004EM6 E02 CA004BD2	CP006 44 CU010 45 46 47 CU005 50 51 CS007 52 CU010 53 54 55 CK007 56 CP007 57 58 CU010 59 CP006 5B CP007 5E CK007 5F							
A3	CONNECTOR B02 CX003FD4 B04 CX003FE4 B05 CX003FE4 B06 CX003FE4 B08 RA004DJ2 B09 RA004DJ4 B10 RA004DJ6 B12 RA004DK1 B13 RA004DK3 B02 RA004DK5 D03 RA004DK7 D05 RA004DL2 D07 RA004DL6 D09 RA004DM1 D10 RA004DM3 D11 CX002GM6 D13 CX002GD6	C4	DOUBLE CARD 8254627 CF99	CU017 00 CU016 01 CU017 02 CU016 03 CU017 04 CU016 05 06 CU017 07 08 09 0A 0B 0E CU016 0F 0G 0H 0J 0K 0L CU017 0R CU016 0S 0T 0U 0V 0W CU016 10 11 12 13 14 CU017 15 CU016 16 17 CU017 18 19 1A 1B 1C 1D	G1	CONNECTOR A13 CQ001GL2	G2	QUAD CARD 8233246 Y702	G3	G4	G5	J6	CONNECTOR A02 CA004EG6 B04 CA004EH6 C02 CA004EJ6 C04 CA004EK6 D02 CA004EM2 D04 CA004EM6 E02 CA004BD2	CP006 44 CU010 45 46 47 CU005 50 51 CS007 52 CU010 53 54 55 CK007 56 CP007 57 58 CU010 59 CP006 5B CP007 5E CK007 5F								
A4	CONNECTOR B02 AP007EC4 B04 AP007EL4 B05 AP007EH4 B06 AP007EM4 B08 AP007EK4 B09 AP007EN4 B10 AP007EJ4 B12 AP007FM4 B13 AP002DL6 D02 AP002DM1 D03 AP002DM3 D05 AP002DM5 D06 AP002DL2 D07 AP002DL4 D09 AP002DN7 D10 AP002DN2 D11 AP002DN4 D13 AP002DN6	C6	CONNECTOR A02 DQ004CF2 B04 DQ004GK2 B02 DQ004GK2 B04 DP991GB6 C02 DR991GE6 D04 DR992GA6 E04 DC974PK6	E6	CONNECTOR E11 C5002BK4 E13 CP003DD6	D1	CONNECTOR E11 C5002BK4 E13 CP003DD6	E6	CONNECTOR A02 DK974EH6 B04 DH014G86 C02 DH014GF6 C04 DH014GK6 D04 DJ014GK6 E02 DJ014GK6	CX008 00 01 02 03 04 05 06 07 08 CX009 0A 0B 0C 0D 0E 0F 0G 0H 0J 0K CX009 0L CX002 0M 0N 0P 0Q 0R 0S 0T 0U CX009 0V CX002 0W 0X 0Y 0Z 10 11 12 13 14 15 16 17 18 CX003 19 1A 1B 1C 1D 1E 1F 1G 1H 1J 1K 1L 1M 1P 1Q 1R 1S 1T 1U 1V 1W 1X 1Y 20 21 22 23 F1	CQ001 03 04 05 CQ005 07 08 09 CQ005 0A CQ005 10 CQ005 11 CQ005 12 CQ005 13 14 15 CQ003 17 18 19 1A 1B 1C 1E CZ001 1F 1G 1H 1J 1K 1L 1N 1P 1Q 1R 1S 1T 1U 1V 1W 1X 1Y 20 21 22 23 CZ005 24 CZ002 25 CZ003 26	CD001 00 CD003 01 CD003 2L 2M 2N CD003 2Q CD003 2R CD003 2S 2T CD003 2V CD003 2W 2X CD004 2X 2Z 30 31 32 CD005 31 CD001 32 33 34 CD005 3K 3L 3M CD005 3Q 3R 3S 3D 3E CD002 27 28 29 2A 2B 2C 2D 2E 2F 2G 2H 2J 2K 2L 2M 2N 2P 2Q 2R 2U 2V CZ005 2U CZ003 2X 2Y 2Z 30 31 32 CZ004 33 34 35 36 37 38 CZ004 39 3A 3B 3C 3D 3E CZ004 3F 3G CZ002 3H 3I 3J 3K 3L 3M CZ003 3M 3N 3O 3P 3Q 3R CZ004 40 41 42 43 44 45 CZ004 46 CZ004 47 CZ004 48 49 CZ004 50 51 CZ005 52 CZ005 53 CZ005 54 CZ005 55 CZ001 56 CZ004 57 58 CZ004 59 CZ005 5A 5B CZ005 5C 5D CZ005 5E CZ005 5F CZ005 5G CZ005 5H CZ005 5I CZ005 5J CZ005 5K CZ005 5L CZ005 5M CZ005 5N CZ005 5O CZ005 5P CZ005 5Q CZ005 5R CZ005 5S CZ005 5T CZ005 5U CZ005 5V CZ005 5W CZ005 5X CZ005 5Y CZ005 5Z J1	CL001 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 0G 0H 0J 0K 0L CL002 0M 0N 0P 0Q CL002 0R 0S 0T CL003 1D 1E 1F 1G 1H 1J 1K 1L 1M 1N 1P 1Q 1R 1S 1T 1U 1V CL004 1W 1X 20 21 22 23 24 25 27 28 29 2J 2K 2L 2M 2N 2P 2Z CL005 20 25 2U 2V 2W 2X 22 30 34 35 36 37 CQ001 39 40 CQ003 44 45 46 D002 48 4B 4C E02 4C 4E 4F E04 4G 4H 4J CQ005 4K 4L CQ005 4M 4N	L6	CONNECTOR D02 C5001DM2 D04 CS007CH2 E02 CS007SD4	M1	CONNECTOR A13 CK001GD2 B11 CK002DA2 B13 CK002DB2 C11 CK002DC2 C13 CK002DD2 D11 CK002DH2 E13 CK002DJ2	M2	QUAD CARD 5857418 6818	M3	M4	M5
A5	CONNECTOR B02 CU006GC6 B04 CU010PJ2 B05 CU010GK6 B06 CU002CA6 B08 CC007HK3 B09 CC007HJ0 B10 CP001GK2 B12 CP001GM2 B13 CQ002DF6 D02 CQ002BK6 D03 CQ003GC6 D05 CQ002DD6 D06 CQ002BC6 D07 CL010AJ6 D09 CP001GG2 D10 RA003DM1 D11 RA003DM3 D13 RA003DP3	F2	QUAD CARD 8235942 6810	F3	QUAD CARD 8218933 6808	F4	QUAD CARD 8218933 6808	F5	QUAD CARD 8218933 6808	CS002 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 0G 0H 0J 0K 0L CS003 0K 0J 0L 0R 0N 0P 0Q 0R 0S 0T 0U 0W 0X 0Y 0Z 10												

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CU007 05		CU003 06		R5		CC001 00		CC005 00		CR005 43		RP003 B1		U2		RP003 B1		U4		B09		CU017FC6		PLUG LIST																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																								
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A1	CONNECTOR D11 DN004GB2 D13 DN004GD2 E11 DN004GF2	CG001 OF OG OL CG005 OM ON OP OQ OR OS CG001 OT CG005 OU OV CP001 23 24	F1	B13 DK974EH6 C11 DL004GB6 C13 DL004GF6 D11 DL004GX6 E13 DR004GB6	DN002 2W DP992 2X 2Y DN004 2Z DP992 30 31 32 33 35 DP993 37 DP994 38 3C DP991 3D DP992 3E 3F 3G 3H DP993 3J DN001 3K 3L DP992 3M 3N 50 51 DP993 53	H6	A02 DK976EJ6 A04 CQ004FJ6 B02 CS004BK6 C04 CS004BM2 D02 CS004CA6 D04 CS004DB2 E02 CS004ED2 E04 CS004FG2	J6	C02 CS005SC4 C04 CS005CA6 B02 CS005CC6 C04 CK003CC4 D02 DG974GG2	L2	QUAD CARD 5857402 6802																																																																													
A2	SINGLE CARD 8231695 6799	C6 CONNECTOR A02 CP005AB4 A04 AA002DN2 B02 AA002DN4 B04 AA002DN6 C02 DF002GF4 D04 CG004DM2 E02 CG004FH2 E04 CG005FL6	F2 SINGLE CARD 8211470 7593 ROS1 CW011 00 CW012 01 CW011 02 CW012 04 05 06 07 08 09 CW012 09 0B 0C 0D 0E 0F CW011 0N 0P 0Q 0R 0S 0T CW012 0U 0V CW011 0Y 0Z	G6 CONNECTOR A04 CQ001GL2 H1 CONNECTOR A11 CA003HE6 A13 CA003HH2 B11 CA003HL2 C13 CA004BA2 D11 CA004EB6 D13 CA004EC6 E11 CA004EE6 E13 CA004EF6	K1 CONNECTOR A13 CG001CK6 B11 DG977GK6 B13 DK977GK6	J1 CONNECTOR A11 CA004EG6 B13 CA004EH6 C13 CA004EK6 D11 CA004EM2 D13 CA004EM6 E11 CA004DD2	K2 QUAD CARD 5857401 6801	L3	DH014 00 01 02 DH002 03 04 05 DH014 06 DH016 07 08 DH014 09 0A DH011 0B OC OD OE OF OG DH002 0H OJ OK DH014 0L OM DH002 0N OP OQ OR DH003 0S OT DH011 0U DH016 0V DH011 0W OX DH003 0Y DH017 0Z 10 11 12 13 DH003 14 15 DH011 16 17 18 DH003 19 1A DH008 1B DH016 1C 1D DH011 1E 1F 1G DH017 1H 1J 1K 1L 1M DH015 1N 1P 1Q 1R DH014 1S DH008 1T 1U 1V 1W 1X 1Y DH014 1Z DH016 20 21 22 23 24 25 DH014 28 29 2A DH015 2B DH008 2C 2D 2E DH009 2F 2G 2H 2J 2K 2L DH015 2P 2Q 2R DH009 25 2T DH010 2U 2V 2W 2X DH015 2Y DH016 22 DH014 30 DH010 31 32 33 34 35 36 DH002 37 38 39 DH003 3A 3B 3C DH008 3D 3E 3F 3G DH011 3J 3K 3L DH009 3N 3O 3P 3Q 3R 3S 3T 3W DH016 3G 3H 3I 3L 3M 3N 3O 3P 3Q 3R 3S 3T 3U DH015 3X DH010 3Y 3Z 40 41 DH014 42 43 44 45 DH002 46 47 48 49 DH008 4E 4F 4G 4H DH010 4J 4K 4L 4M DH002 4X 4Y 4Z	DH014 00 01 02 DH002 03 04 05 DH014 06 DH016 07 08 DH014 09 0A DH011 0B OC OD OE OF OG DH002 0H OJ OK DH014 0L OM DH002 0N OP OQ OR DH003 0S OT DH011 0U DH016 0V DH011 0W OX DH003 0Y DH017 0Z 10 11 12 13 DH003 14 15 DH011 16 17 18 DH003 19 1A DH008 1B DH016 1C 1D DH011 1E 1F 1G DH017 1H 1J 1K 1L 1M DH015 1N 1P 1Q 1R DH014 1S DH008 1T 1U 1V 1W 1X 1Y DH014 1Z DH016 20 21 22 23 24 25 DH014 28 29 2A DH015 2B DH008 2C 2D 2E DH009 2F 2G 2H 2J 2K 2L DH015 2P 2Q 2R DH009 25 2T DH010 2U 2V 2W 2X DH015 2Y DH016 22 DH014 30 DH010 31 32 33 34 35 36 DH002 37 38 39 DH003 3A 3B 3C DH008 3D 3E 3F 3G DH011 3J 3K 3L DH009 3N 3O 3P 3Q 3R 3S 3T 3U DH016 3G 3H 3I 3L 3M 3N 3O 3P 3Q 3R 3S 3T 3U DH015 3X DH010 3Y 3Z 40 41 DH014 42 43 44 45 DH002 46 47 48 49 DH008 4E 4F 4G 4H DH010 4J 4K 4L 4M DH002 4X 4Y 4Z																																																																														
A3	SINGLE CARD 8231695 6799	D1 CONNECTOR E11 DG976EA6 E13 DG974EB6	D2 QUAD CARD 8218932 6798	F5 SINGLE CARD 5862885 N885 DW001 11 13 17 19 1B 1D 1E 1F 1G 1H 1J 1K	J1 CONNECTOR A11 CA004EG6 B13 CA004EH6 C13 CA004EK6 D11 CA004EM2 E11 CA004EM6 E11 CA004DD2	J2 QUAD CARD 5857401 6801	J3	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J4	DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J5	DG002 00 01 DF974 02 03 DF002 04 DF971 05 06 07 08 09 0A OB DF002 0C 0D 0E 0F DF974 0J 0K DF975 0L DF975 0M 0N 0P DF003 0Q DF008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J6	DG002 00 01 DF974 02 03 DF002 04 DF971 05 06 07 08 09 0A OB DF002 0C 0D 0E 0F DF974 0J 0K DF975 0M 0N 0P DF003 0Q DF008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J7	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J8	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J9	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J10	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J11	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J12	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J13	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J14	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J15	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J16	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J17	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J18	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J19	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J20	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J21	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J22	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J23	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J24	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J25	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J26	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J27	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J28	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J29	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J30	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J31	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J32	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J33	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J34	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J35	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J36	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J37	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J38	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J39	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J40	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J41	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J42	DG002 00 01 DG974 02 03 DG002 04 DG971 05 06 07 08 09 0A OB DG002 0C 0D 0E 0F DG974 0J 0K DG975 0M 0N 0P DG003 0Q DG008 0R 0S 0T 0U 0V 0W OX 0Y 0Z	J43	DG002 00 01 DG974 02 03 DG002 04 DG97

SOCKET LISTING  
DATE 06-04-81 MACH. 3705  
  
LOG 288 BOARD 01A-B4  
PREV. ENGR. 10-14-80 344270  
PRES. ENGR. 06-02-81 344828  
P.N. 1986967  
  
IBM CORP. SDD BLK.

## SOLID LOGIC DESIGN AUTOMATION - SOCKET LISTING

PLUG LIST											
PART NO	RCC	TYPE	SOCKETS	TOTAL							
5857401	6801	J2 K2 N2	03								
5857402	6802	L2 M2 P2 Q2	04								
5857407	6807	R2	01								
5862885	N885	F5	01								
8211470	7593	F2	01								
8218932	6798	D2	01								
8231437	6797	R4 B2	02								
8231689	6845	U2 U3	02								
8231695	6799	R2 A3	02								
8250101	AB89	E2	01								
8250102	AB90	G2 H2	02								
8252018	AB93	C2	01								
CONN	A1	A5 A6 B1									
	B6	C1 C6 D1									
	D6	E1 E6 F1									
	F6	G1 G6 H1									
	H6	J1 J6 K1									
	K6	L1 L6 M1									
	M6	N1 N6 P1									
	P6	Q1 Q6 R1									
	R6	S1 S6 T1									
	T2	T5 U6 V1									
	U4	V2 V3 V4 V5									
	V6										
CF002	00	03 04 05									
CF001	06	07 08 09 0A 0B									
	0C	0D 0E 0F 0G 0H									
CF002	0J	0K 0L 0M 0N 0P									
	0Q	0R 0S 0T 0U 0V									
	0W	0X 0Y 0Z 10 11									
	12	13 14 15 16 17									
	18										
CF003	19	1A 1B 1C 1D 1E									
	1F	1G 1H 1I 1K 1L									
	1M	1N 1P 1Q 1R 1S									
	1T	1U 1V 1W 1X 1Y									
	20										
CF004	21	22 23 24 25 26									
	27	28 29 2A 2B 2C									
	2D	2E									
CF003	2F										
CF004	2G	2H									
DQ001	2J										
DQ002	2K	2L									
CF002	2M										
DQ002	2N	2P									
CF003	22										
CF004	33										
DQ001	34										
CF003	36										
CF004	38	40									
CF003	4A	4B 4C									
CF002	4D										
CF003	4E										
	4F	4G 4H 4J 4K 4L									
	4M	4N 4P									
DM004	00	01 02									
DM002	03	04 05									
DM004	06										
DM006	07	08									
DM004	09	0A									
DM001	0B	0C 0D 0E 0F 0G									
DM002	0H	0J 0K									
DL004	0L	0M									
DL002	0N	0P 0Q 0R									
DL003	0S	0T									
DL004	0U										
DL006	0V										
DL007	0W	0X									
DM007	0Z	10 11 12 13									
DM003	14	15									
DM001	16	17 18									
DM003	19	1A									
DM008	1B										
DM006	1C	1D									
DM001	1E	1F 1G									
DM007	1H	1J 1K 1L 1M									
DM005	1P	1Q 1R									
DM004	1S										
DM008	1T	1U 1V 1W 1X 1Y									
DM004	12										
DM006	20	21 22 23 24 25									
DM004	26	27									
DM004	28	29 2A									
DM005	2B										
DM008	2C	2D 2E									
DM009	2F	2G 2H 2J 2K 2L									
DM004	2M	2N									
DM005	2P	2Q 2R									
DM004	28	29 2A									
DM004	30										
DM004	31	32 33 34 35 36									
DM002	37	38 39									
DM003	3A	3B 3C									
DM003	3D										
DM008	3E	3F 3G									
DM009	3H										
DM004	3I										
DM008	3J										
DM004	3K										
DM008	3L										
DM003	3M										
DM008	3N										
DM008	3O										
DM008	3P										
DM008	3Q										
DM008	3R										
DM008	3S										
DM008	3T										
DM008	3U										
DM008	3V										
DM008	3W										

F2 DOUBLE CARD  
8252028 CE48 NROS

CW013  
CW014

SEE 

1	2	3	4
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PART NO. ACC TYPE SOCKET  
8252028 NROS CE48 F2

NOTES

- 1 WITH TWO CA-4'S, NROS IS INSTALLED IN OIA-B4F2
- 2 WITH A CA-1, TYPE I ROS IS INSTALLED IN OIA-B4F2, CARD P/N 8211470
- 3 WITH ONE CA-4, TYPE I ROS OR NROS MAY BE INSTALLED
- 4 WITH A STANDALONE REMOTE (NO. CA'S INSTALLED), NO ROS CARD IS PRESENT IN OIA-B4F2/F3

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SOCKET LISTING ADDENDUM

DATE JUN81 MACH: 3705 D  
P/N 4499514 BRD: OIA-B4 Z  
VERS: 000 O  
IBM CORP SCD O  
E.C. 344828 2  
A

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